

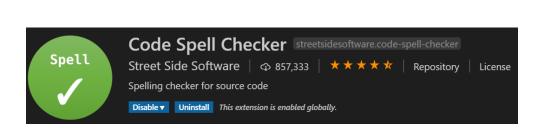
Ho to program STM32F7 using open source, cross-platform tools only?

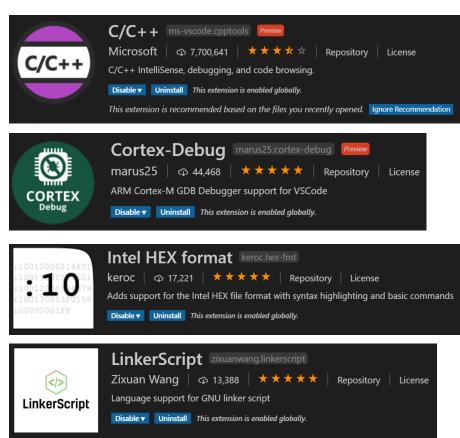
Demo with graphics using STM32F769 Discovery Kit by Tomasz Jastrzębski, November 2019 source code available at:

https://github.com/tdjastrzebski/DISCO-F769NI LCD demo

#### VS Code Installation

- Install VS Code <a href="https://code.visualstudio.com/">https://code.visualstudio.com/</a>
- Install VS Code Extensions:
  - C/C++ VS
  - Cortex-Debug
  - Intel HEX format
  - LinkerScript
  - Code Spell Checker





#### GNU Arm Embedded Toolchain Installation

- Download and install GNU Arm Embedded Toolchain installer, version 8-2019-q3-update <a href="https://developer.arm.com/tools-and-software/open-source-software/developer-tools/gnu-toolchain/gnu-rm/downloads">https://developer.arm.com/tools-and-software/open-source-software/developer-tools/gnu-toolchain/gnu-rm/downloads</a>
- 2. During the installation check "Add path to environment variable"

#### Alternatively:

- Download zip package, unpack it to selected folder, e.g.
   C:/Program Files (x86)/GNU Tools ARM Embedded/8 2019-q3-update
   this is the default location if you use installer, it contains white space but that's OK
- 2. Add **bin** folder to the **PATH** system environment variable Example: **C:/Program Files (x86)/GNU Tools ARM Embedded/8 2019-q3-update/bin**

## Sample Source Code

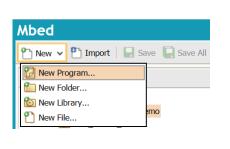
- Login to <a href="https://www.mbed.com/">https://www.mbed.com/</a>
  - you need to create an account first
- Go to Hardware/Boards website menu, find DISCO-F769NI board (STM32F769I-DISCO), select Add to your Mbed Compiler option in the right panel



• Go to Compiler -> arm MBED Overview ▼ Hardware ▼ Docs ▼ Code ▼ Support ▼ Case Studies ▼ Q Portal Compiler Q ▼

• Select New/New Program menu option.
In the config form select your board and

Basic example showing how to drive the LCD template



Create new program for "DISCO-F769NI"

This will create a new C++ program for "DISCO-F769NI" in your workspace. You can always change the platform of this program once created.

I Please specify program name

Platform:

DISCO-F769NI

Template:

DISCO-F769NI

Template:

DISCO-F769NI

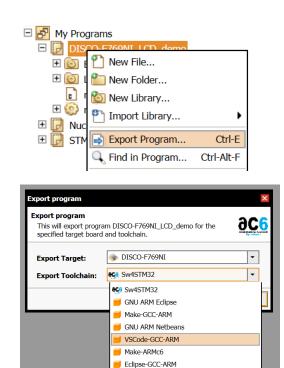
The name of the program to be created in your workspace

Update this program and libraries to latest revision

Right-click your new program, select Export
 Program option

Select VSCode-GCC-ARM export toolchain

Save generated ZIP file locally, unzip to preferred location



#### Git source control

1. Download and install **Git** https://git-scm.com/downloads

2. On Windows we need not only source control but some convenient Unix commands available in Bash shell installed with Git.

Namely: **find** and **rm** 

#### Git

 Add .gitignore file with the following content: /BUILD

- 2. From the terminal window issue commands:
  - git config --global user.email "your@email.here.com"
  - git init
  - git add .
  - git commit -m "initial version"

### GNU make

Obtain GNU Make build tool

https://www.gnu.org/software/make/

There many sources available.

#### Example:

- 1. Download **gnumake-4.2.1-x64.exe** or **gnumake-4.2.1.exe** from <a href="https://github.com/mbuilov/gnumake-windows">https://github.com/mbuilov/gnumake-windows</a>
- 2. Rename downloaded file to make.exe
- 3. Place it where it will be easily accessible, e.g. C:/Windows/System32

## modify c\_cpp\_properties.json

Leave only one configuration, name it "GCC"
 we do not need separate configurations for Mac, Linux and Windows since build is not OS specific but compiler specific

```
2. "compilerPath": "arm-none-eabi-g++"
3. "includePath": [
        "/usr/src/mbed-sdk/",
        "BSP_DISCO_F769NI/**",
        "LCD_DISCO_F769NI/**",
        "mbed/**"
]
```

# recreate tasks.json

Supplied **tasks.json** file uses old syntax version 0.1.0 Create new content:

# recreate tasks.json

#### Add **Build** task

```
"label": "Build",
"type": "shell",
"command": "make",
"args": [
    "-j8",
    "-output-sync=recurse",
    "all",
    "OPT=-00 -g"
"options": {
    "cwd": "${workspaceRoot}"
"presentation": {
    "clear": true
"problemMatcher": ["$gcc"],
"group": {
    "kind": "build",
    "isDefault": true
```

# recreate tasks.json

#### Add **Clean** task to **tasks.json** file:

```
"label": "Clean",
"type": "shell",
"command": "make",
"args": [
    "clean"
"options": {
    "cwd": "${workspaceRoot}"
},
"group": {
    "kind": "build",
    "isDefault": true
"problemMatcher": [],
"presentation": {
    "clear": true
```

# For debug build modify Makefile

• In line 35 before **VPATH** variable add **OPT** variable definition: # optimization params for RELEASE, change to [-00 -g] for DEBUG version OPT = -0s -g1

• In C\_FLAGS, CXX\_FLAGS and ASM\_FLAGS replace -Os and -g flags with OPT variable, e.g. change:

```
C_FLAGS += -0s
C_FLAGS += -g
to:
C_FLAGS += $(0PT)
```

Fix mbed\_config.h path in ASM\_FLAGS

### Run build

At this stage you should be able to build your project. Press Ctrl+Shift+B, select **Build** task.

After the build is finished, BUILD bolder should contain DISCO-F769NI\_LCD\_demo.bin output file.

### Run build

Note: on some systems you may get the following "unexpected EOF" error in Makefile around line **530** 

```
/usr/bin/sh: -c: line 0: unexpected EOF while looking for matching `"'
/usr/bin/sh: -c: line 1: syntax error: unexpected end of file
make[1]: *** [h:/DISCO-F769NI_LCD_demo/Makefile:530: DISCO-F769NI_LCD_demo.elf] Error 1
make: *** [Makefile:26: all] Error 2
The terminal process terminated with exit code: 2
```

While I was unable to determine the root cause, it seems the problem is related to shell command length limits.

On Windows you may try to use cmd.exe or PowerShell.

The problem does not occur if you use **Build++**. Continue reading.

## get stlink utility

https://github.com/texane/stlink/

- 1. Download **stlink** version 1.3.0 (Windows & Mac) <a href="https://github.com/texane/stlink/releases/tag/1.3.0">https://github.com/texane/stlink/releases/tag/1.3.0</a>
- 2. Windows:
  - 1. Extract content to **C:/Program Files/StLink**
  - 2. Add C:/Program Files/StLink/bin to the system PATH environment variable
  - Download libusb from <a href="https://libusb.info/">https://libusb.info/</a>
  - 4. Copy files from MS64/dll folder to C:/Program Files/StLink/bin
    - 7Zip utility needed from <a href="http://www.7-zip.org">http://www.7-zip.org</a>

Note: the latest version 1.5.1 and Linux executables are not available from author's Git repo – you have to build them yourself.

Executable **stlink** version 1.5.1 for Windows is available from my repo:

https://github.com/tdjastrzebski/DISCO-F769NI\_LCD\_demo

## Linux: build latest **stlink** utility

Download source code for the latest stable version as zip package from <a href="https://github.com/texane/stlink/releases">https://github.com/texane/stlink/releases</a>

Follow build steps

https://github.com/texane/stlink/blob/master/doc/compiling.md

## create **Deploy** task

Add **Deploy** task definition to **tasks.json** file:

```
"label": "Deploy",
  "type": "shell",
  "command": "st-flash",
  "args": [
        "write",
        "${workspaceRoot}/BUILD/${workspaceRootFolderName}.bin",
        "0x08000000"
],
  "group": {
        "kind": "build",
        "isDefault": true
},
  "problemMatcher": [],
  "presentation": {
        "clear": true
}
```

At this point you should be able to deploy your program – run **Deploy** task

## Debug

- Obtain STM32F7x9.svd file from <u>https://github.com/posborne/cmsis-svd/tree/master/data/STMicro</u> and place it in the project root folder
- 2. Replace **launch.json** file content with:

## Debug – cont'd

3. Add **Build & Deploy** task to **tasks.json** file:

```
{
    "label": "Build & Deploy",
    "dependsOn": ["Build", "Deploy"],
    "dependsOrder": "sequence",
    "group": {
        "kind": "build",
        "isDefault": true
    },
    "problemMatcher": []
}
```

Finally, set breakpoint within main.cpp file, press F5 to build, deploy and start

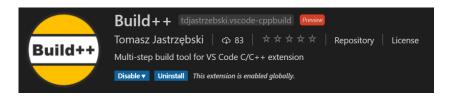
#### Build++

- 1. Install Node.js from <a href="https://nodejs.org/en/download/">https://nodejs.org/en/download/</a>
- 2. Install CppBuild: npm install cppbuild -g
- 3. Install **Build++** VS Code extension
- 4. Add **Build++** task to **tasks.json** file:

```
"label": "Build++",
"type": "shell",
"command": "cppbuild",
"args": [ "GCC", "debug", "-w" ],
"presentation": {
        "clear": true
},
"problemMatcher": ["$gcc"],
"group": {
        "kind": "build",
        "isDefault": true
}
```

#### 5. Modify **lunch.json** file:

```
"executable": "./BUILD/debug/${workspaceRootFolderName}.elf",
"preLaunchTask": "Build++"
```



#### 6. Add c\_cpp\_build.json file to .vscode folder

```
"version": 1,
         "params": { "buildDir": "BUILD" },
                                                                                                                                                                                  Do NOT just copy-paste this code. Very likely some characters (-) will be missing.
         "configurations": [
                                                                                                                                                                                  Get this code from: https://github.com/tdjastrzebski/DISCO-F769NI LCD demo
                            "name": "GCC",
                            "problemMatchers": ["$gcc"],
                            "buildTypes": [
                                              "name": "debug",
                                               "params": { "buildTypeParams": "-00 -g" }
                                               "name": "release",
                                              "params": { "buildTypeParams": "-0s -g1" }
                            "params": { "buildOutput": "${buildDir}/${buildTypeName}" },
                            "buildSteps": [
                                              "name": "C Compile",
                                              "filePattern": "**/*.c".
                                              "outputFile": "${buildOutput}/${fileDirectory}/${fileName}.o"
                                              "command": "arm-none-eabi-gcc -std=gnu11 -c ${buildTypeParams} -Wall -Wextra -Wno-unused-parameter -Wno-missing-field-initializers -fmessage-length=0 -fno-exceptions -ffunction-sections -fdata-sections -funsigned-char -MMD -fno-delete-null-pointer-checks -
fomit-frame-pointer -mcpu=cortex-m7 -mthumb -mfpu=fpv5-d16 -mfloat-abi=softfp (-I[$${includePath}]) (-D$${defines}) (-include [$${forcedInclude}]) [${filePath}] -o [${outputFile}]"
                                              "name": "C++ Compile",
                                              "filePattern": "**/*.cpp",
                                              "outputFile": "${buildOutput}/${fileDirectory}/${fileName}.o",
                                              "command": "arm-none-eabi_g++ -std=gnu++14 -C ${buildTypeParams} -fno-rtti -Wvla -Wall -Wextra -Wno-unused-parameter -Wno-missing-field-initializers -fmessage-length=0 -fno-exceptions -ffunction-sections -fdata-sections -funsigned-char -MMD -fno-delete-
null-pointer-checks -fomit-frame-pointer -mcpu=cortex-m7 -mthumb -mfpu=fpv5-d16 -mfloat-abi=softfp (-I[$${includePath}]) (-D$${defines}) (-include [$${forcedInclude}]) [${filePath}] -o [${outputFile}]'
                                              "name": "Build link script".
                                              "command": "arm-none-eabi-cpp -E -P -Wl,--gc-sections -Wl,--wrap,_main -Wl,--wrap,_malloc_r -Wl,--wrap,_free_r -Wl,--wrap,_memalign_r -Wl,--wrap,_calloc_r -Wl,--wrap,_exit -Wl,--wrap,atexit -Wl,-- -wrap,atexit -Wl,-- -wrap, -memalign_r -Wl,--wrap,_memalign_r -Wl,--wrap,_calloc_r -Wl,--wrap,atexit -Wl,--wrap,atexit -Wl,-- -wrap,-memalign_r -Wl,--wrap,-memalign_r -Wl,--wrap,-memalign_r -Wl,--wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wrap,-wra
DMBED BOOT STACK SIZE=4096 [mbed/TARGET DISCO F769NI/TOOLCHAIN GCC ARM/STM32F769xI.ld] -o [${buildOutput}/${workspaceRootFolderName}.link script.ld]"
                                               "name": "Build object list 1".
                                              "command": "find [${buildOutput}] -type f -name '*.o' > [${buildOutput}/object_list.txt]"
                                              "name": "Build object list 2",
                                              "command": "find mbed -type f -name '*.o' >> [${buildOutput}/object list.txt]"
                                              "command": "arm-none-eabi-gcc -Wl,--gc-sections -Wl,--wrap, main -Wl,--wrap, malloc r -Wl,--wrap, free r -Wl,--wrap, realloc r -Wl,--wrap, calloc r -Wl,--wrap,exit -Wl,--wrap,atexit -Wl,-- mcpu=cortex-m7 -mthumb -mfpu=fpv5-d16 -
mfloat-abi=softfp -DMBED_ROM_START=0x8000000 -DMBED_ROM_STZE=0x200000 -DMBED_ROM_STZE=0x2000000 -DMBED_ROM_STZE=0x200000 -DMBED_ROM_STZE=0x2000000
T [${buildOutput}/${workspaceRootFolderName}.link script.ld] -L[mbed/TARGET DISCO F769NI/TOOLCHAIN GCC ARM] -o [${buildOutput}/$fworkspaceRootFolderName}.elf] @[${buildOutput}/$fworkspaceRootFolderName}.link script.ld] -L[mbed/TARGET DISCO F769NI/TOOLCHAIN GCC ARM] -o [${buildOutput}/$fworkspaceRootFolderName}.elf] @[${buildOutput}/$fworkspaceRootFolderName}.link script.ld] -L[mbed/TARGET DISCO F769NI/TOOLCHAIN GCC ARM] -o [${buildOutput}/$fworkspaceRootFolderName}.elf] @[${buildOutput}/$fworkspaceRootFolderName}.link script.ld] -L[mbed/TARGET DISCO F769NI/TOOLCHAIN GCC ARM] -o [${buildOutput}/$fworkspaceRootFolderName}.elf] @[${buildOutput}/$fworkspaceRootFolderName}.elf] @[${buildOutput}/$fworkspaceRootFolderNam
group"
                                              "command": "arm-none-eabi-objcopy -O binary [${buildOutput}/${workspaceRootFolderName}.elf] [$${buildOutput}/${workspaceRootFolderName}.bin]"
                                              "command": "arm-none-eabi-objcopy -0 ihex [${buildOutput}/${workspaceRootFolderName}.elf] [${buildOutput}/${workspaceRootFolderName}.hex]"
                                              "command": "st-flash write [${buildOutput}/${workspaceRootFolderName}.bin] 0x08000000'
```

### Build++ cont'd

Press **F5**. Your project should build, deploy and start in debug mode.

Happy coding ©

Tomasz Jastrzębski

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