

Subject:

Year:

Month:

Date:

NOTEBOOK

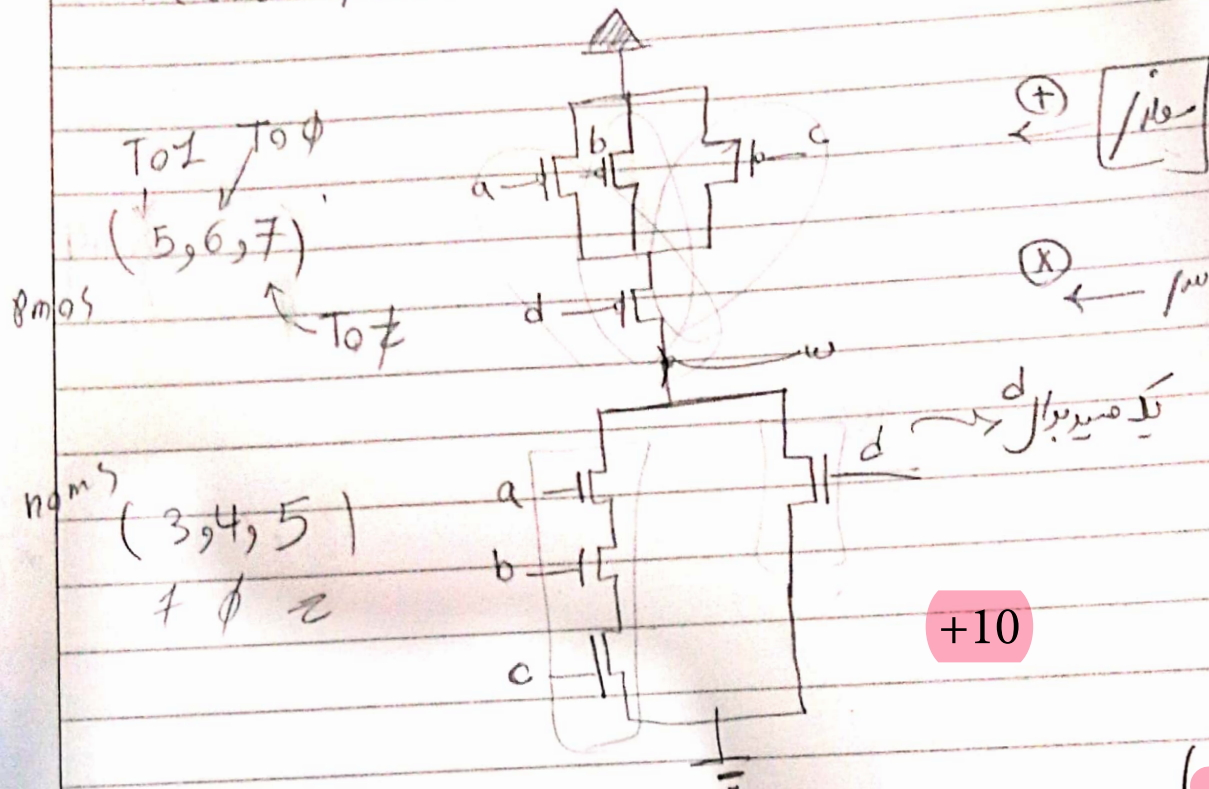
بسم تعالیٰ - سعید علی خیلو - ۹۹۴۳/۱۸

(الف)

-5 for use extra NOT gates

-5 for didn't put NOT for inputs

$$f(a+b+c) d$$



+10

PMOS $T_{01} \rightarrow (10, 9) = 10$

-10

$T_{0\phi} \rightarrow (14, 4) = 14$

-10

nmos

Sabalan