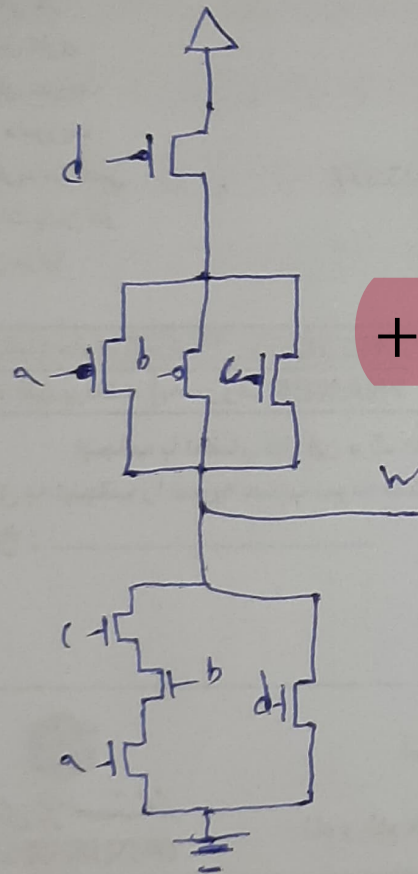


$$F = (a + b + c) d$$

nmos # (f, a, b, c)

pmos # (c, v, a)



$$\left. \begin{array}{l} t_{00} \rightarrow \text{شبهه } n \text{ منفرد: بدترین تأخیر} \\ d = 1 \rightarrow 3 \times 4 = 12 \\ z, p \text{ شبهه} \Rightarrow 2 \times 4 = 8 \end{array} \right\} \text{max} = 12$$

$$t_{00} = 12$$

+10

$$\left. \begin{array}{l} t_{01} \rightarrow \text{بدترین تأخیر: یک شبهه } p \\ c = 1 \rightarrow 2 \times 4 = 8 \\ z, n \text{ شبهه} \rightarrow 3 \times 4 = 12 \end{array} \right\} \text{max} = 12$$

$$t_{01} = 12$$

-5 for inverted inputs

-5 for didn't put NOT gate on inputs