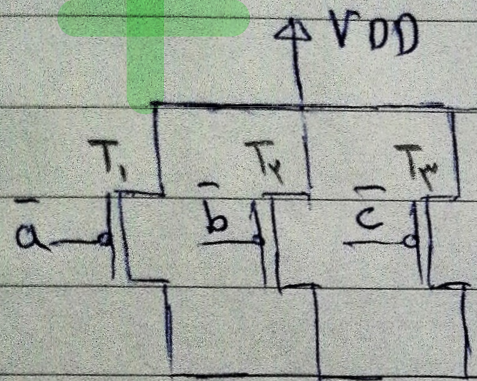


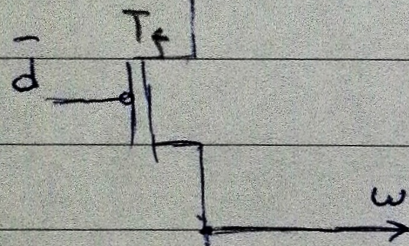
$$Y = f_z(a+b+c).d$$

nMOS # (4, 2, 4)

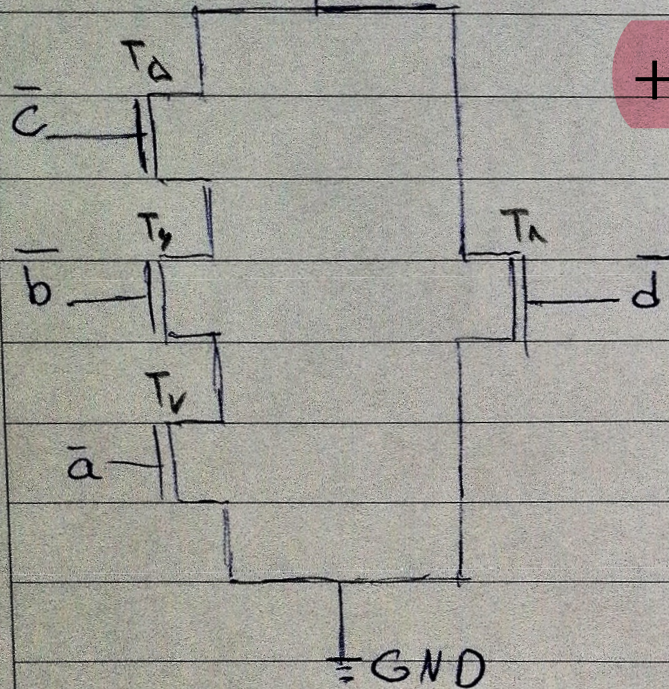
pMOS # (4, 1, 1)



$T_{01}: (T_1, T_2) = 4 + 4 = 8$  (12) بدون در نظر گرفتن  $N_{ot}$   
 $(T_4) \rightarrow Z = 4$  -10



$T_{00}: (T_4) = 2$   
 $(T_1, T_2) \rightarrow Z = 1$  (1) بدون در نظر گرفتن  $N_{ot}$  -10



+15

-5 for inverted inputs