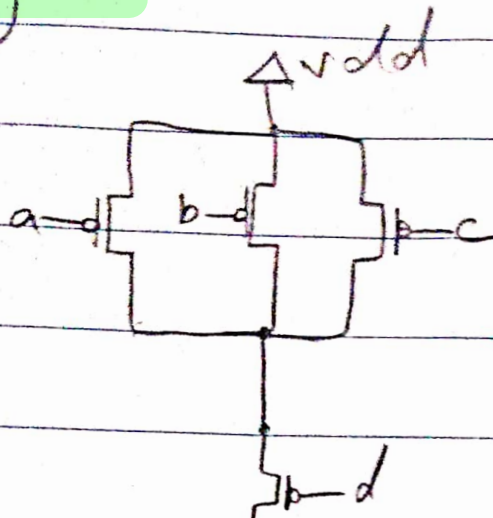


$$f = abc + d$$

$\left\{ \begin{array}{l} \text{NMOS } \#(r, \varepsilon, \delta) \\ \text{PMOS } \#(0, 4, v) \end{array} \right.$

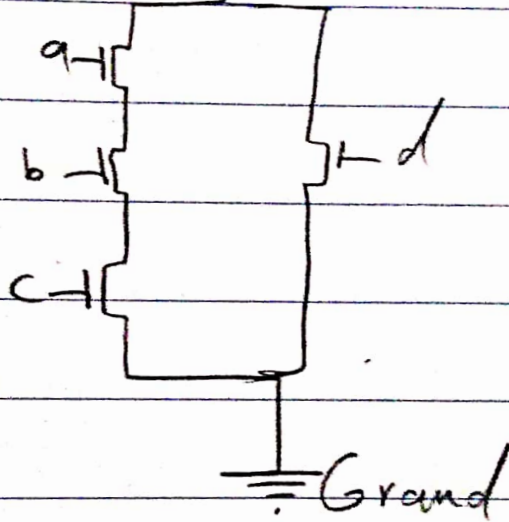
الف)



$\left\{ \begin{array}{l} \text{NMOS } \#(r, \varepsilon, \delta) \\ \text{PMOS } \#(0, 4, v) \end{array} \right.$

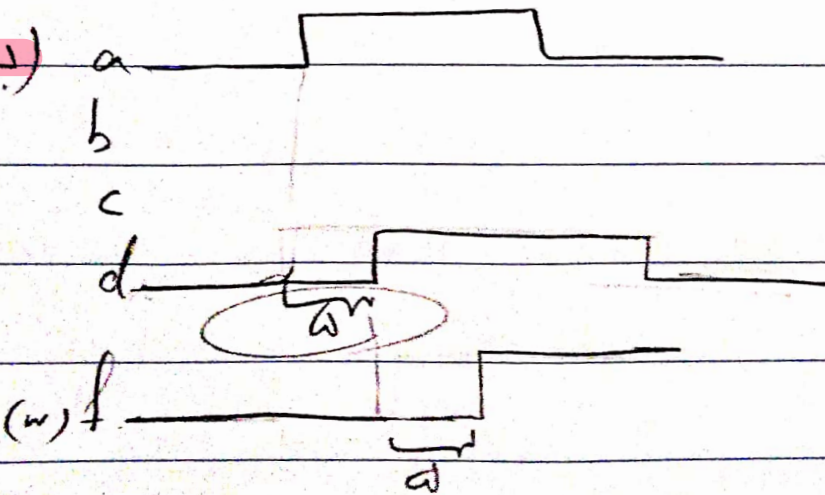
-5 for did not implement not gate

+15



$\left\{ \begin{array}{l} \text{NMOS } \#(r, \varepsilon, \delta) \\ \text{PMOS } \#(0, 4, v) \end{array} \right.$

ب)



to 1 → 10

(PMOS)

to 0 →

-10

-10

9V89M1.2. 625021N6