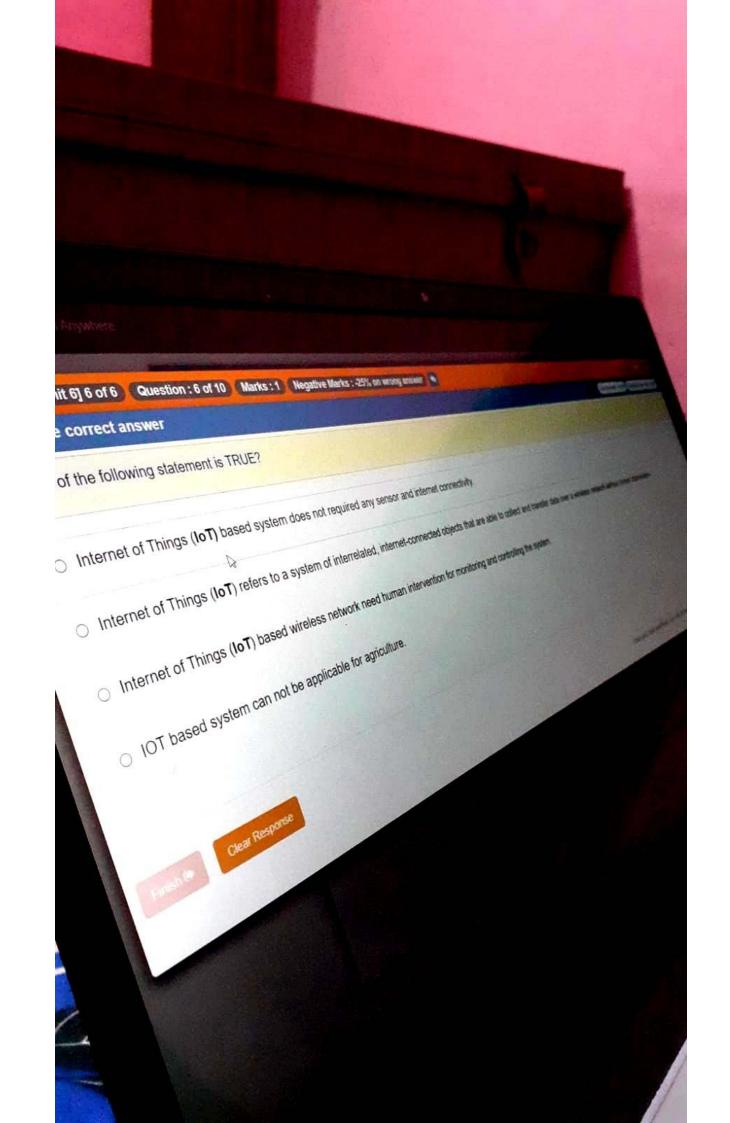
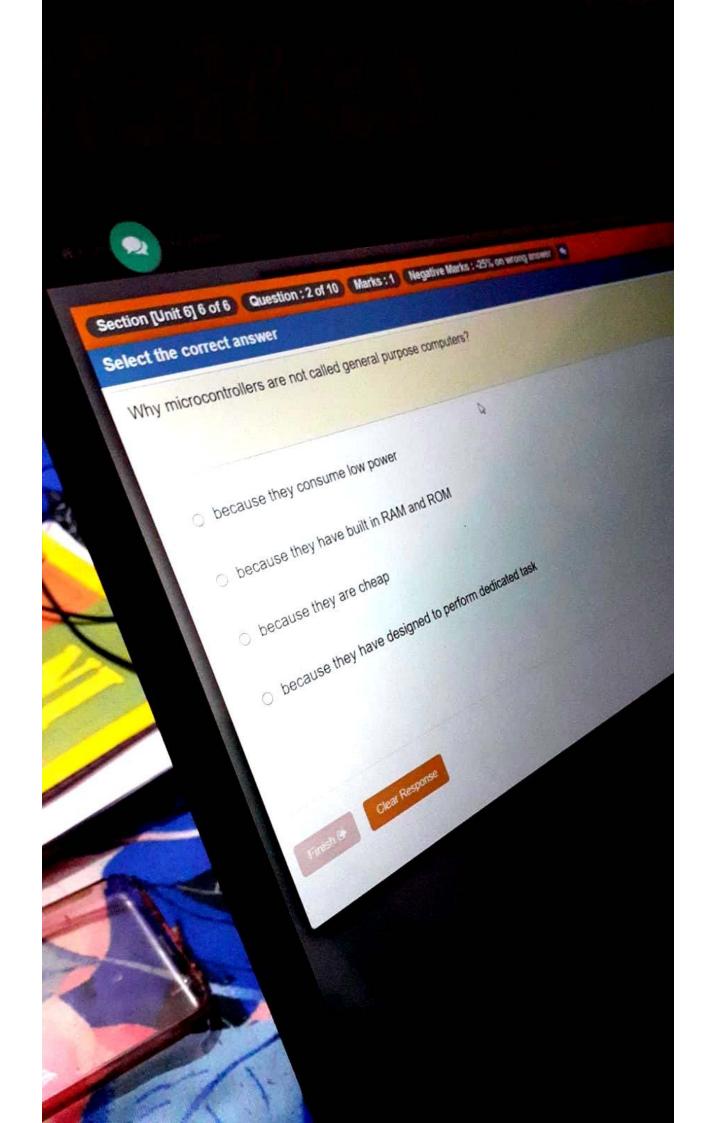
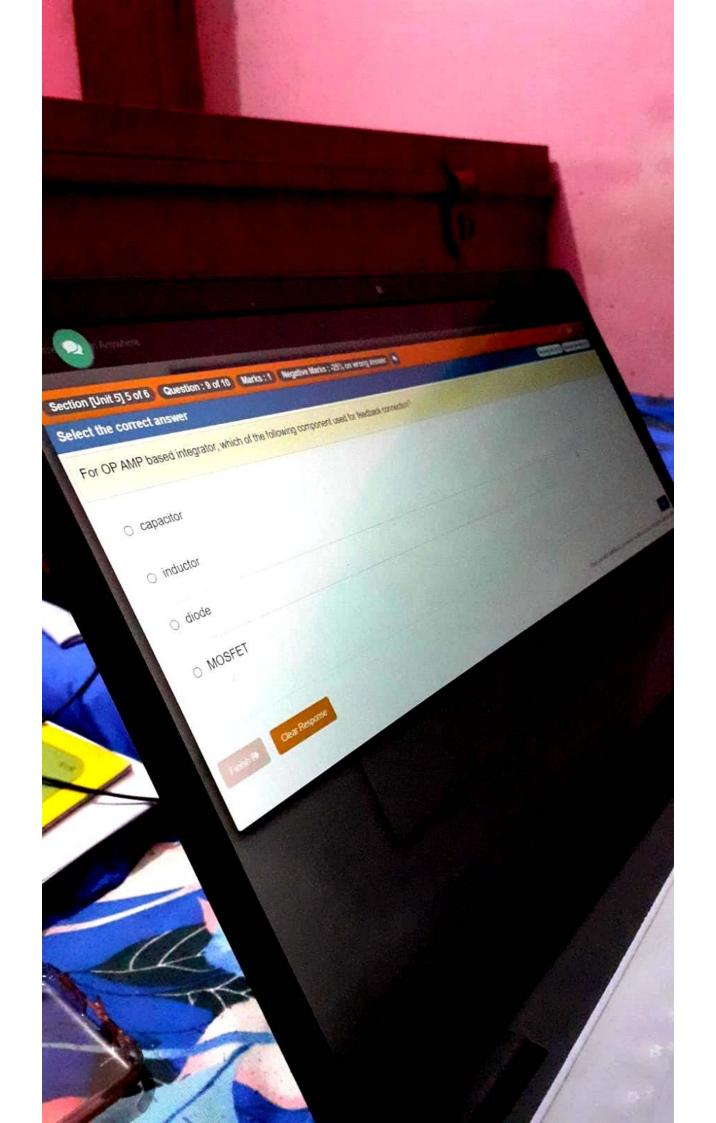
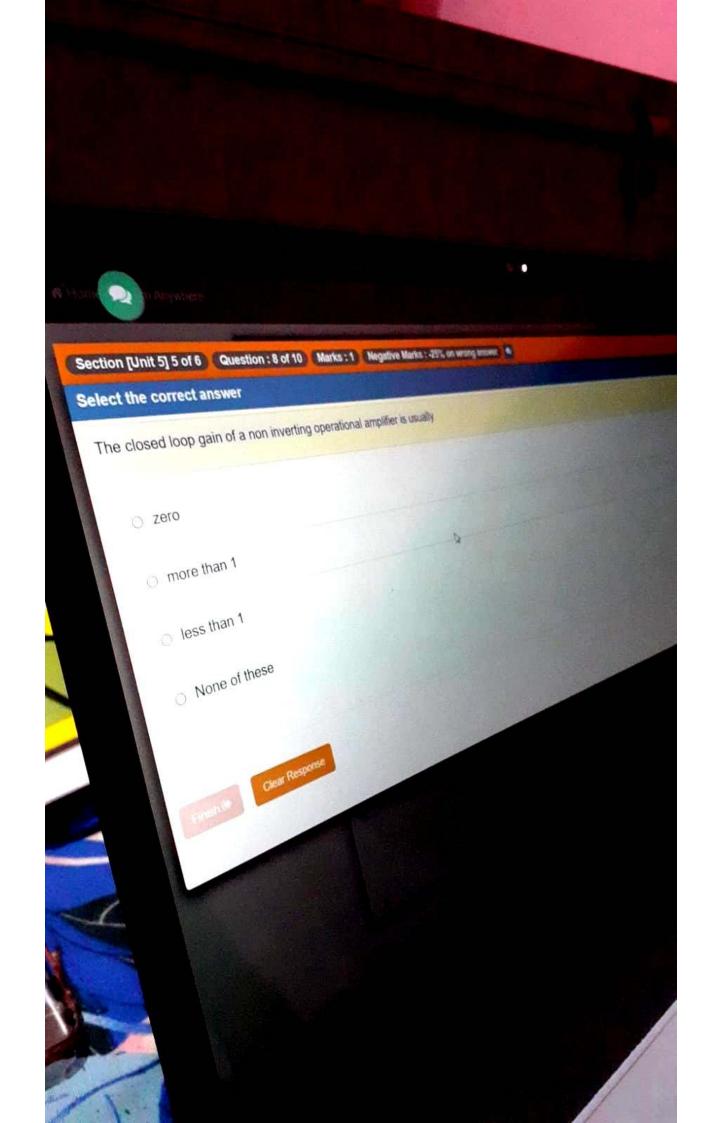


on [Unit 6] 6 of 6 Question: 4 of 10 Navis; 1 Regular Barbs: 278.00 Margarithm Which of the following statement is true for a microprocessor lect the correct answer Microprocessor does not consists any Central Processing Date Microprocessor consists of only a Central Progressy Link All input and output devices are internally corrected are thorascenses RAM is connected internally with Mccopageser

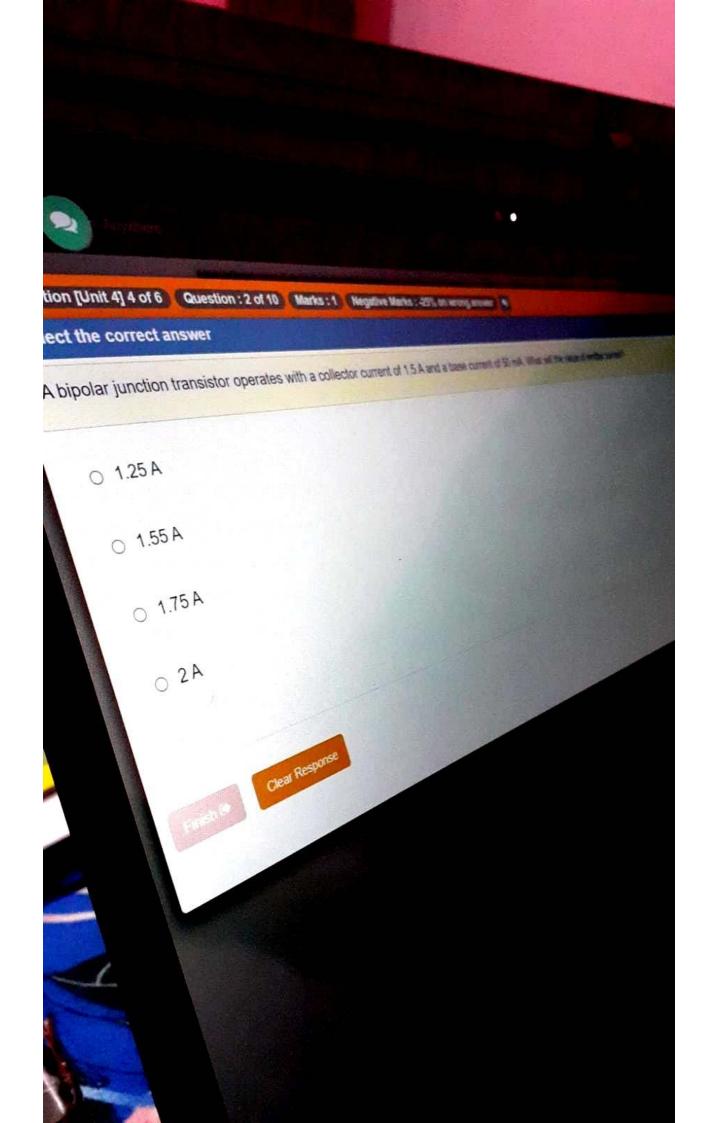


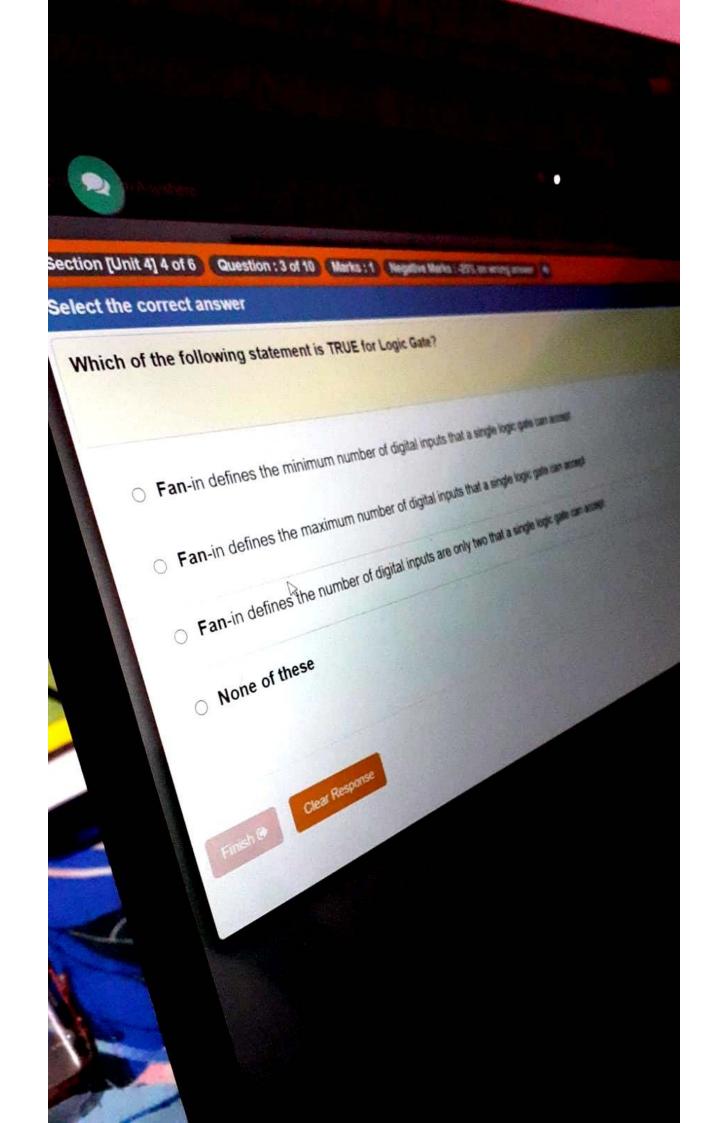


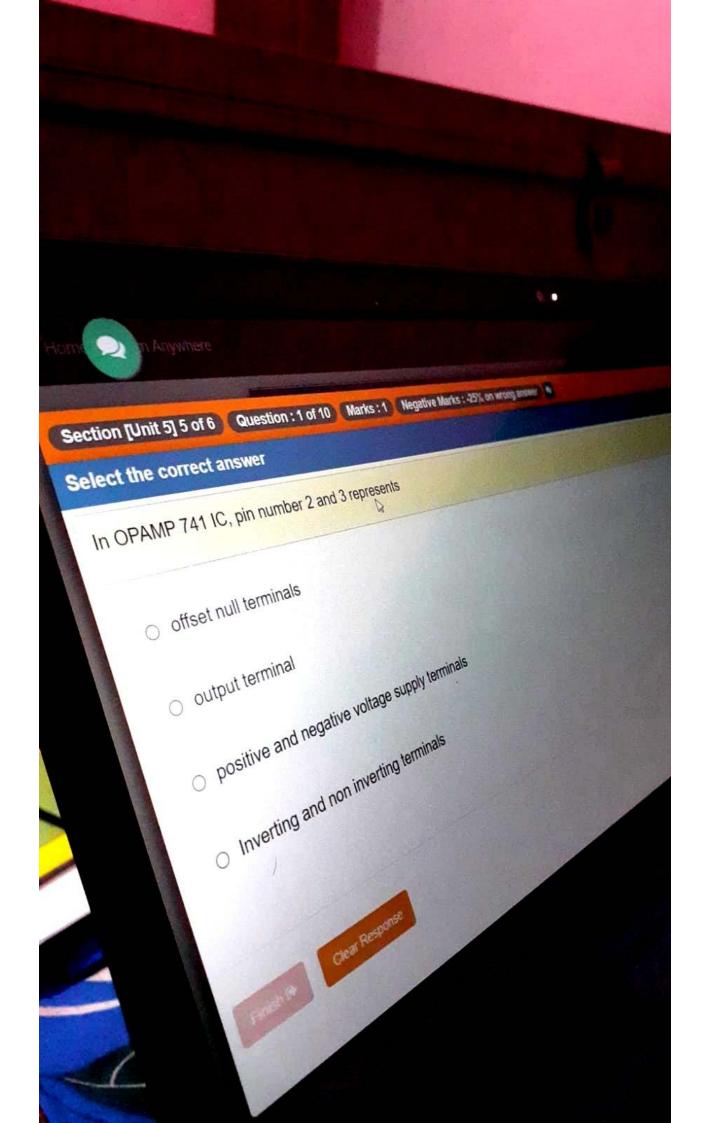


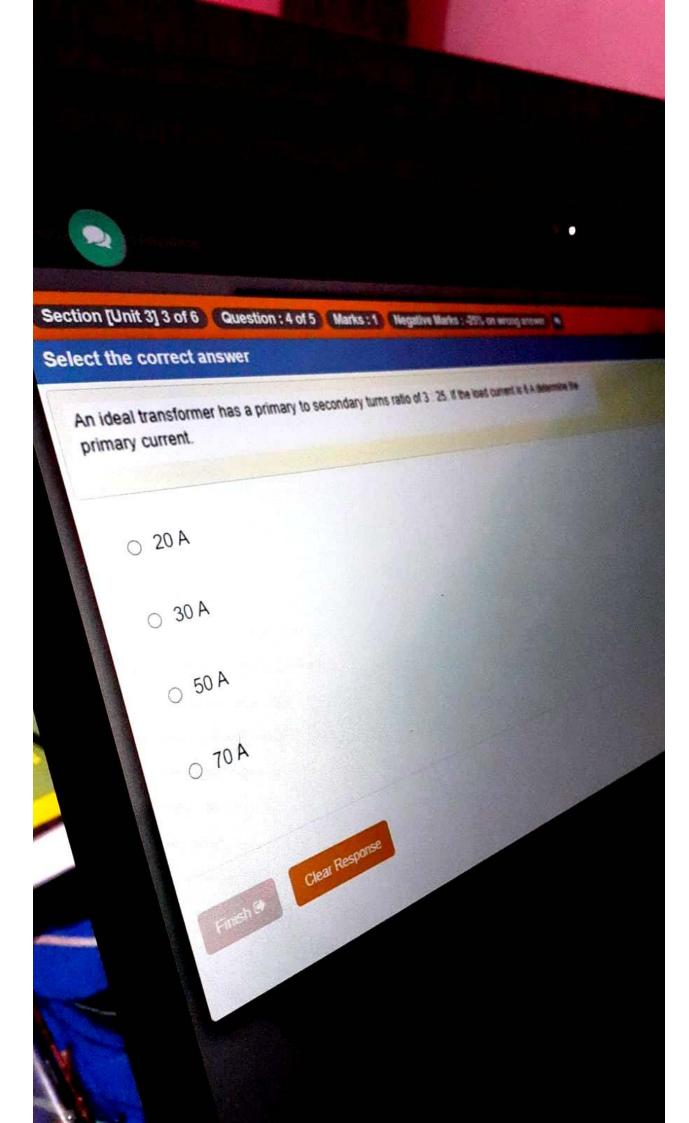


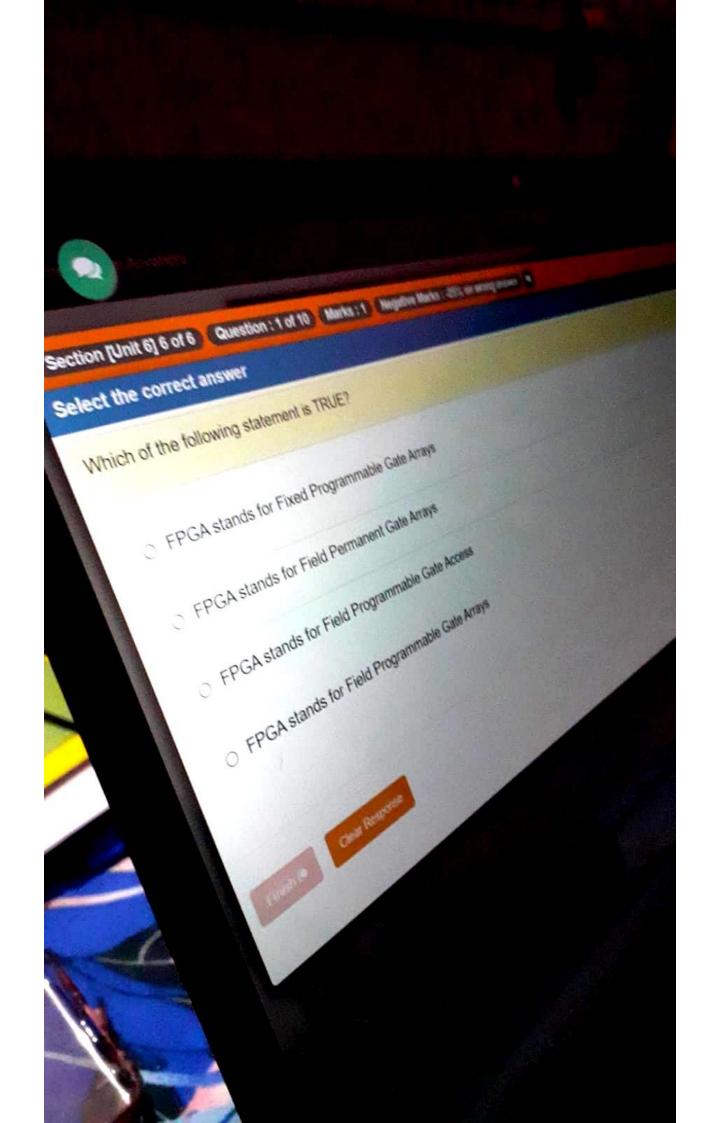
Section [Unit 4] 4 of 6 Question: 6 of 10 Maria: 1 Name Note the Select the correct answer The output of following digital logic circuit is O A.B O A+B O A.(A+B)
O B.(A+B) Clear Restored











[Unit 6] 6 of 6 Question: 7 of 10 Marks: 1 Negative Marks: 45% on wrong more 6 the correct answer ich of the following statement is true for FPGA? FPGA stands for First Programmable Gate Arrays FPGAs can not be reprogrammed to implement different logic fordors FPGAs can be reprogrammed to implement different logic functions EPGA can not be configured to function as a processor

