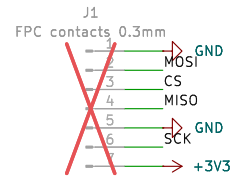


Internal pulldowns
on all SPI lines



Ideally each signal line is
next to a ground line.
However, this is an
extremely space limited
part so the most natural
wiring from the chip out
takes priority. CS kinda
acts like ground when it
is selected

Supports SPI mode 0 or 3,
automatically

Sheet: /		
File: MA735 encoder board.kicad_sch		
Title:		
Size: A4	Date: 2025-05-05	Rev:
KiCad E.D.A. 9.0.1	Id: 1/1	