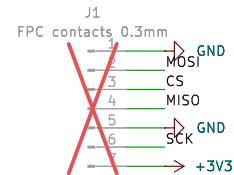


Internal pulldowns
on all SPI lines

Supports SPI mode 0 or 3,
automatically



Ideally each signal line is
next to a ground line.
However, this is an
extremely space limited
part so the most natural
wiring from the chip out
takes priority. CS kinda
acts like ground when it
is selected

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Size: A4 Date: 2025-11-16

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Rev: V1.1

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