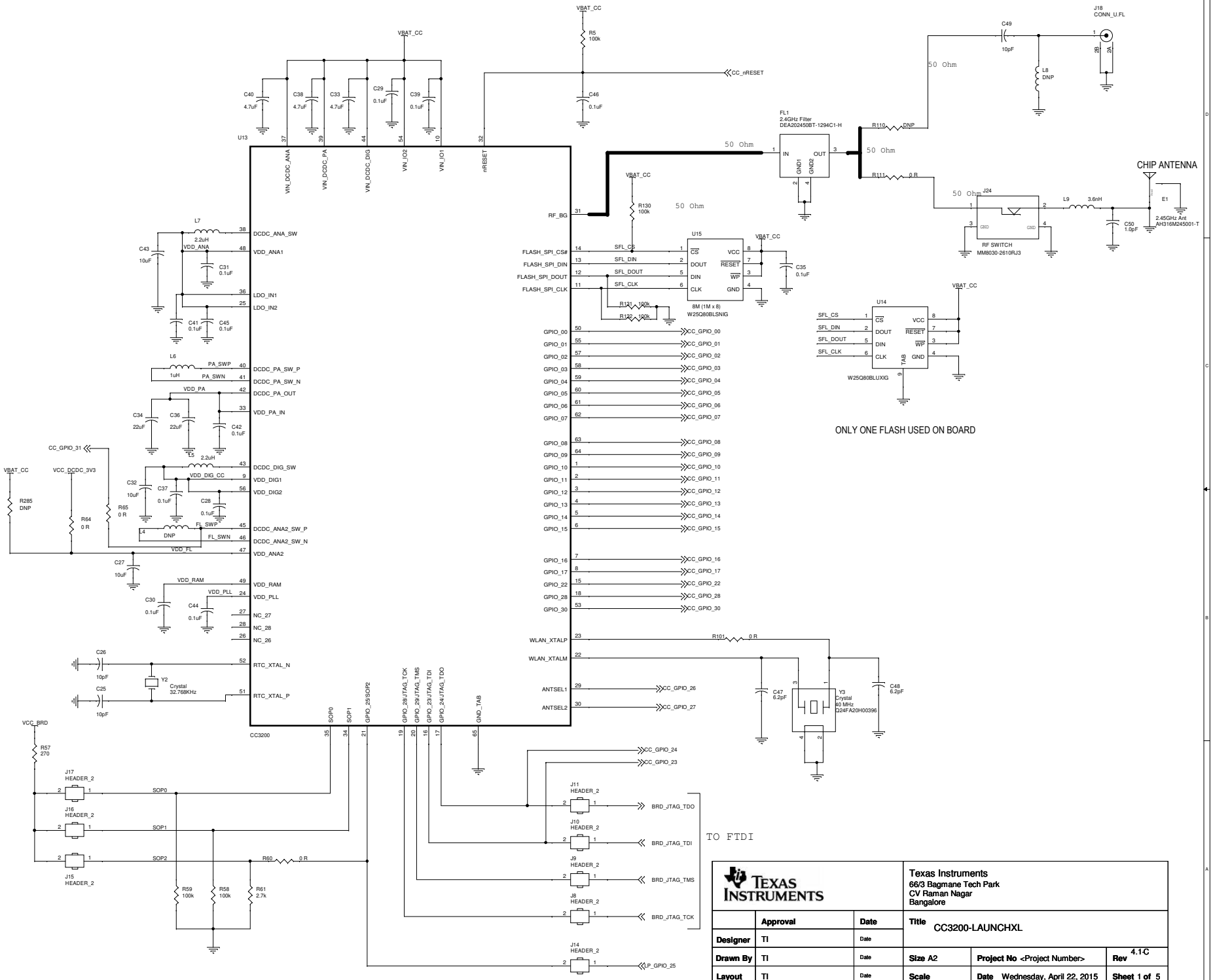

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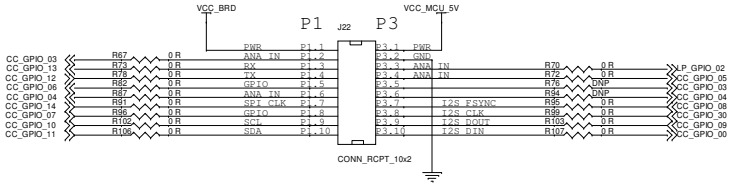
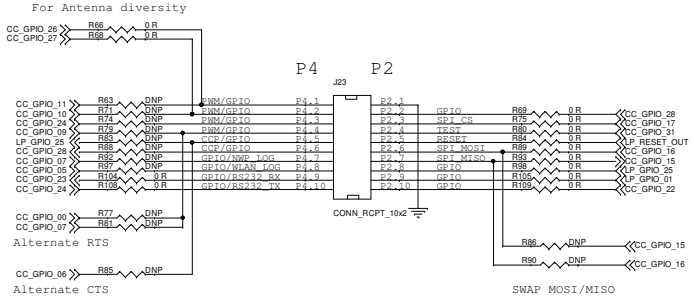
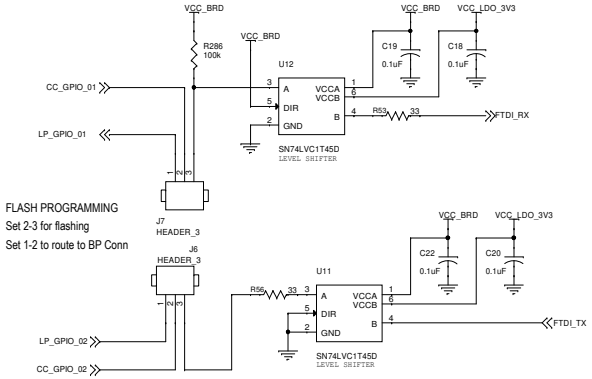
Designer	TI	Date			
Drawn By	TI	Date	Size A2	Project No <Project Number>	Rev 4.1C
Layout	TI	Date	Scale	Date Wednesday, April 22, 2015	Sheet 1 of 5

BOOSTER PACK INTERFACE

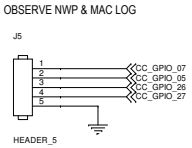




UART MUXING



TEST POINTS



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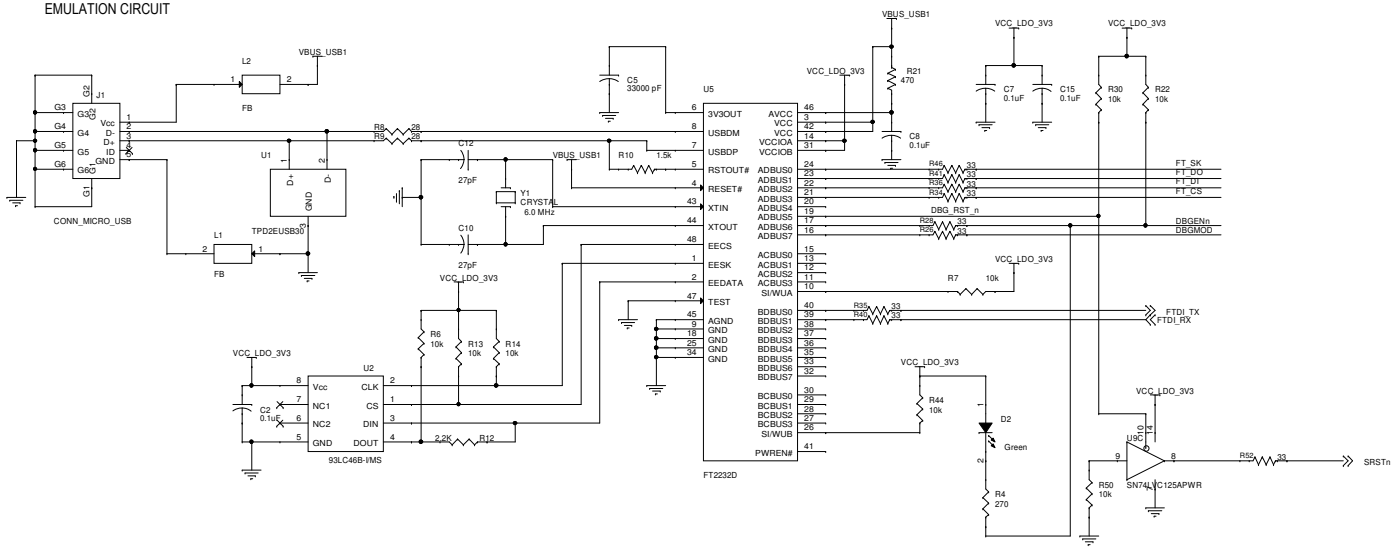
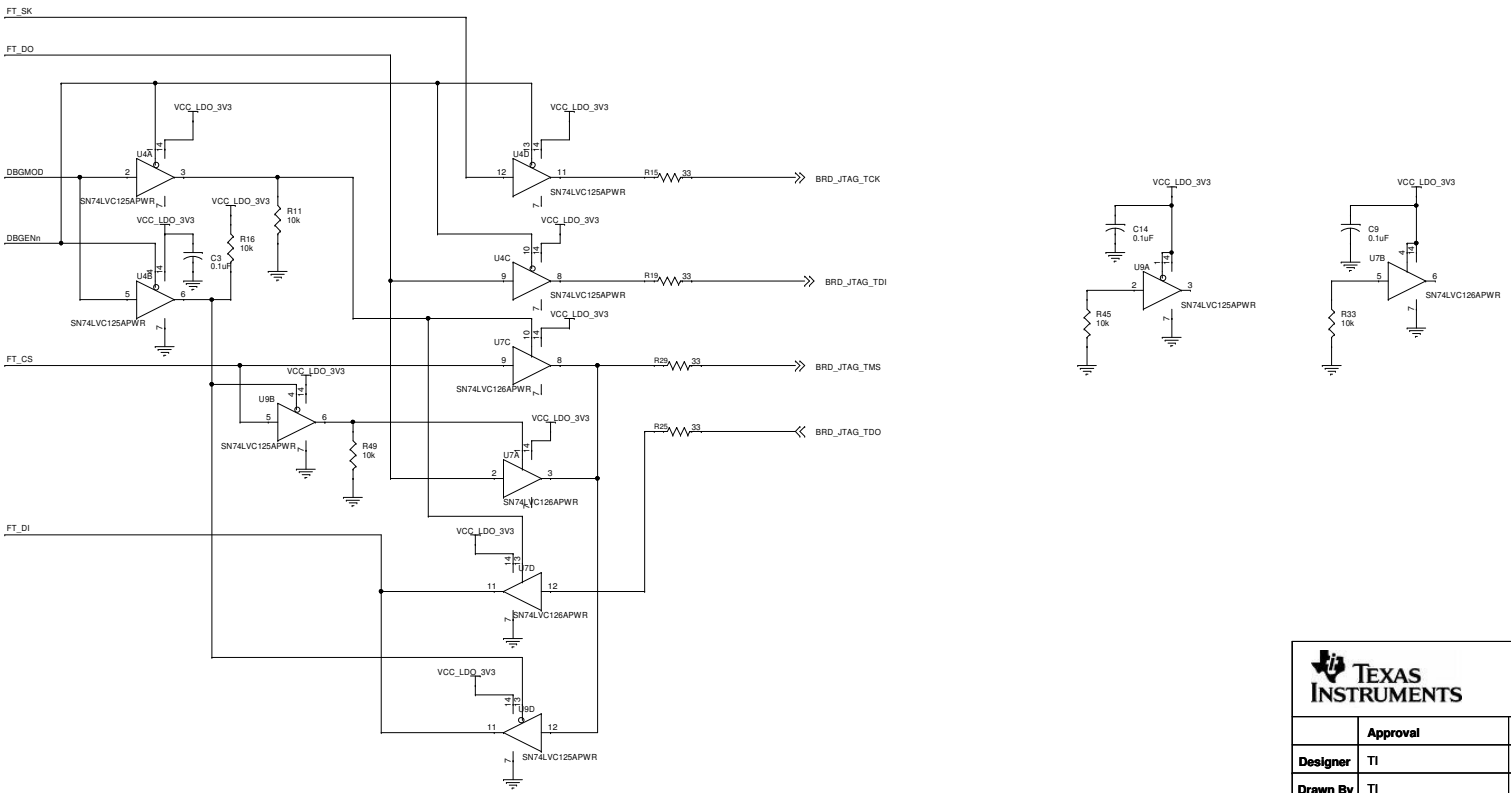
Designer	TI	Date			
Drawn By	TI	Date	Size A2	Project No <Project Number>	Rev 4.1
Layout	TI	Date	Scale	Date Wednesday, April 22, 2015	Sheet 2 of 5

EMULATION CIRCUIT



JTAG/SWD SWITCH



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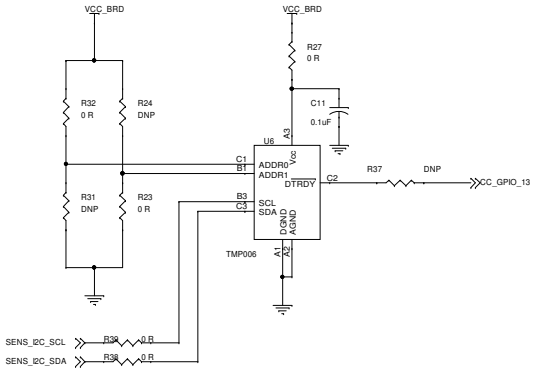
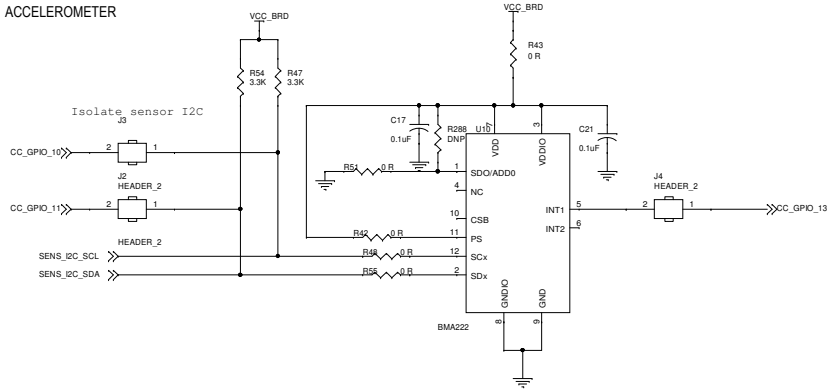
Designer	TI	Date	
Drawn By	TI	Date	**Size** A2 / **Project No** <Project Number> / **Rev** 4.1
Layout	TI	Date	**Scale** / **Date** Wednesday, April 22, 2015 / **Sheet** 3 of 5

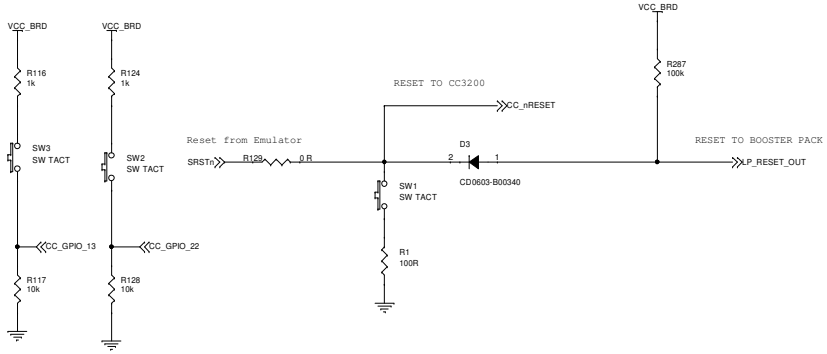
TEMP SENSOR



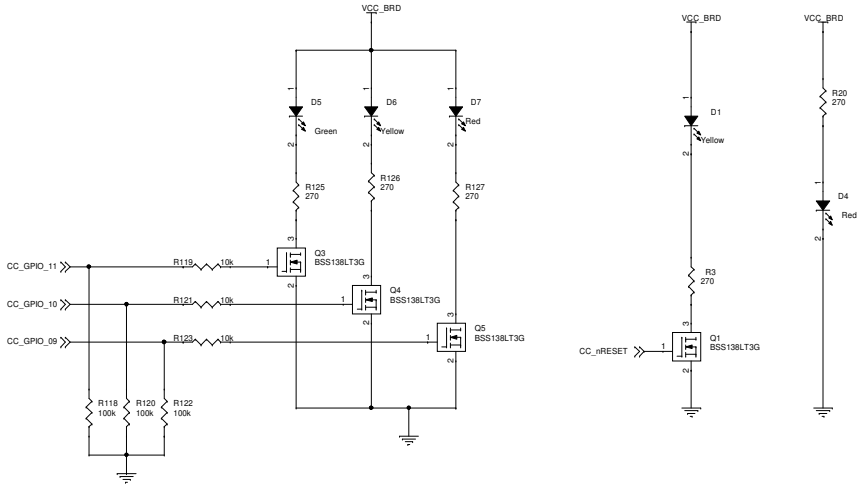
ACCELEROMETER



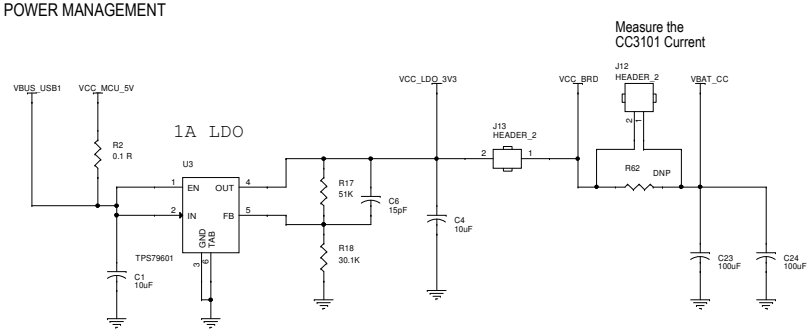
SWITCHES & RESET

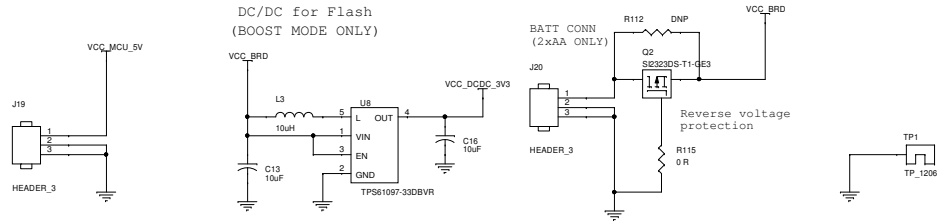


LED INDICATORS



POWER MANAGEMENT





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Drawn By	TI	Date	**Size** A2	**Project No** <Project Number>	**Rev** 4.1
Layout	TI	Date	**Scale**	**Date** Wednesday, April 22, 2015	**Sheet** 4 of 5

Board Revision control naming convention
X.Y-Z (X= schemtic ver, Y = layout version (include silk changes), Z = BOM variant)

Revision history
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Changes in Rev 4.0-A
1. Add pull-ups and pull-down resistors for the serial flash
2. Move the nRESET pull from VCC_BRD to VBAT_CC
3. Add pulll-up on UART_TX going to the FTDI to prevent false start bits.
4. Add pull-up resistor for Acccelerometer address to avoid conflict with Audio Booster pack
5. Add 100K pull-up on RESET_OUT net for any BP without RESET pulls.
6. Change R61 to 2.7K, R57-> 270 Ohms

Rev 4.1-B
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1. Changed the s-flash from Micron to Winbond

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Layout	TI	Date	**Scale**	**Date** Wednesday, April 22, 2015	**Sheet** 5 **of** 5