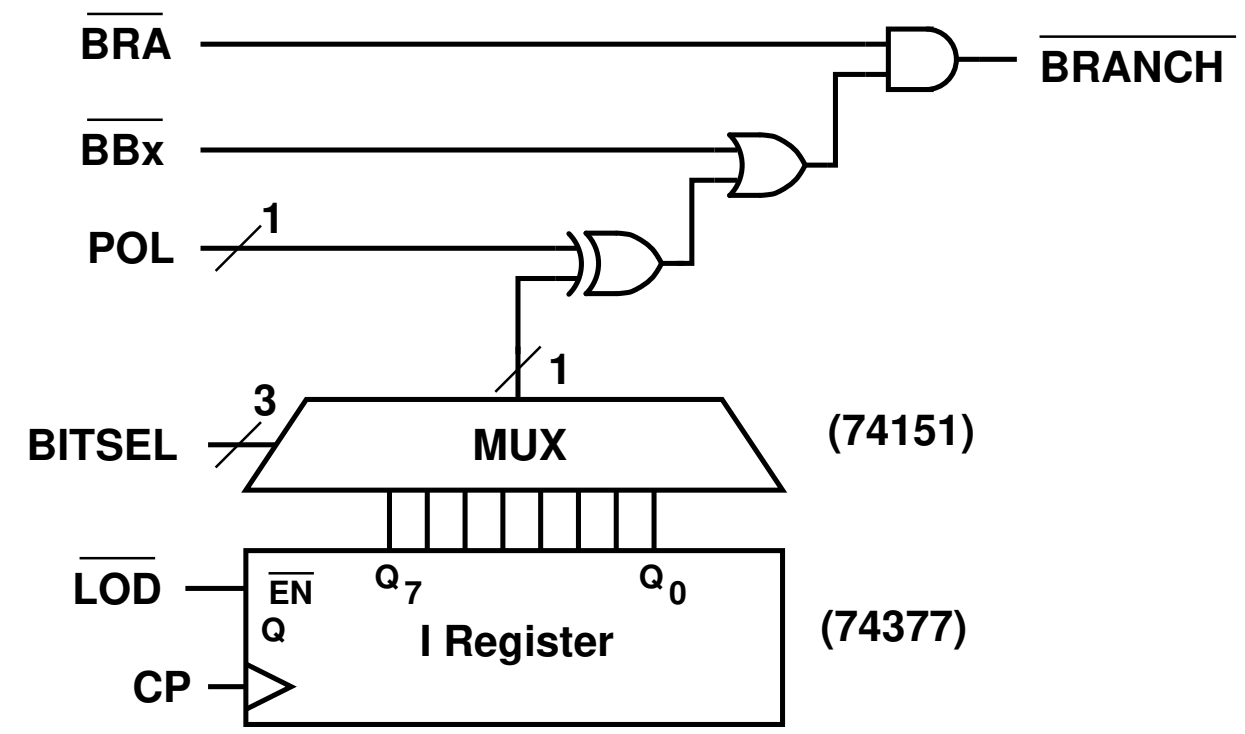


Instruction Fetch

Instruction Decode



Branch Logic