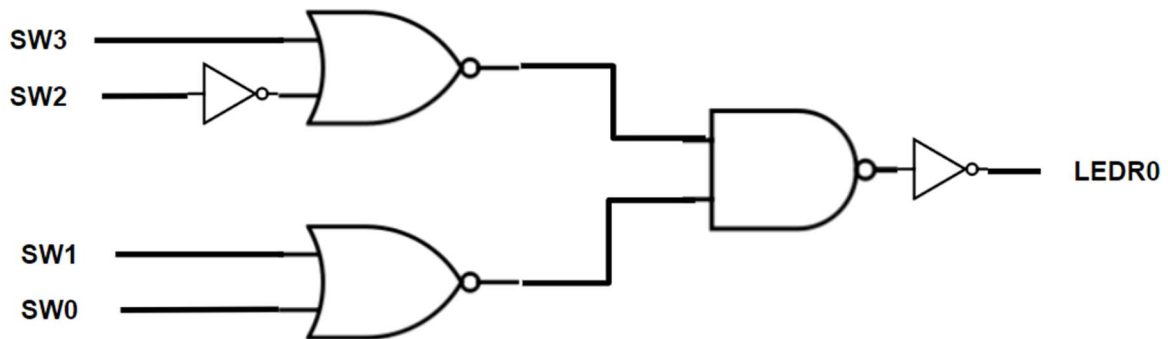
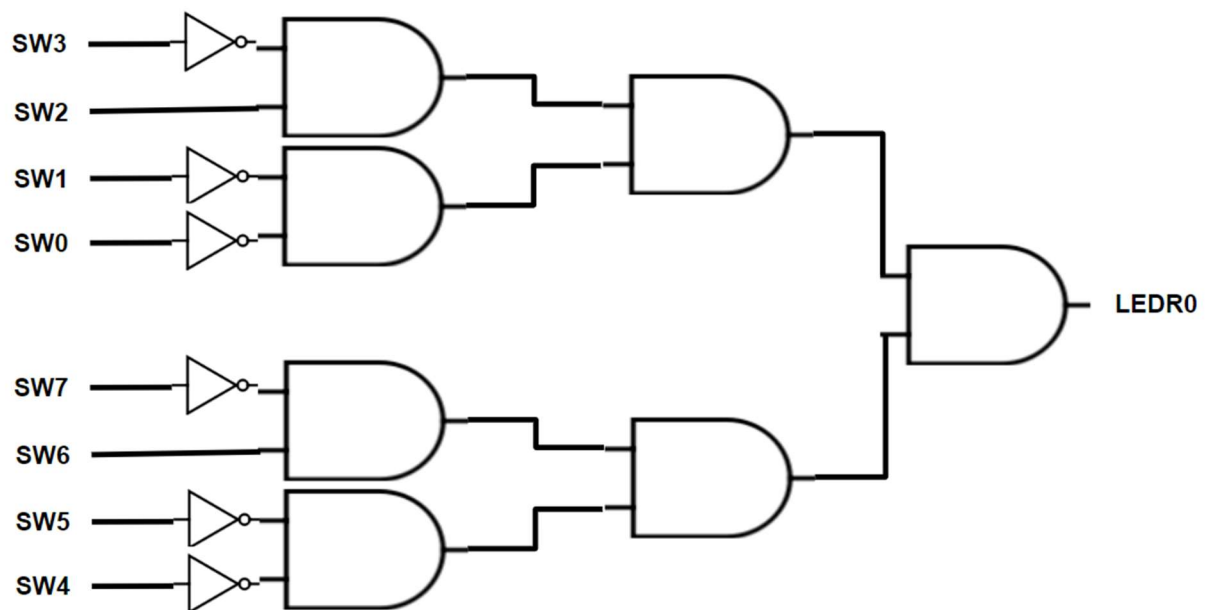


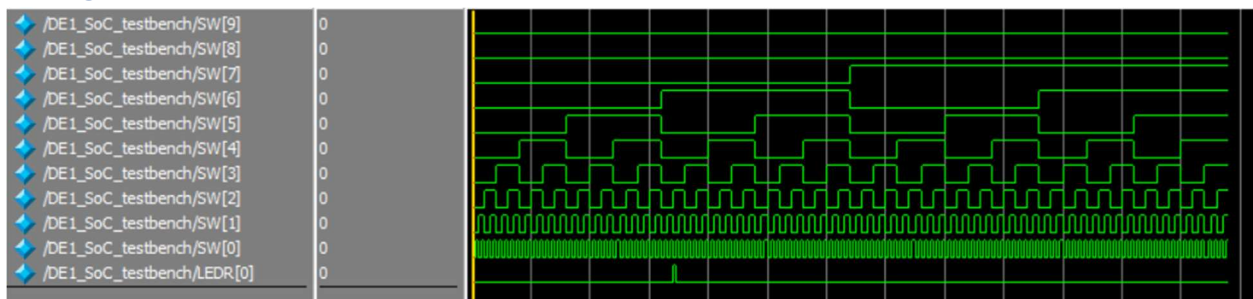
Design Problem #1 Schematic



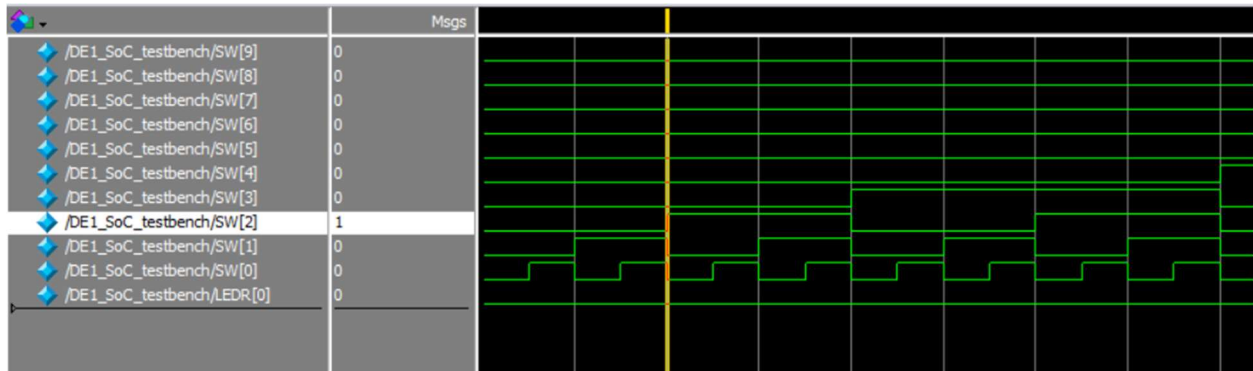
Design Problem #2 Schematic



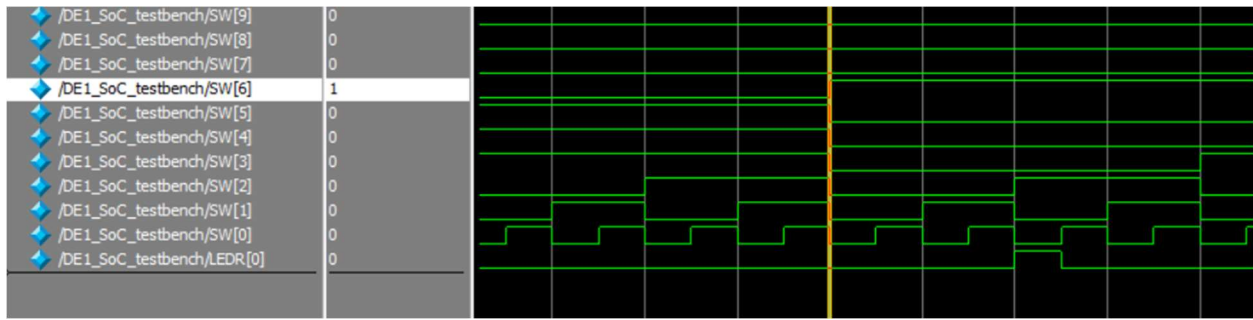
Design Problem #2 Simulations



1. Full results for every possible input combination for SW[0]...SW[9]. The output, LEDR[0] remains at low except for one input.



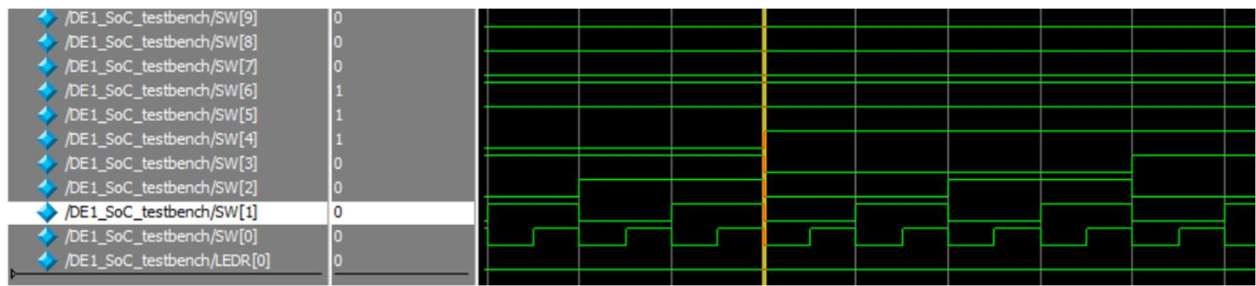
2. Input of high on SW[2], corresponding to last digit of student number. LEDR[0] remains at low.



3. Input of high on SW[6], corresponding to second to last digit of student number. LEDR[0] remains at low.



4. Input of high on both SW[6] and SW[2]. LEDR[0] is now set to high.



5. Randomly chosen input combination of switches. LEDR[0] is at low.