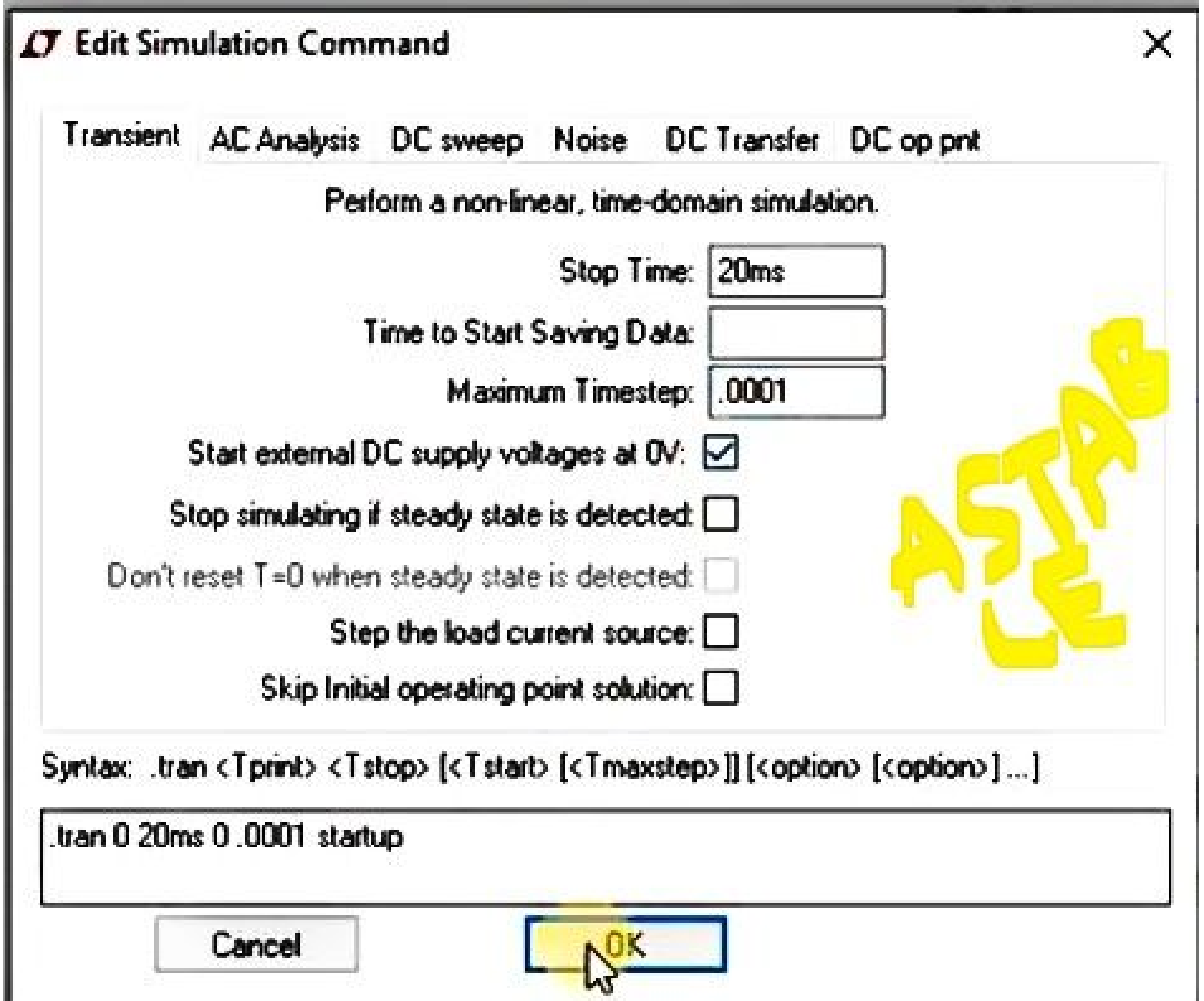
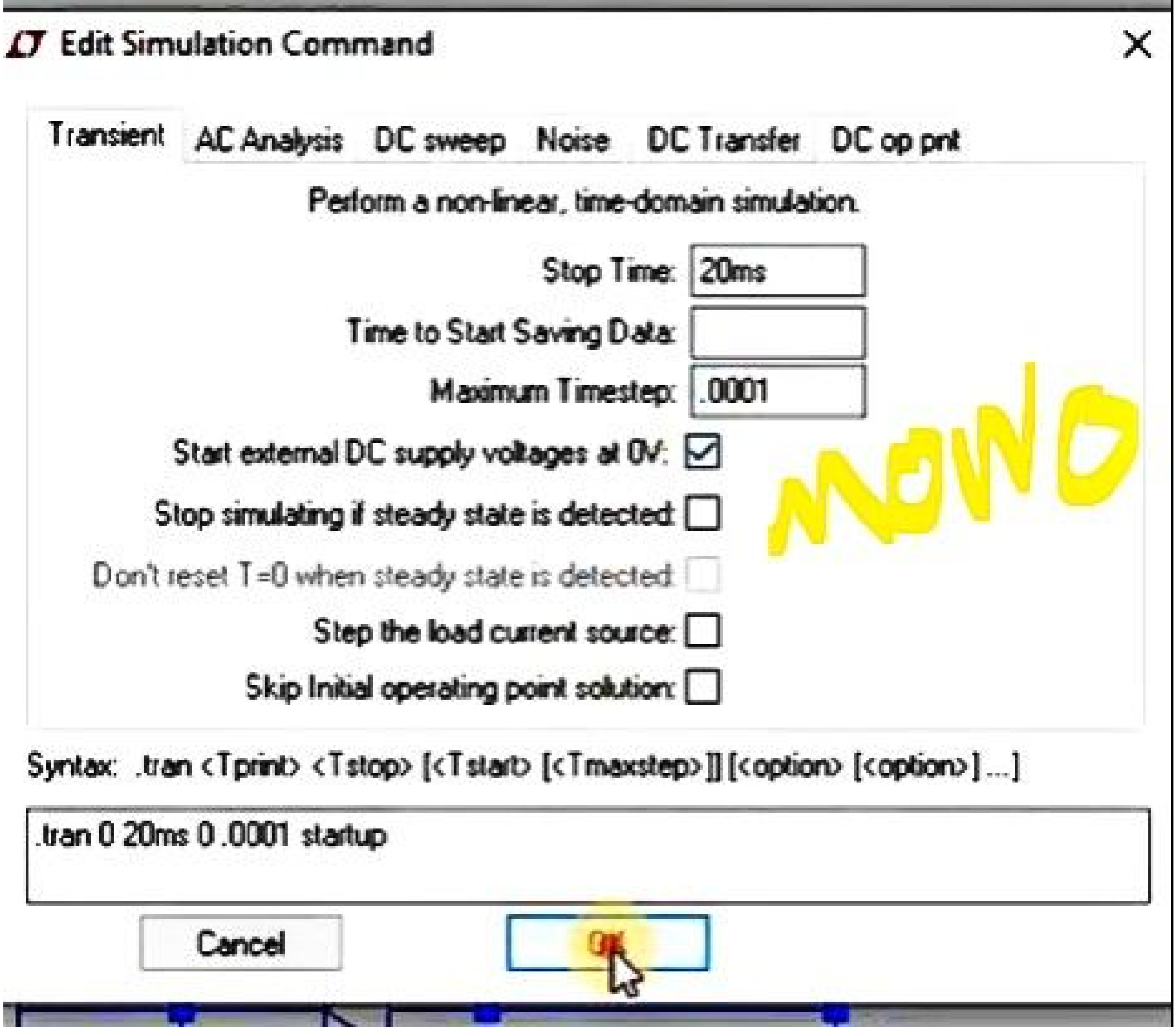
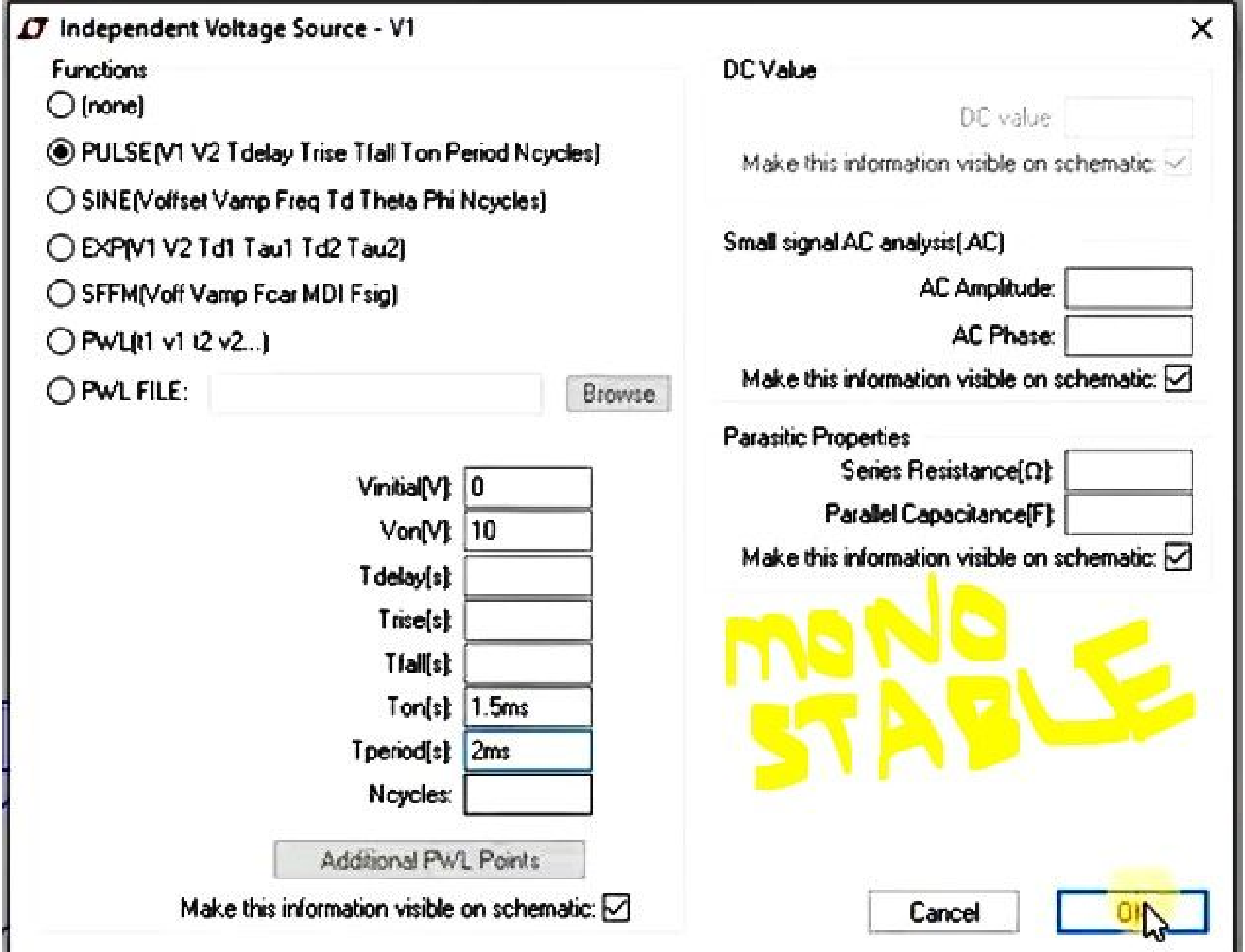
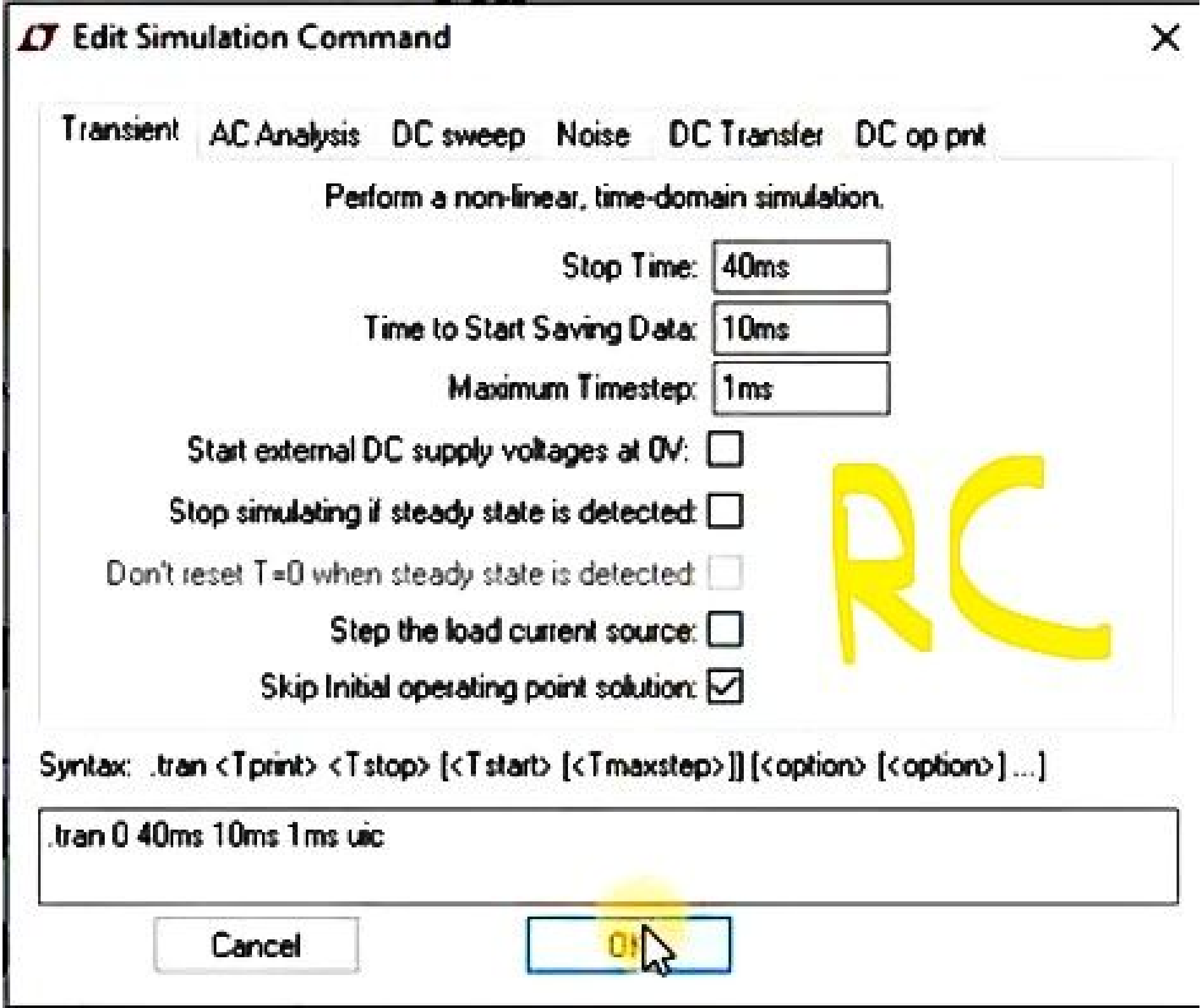
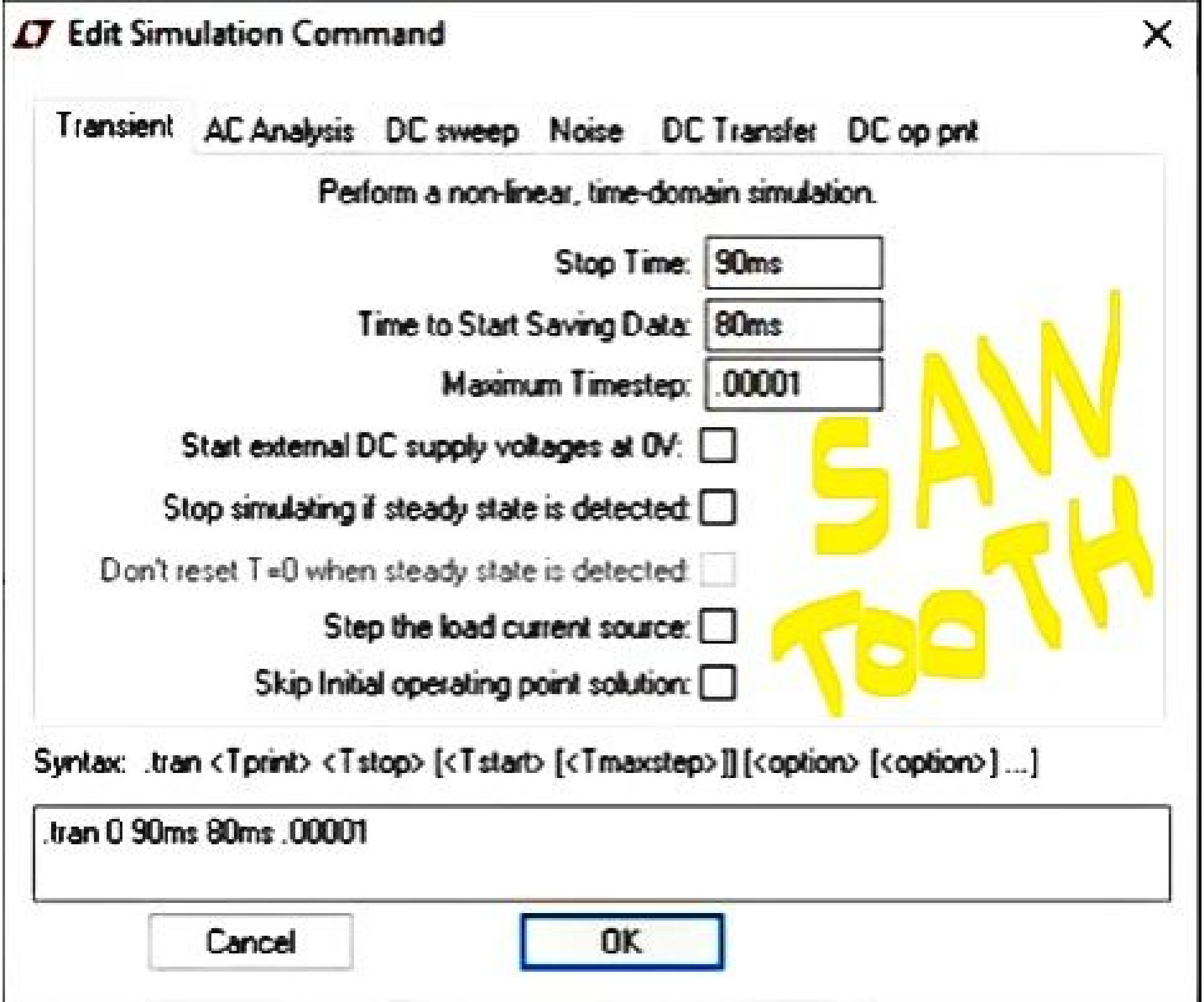
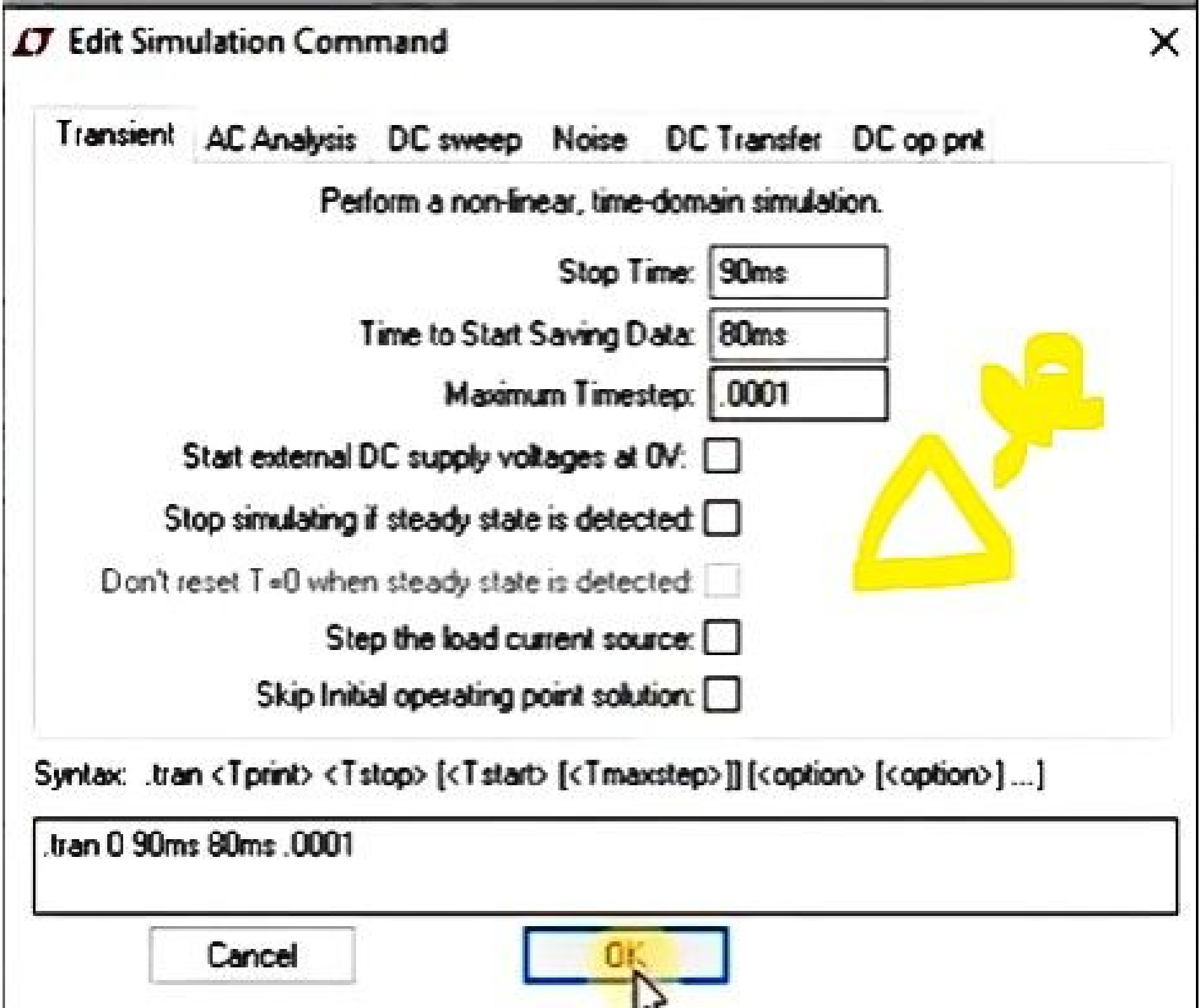

Edit Simulation Command
Transient
AC Analysis
DC sweep
Noise
DC Transfer
DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time:
20ms
Time to Start Saving Data:
Maximum Timestep:
.0001
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution:
Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]
.tran 0 20ms 0 .0001 startup
Cancel
OK


Edit Simulation Command
Transient
AC Analysis
DC sweep
Noise
DC Transfer
DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time:
20ms
Time to Start Saving Data:
Maximum Timestep:
.0001
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution:
NOW
Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>]...]
.tran 0 20ms 0 .0001 startup
Cancel
OK


Independent Voltage Source - V1
Functions
(none)
PULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles)
SINE(Voffset Vamp Freq Td Theta Phi Ncycles)
EXP(V1 V2 Td1 Tau1 Td2 Tau2)
SFFM(Voff Vamp Fcar MDI Fsig)
PWL(t1 v1 t2 v2...)
PWL FILE:
Browse
Vinitial[V]: 0
Von[V]: 10
Tdelay[s]:
Trise[s]:
Tfall[s]:
Ton[s]: 1.5ms
Tperiod[s]: 2ms
Ncycles:
Additional PWL Points
Make this information visible on schematic:
DC Value
DC value:
Make this information visible on schematic:
Small signal AC analysis(AC)
AC Amplitude:
AC Phase:
Make this information visible on schematic:
Parasitic Properties
Series Resistance[Ω]:
Parallel Capacitance[F]:
Make this information visible on schematic:
MONO STABLE
Cancel
OK


Edit Simulation Command
Transient
AC Analysis
DC sweep
Noise
DC Transfer
DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time:
40ms
Time to Start Saving Data:
10ms
Maximum Timestep:
1ms
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution:
RC
Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]
.tran 0 40ms 10ms 1ms uic
Cancel
OK


Edit Simulation Command
Transient
AC Analysis
DC sweep
Noise
DC Transfer
DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time:
90ms
Time to Start Saving Data:
80ms
Maximum Timestep:
.00001
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution:
SAW TOOTH
Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]
.tran 0 90ms 80ms .00001
Cancel
OK


Edit Simulation Command
Transient
AC Analysis
DC sweep
Noise
DC Transfer
DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time:
90ms
Time to Start Saving Data:
80ms
Maximum Timestep:
.0001
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution:
Syntax: .tran <Tprint> <Tstop> [<Tstart> [<Tmaxstep>]] [<option> [<option>] ...]
.tran 0 90ms 80ms .0001
Cancel
OK