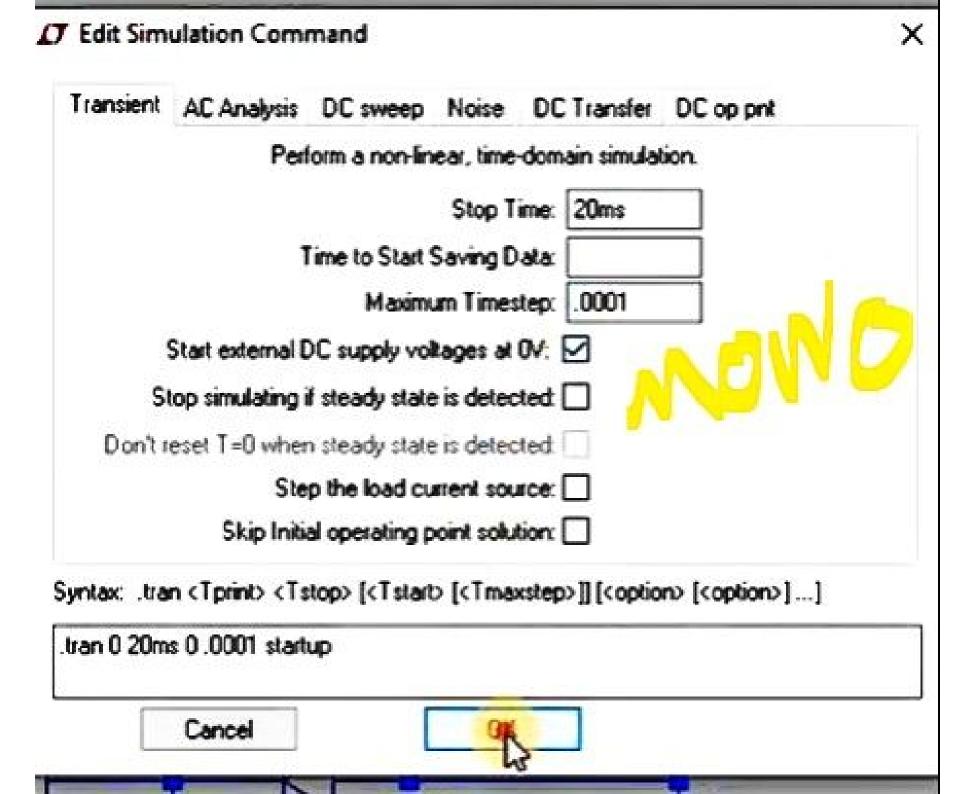
ransient	AC Analysis	DC sweep	Noise	DC Transfe	er DC op pr	at .
	Perl	orm a non-lin	ear, time	domain simu	dation.	
			Stop T	ime: 20ms		
	7	ime to Start :	Saving D	ata:		
		Maximo	ım Times	tep: 0001		
	Start external [C supply vol	tages at	ov: ☑	1	
Si	op simulating i	f steady state	is detec	ted: 🗌		2/1
Don't r	eset T=0 wher	steady state	is detec	ted: 🗌	- (~)	12
	Ste	p the load cu	rrent sou	rce: 🔲		
	Skip Initi	al operating p	ioint solui	tion: 🔲		
ntax: .tra	n <tprint> <ts< td=""><td>top> (<t start<="" td=""><td>> [<tmax< td=""><td>cstep>]] [<op< td=""><td>tion> (coption</td><td>ນ]]</td></op<></td></tmax<></td></t></td></ts<></tprint>	top> (<t start<="" td=""><td>> [<tmax< td=""><td>cstep>]] [<op< td=""><td>tion> (coption</td><td>ນ]]</td></op<></td></tmax<></td></t>	> [<tmax< td=""><td>cstep>]] [<op< td=""><td>tion> (coption</td><td>ນ]]</td></op<></td></tmax<>	cstep>]] [<op< td=""><td>tion> (coption</td><td>ນ]]</td></op<>	tion> (coption	ນ]]
an 0 20m	s 0 .0001 start	up				



☐ Independent Voltage Source - V1		×		
Functions		DC Value		
○ (none)	DC value			
PULSE(V1 V2 Tdelay Trise Tfall Ton P	eriod Noycles)	Make this information visible on schematic: <		
SINE(Volfset Vamp Freq Td Theta Phi				
○ EXP(V1 V2 Td1 Tau1 Td2 Tau2)	Small signal AC analysis(AC)			
○ SFFM(Voff Vamp Fcar MDI Fsig)	AC Amplitude:			
○ PWL(t1 v1 t2 v2)				
O PWL FILE:	Browse	Make this information visible on schematic:		
		Parasitic Properties Series Resistance(Ω):		
Vinitial(V):		Parallel Capacitance[F]:		
Von[V]:	10	Make this information visible on schematic:		
T delay(s):				
Trise(s):				
Tfall(s):				
Ton(s):	1.5ms			
Tperiod(s):	2ms			
Ncycles:				
Additional PW	L Points			
Make this information visible	on schematic: 🗹	Cancel 01		

Transient ACAnalysis DC sweep Noise DC Transfer DC op pnt
Perform a non-linear, time-domain simulation.
Stop Time: 40ms
Time to Start Saving Data: 10ms
Maximum Timestep: 1ms
Start external DC supply voltages at 0V:
Stop simulating if steady state is detected:
Don't reset T=0 when steady state is detected:
Step the load current source:
Skip Initial operating point solution: 🗹
Syntax: .tran <tprint> <tstop> [<tstart> [<tmaxstep>]] [<option> [<option>]]</option></option></tmaxstep></tstart></tstop></tprint>
tran 0 40ms 10ms 1 ms uic
Cancel

Transient	AC Analysis	DC sweep	Noise	DC	Transfer	DC op pn	k
	Per	orm a non-lin	ear, time	-doma	ein simulal	tion.	
			Stop T	ime:	90ms		
	1	ime to Start	Saving D	ata:	80ms		
		Maxim	um Times	tep:	.00001		N
	Start external [C supply vo	ltages at	ov: [J (-11	AA
SI	op simulating	f steady state	e is detec	ted:[_	-	-11
Don't r	eset T=0 wher	n steady state	e is detec	ted	J 👑		77
	Ste	p the load cu	arrent sou	rce: [J 🦯		100
	Skip Initi	al operating p	oint solu	tion: [10-	
yntax: .tra	n <tprint> <t:< th=""><th>top> [<tstart< th=""><th>o [<tmax< th=""><th>estep:</th><th>] [<optio< th=""><th>n> (coption</th><th>[[٥</th></optio<></th></tmax<></th></tstart<></th></t:<></tprint>	top> [<tstart< th=""><th>o [<tmax< th=""><th>estep:</th><th>] [<optio< th=""><th>n> (coption</th><th>[[٥</th></optio<></th></tmax<></th></tstart<>	o [<tmax< th=""><th>estep:</th><th>] [<optio< th=""><th>n> (coption</th><th>[[٥</th></optio<></th></tmax<>	estep:] [<optio< th=""><th>n> (coption</th><th>[[٥</th></optio<>	n> (coption	[[٥
tran () 90m	s 80ms .00001						
	Cancel		OK		1		

