

Title: IMPLEMENTATION AND PERFORMANCE ANALYSIS OF SDN FIREWALL

Overview of firewall implementation –

I specified the rules for the Firewall by writing down Layer-2 addresses of hosts. I have then put the rules as rows in a list so that I can iterate over each rule separately later. Next created SDNFirewall class in which the actual controller is going to be accessing and checking flows and modifying flow tables accordingly. Whenever a host tries to reach other host over switch, it will first iterate over each rule in list. Here I created an OpenFlow flow_mod message using `of.ofp_flow_mod()` and set its match field to our blocking rule.

Concepts implemented –

- **POX Controller [0.3.0]-**

Pox is an open-source development platform for Python-based software-defined networking control applications

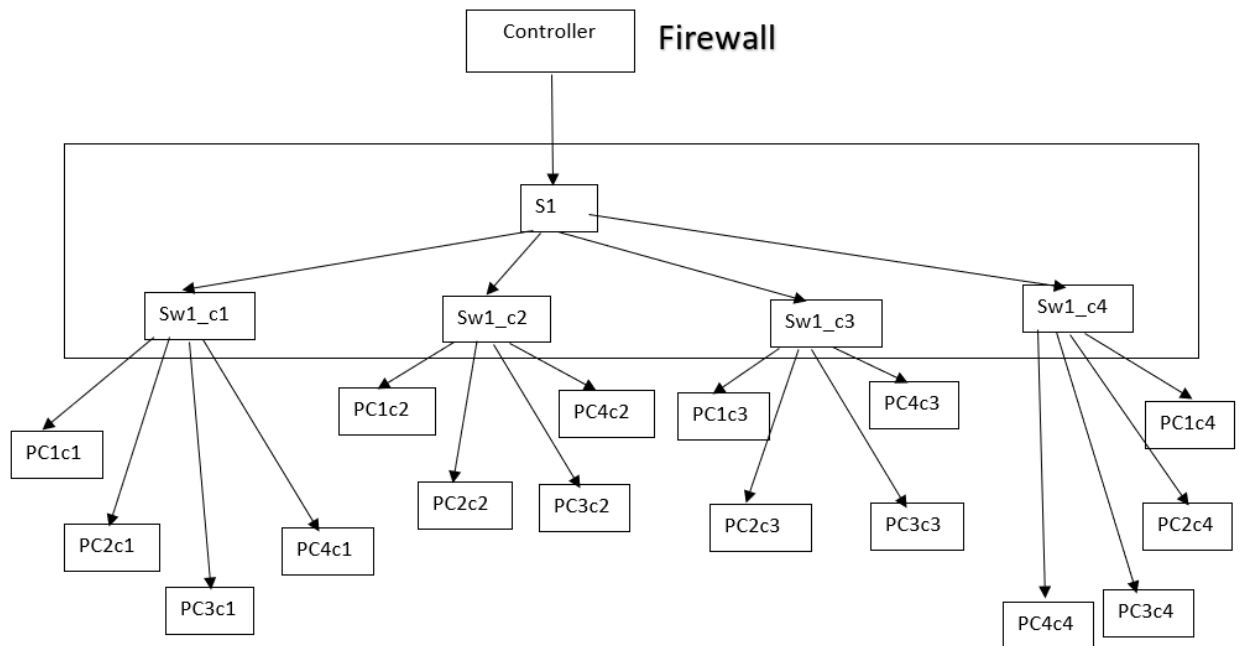
- **OpenFlow switch [Version 1.0]-**

Using OpenFlow protocol and related tools, you can program switches to do almost anything you want with the frames that enter them. OpenFlow makes emulator like Mininet much more useful, since network system designs, including custom packet forwarding with OpenFlow.

- **Simulator Used [Version 2.2.1]-**

Mininet is used to build the topology and run test cases.

Topology Used –



Procedure-

Part 1:

- Install mininet and Pox on system
- Run the python script to build topology

Command –

```
sudo mn --custom topo1.py --topo mytopo
```

- Created Flow between switches:
- Command -

```
sh ovs-ofctl add-flow SW1c1 "priority=0, action=normal"
```

```
sh ovs-ofctl dump-flows SW1c1
```

- Tested the topology using pingall to find the reachability

Part 2 –

- Created a myfirewall.py which will block the desired host among each other
- Run generate_rules.py to generate rule tables for blocked flow Layer-2 address
- Run following command enabling spanning tree to avoid loops-

Command-

```
./pox.py forwarding.l2_learning openflow.discovery  
openflow.spanning_tree --no-flood --hold-down pox.misc.myfirewall
```

- In another terminal run the topology
- Test using pingall

Result –

Part -1

After adding flow amongst all switches. 0% packet dropped is observed, i.e. 240/240 packets were transferred successfully.

```
*** Creating network  
*** Adding controller  
*** Adding hosts:  
PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
*** Adding switches:  
SW1c1 SW1c2 SW1c3 SW1c4 s1  
*** Adding links:  
(SW1c1, PC1c1) (SW1c1, PC1c2) (SW1c1, PC1c3) (SW1c1, PC1c4) (SW1c2, PC2c1) (SW1c2, PC2c2) (SW1c2, PC2c3) (SW1c2, PC2c4) (SW1c3, PC3c1) (SW1c3, PC3c2) (SW1c3, PC3c3) (SW1c3, PC3c4) (SW1c4, PC4c1) (SW1c4, PC4c2) (SW1c4, PC4c3) (SW1c4, PC4c4) (s1, SW1c1) (s1, SW1c2) (s1, SW1c3) (s1, SW1c4)  
*** Configuring hosts  
PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
*** Starting controller  
  
*** Starting 5 switches  
SW1c1 SW1c2 SW1c3 SW1c4 s1 ...  
*** Starting CLI:  
mininet> pingall  
*** Ping: testing ping reachability  
PC1c1 -> PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC1c2 -> PC1c1 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC1c3 -> PC1c1 PC1c2 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC1c4 -> PC1c1 PC1c2 PC1c3 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC2c1 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC2c2 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC2c3 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC2c4 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC3c1 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC3c2 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC3c3 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC3c4 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c3 PC4c4  
PC4c1 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c2 PC4c3 PC4c4  
PC4c2 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c3 PC4c4  
PC4c3 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c4  
PC4c4 -> PC1c1 PC1c2 PC1c3 PC1c4 PC2c1 PC2c2 PC2c3 PC2c4 PC3c1 PC3c2 PC3c3 PC3c4 PC4c1 PC4c2 PC4c4  
*** Results: 0% dropped (240/240 received)  
mininet>
```

Result 0% dropped 240/240

Part -2

After adding the Firewall to topology 20% packed dropped is observed i.e. 192/240 packets transferred successfully.

```
mininet> pingall
*** Ping: testing ping reachability
PC1c1 -> PC1c2 PC1c3 PC1c4 X PC2c2 PC2c3 PC2c4 X PC3c2 PC3c3 PC3c4 X PC4c2 PC4c3 PC4c4
PC1c2 -> PC1c1 PC1c3 PC1c4 PC2c1 X PC2c3 PC2c4 PC3c1 X PC3c3 PC3c4 PC4c1 X PC4c3 PC4c4
PC1c3 -> PC1c1 PC1c2 PC1c4 PC2c1 PC2c2 X PC2c4 PC3c1 PC3c2 X PC3c4 PC4c1 PC4c2 X PC4c4
PC1c4 -> PC1c1 PC1c2 PC1c3 PC2c1 PC2c2 PC2c3 X PC3c1 PC3c2 PC3c3 X PC4c1 PC4c2 PC4c3 X
PC2c1 -> X PC1c2 PC1c3 PC1c4 PC2c2 PC2c3 PC2c4 X PC3c2 PC3c3 PC3c4 X PC4c2 PC4c3 PC4c4
PC2c2 -> PC1c1 X PC1c3 PC1c4 PC2c1 PC2c3 PC2c4 PC3c1 X PC3c3 PC3c4 PC4c1 X PC4c3 PC4c4
PC2c3 -> PC1c1 PC1c2 X PC1c4 PC2c1 PC2c2 PC2c4 PC3c1 PC3c2 X PC3c4 PC4c1 PC4c2 X PC4c4
PC2c4 -> PC1c1 PC1c2 PC1c3 X PC2c1 PC2c2 PC2c3 PC3c1 PC3c2 PC3c3 X PC4c1 PC4c2 PC4c3 X
PC3c1 -> X PC1c2 PC1c3 PC1c4 X PC2c2 PC2c3 PC2c4 PC3c2 PC3c3 PC3c4 X PC4c2 PC4c3 PC4c4
PC3c2 -> PC1c1 X PC1c3 PC1c4 PC2c1 X PC2c3 PC2c4 PC3c1 PC3c3 PC3c4 PC4c1 X PC4c3 PC4c4
PC3c3 -> PC1c1 PC1c2 X PC1c4 PC2c1 PC2c2 X PC2c4 PC3c1 PC3c2 PC3c4 PC4c1 PC4c2 X PC4c4
PC3c4 -> PC1c1 PC1c2 PC1c3 X PC2c1 PC2c2 PC2c3 X PC3c1 PC3c2 PC3c3 PC4c1 PC4c2 PC4c3 X
PC4c1 -> X PC1c2 PC1c3 PC1c4 X PC2c2 PC2c3 PC2c4 X PC3c2 PC3c3 PC3c4 PC4c2 PC4c3 PC4c4
PC4c2 -> PC1c1 X PC1c3 PC1c4 PC2c1 X PC2c3 PC2c4 PC3c1 X PC3c3 PC3c4 PC4c1 PC4c3 PC4c4
PC4c3 -> PC1c1 PC1c2 X PC1c4 PC2c1 PC2c2 X PC2c4 PC3c1 PC3c2 X PC3c4 PC4c1 PC4c2 PC4c4
PC4c4 -> PC1c1 PC1c2 PC1c3 X PC2c1 PC2c2 PC2c3 X PC3c1 PC3c2 PC3c3 X PC4c1 PC4c2 PC4c3
*** Results: 20% dropped (192/240 received)
mininet> █
```

Result 20% dropped 192/240