Assignment-9

```c
#include <pic18f4550.h>
//#include<xc.h>

#define RELAY_PIN LATAbits.LATA4

void __interrupt() extint_isr(void)
{
    unsigned int i;
    if(INT1IF)
    {
        INT1IF = 0;
        INT1IE = 0;
        RELAY_PIN = ~RELAY_PIN;
        for(i=0; i<10000; i++);    //small delay for debouncing
        INT1IE = 1;
    }
}

int main(){
    ADCON1 = 0x0F;        //set pins as Digital in Analog To Digital Control Register 1
    TRISAbits.TRISA4 = 0;  //set relay pin RA4 as output
    TRISBbits.TRISB1 = 1;  //Interrupt pin as input
    RELAY_PIN = 1;

    INT1IE  =  1;             //Enable external interrupt INT1
    INTEDG1 =  0;              //Interrupt on falling edge
    GIE   =  1;             // Enable global interrupt

    while(1);
}
```

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PC: 0x870   n ov z dc c : W:0xf : bank 0   How do I?

Start Page × Code.c ×

Source | History

```c
6      void __interrupt() extint_isr(void)
7    {
8        unsigned int i;
9        if(INT1IF)
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11           INT1IF = 0;
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14           for(i=0; i<10000; i++);        //small delay for debouncing
15           INT1IE = 1;
16       }
17   }
18
19   int main(){
20       ADCON1 = 0x0F;        //set pins as Digital in Analog To Digital Control Reg1
21
```

SFRs ×

Address	Name	Hex	Decimal	Binary	Char
F83	PORTD	0x00	0	00000000	'.'
F84	PORTE	0x00	0	00000000	'.'
F89	LATA	0x10	16	00010000	'.'
F8A	LATB	0x00	0	00000000	'.'
F8B	LATC	0x00	0	00000000	'.'
F8C	LATD	0x00	0	00000000	'.'
F8D	LATE	0x00	0	00000000	'.'
F92	TRISA	0x6F	111	01101111	'o'
F93	TRISB	0xFF	255	11111111	'ÿ'
F94	TRISC	0xC7	199	11000111	'Ç'
F95	TRISD	0xFF	255	11111111	'ÿ'
F96	TRISE	0x07	7	00000111	'.'
F9B	OSCTUNE	0x00	0	00000000	'.'
F9D	PIE1	0x00	0	00000000	'.'
F9E	PIR1	0x00	0	00000000	'.'
F9F	IPR1	0xFF	255	11111111	'ÿ'
FA0	PIE2	0x00	0	00000000	'.'
FA1	PIR2	0x00	0	00000000	'.'
FA2	IPR2	0xFF	255	11111111	'ÿ'
FA6	EECON1	0x00	0	00000000	'.'
FA7	EECON2	0x00	0	00000000	'.'
FA8	EEDATA	0x00	0	00000000	'.'
FA9	EEADR	0x00	0	00000000	'.'
FAB	RCSTA	0x00	0	00000000	'.'
FAC	TXSTA	0x02	2	00000010	'.'
FAD	TXREG	0x00	0	00000000	'.'
FAE	RCREG	0x00	0	00000000	'.'
FAF	SPBRG	0x00	0	00000000	'.'
FB0	SPBRGH	0x00	0	00000000	'.'
FB1	T3CON	0x00	0	00000000	'.'
FB2	TMR3	0x0000	0	00000000 00000000	'..'
FB2	TMR3L	0x00	0	00000000	'.'
FB3	TMR3H	0x00	0	00000000	'.'
FB4	CMCON	0x07	7	00000111	'.'
FB5	CVRCON	0x00	0	00000000	'.'

Memory SFRs   Format Individual

Filter Address

Variables ×

Name	Type	Address	Value	Hexadecimal	Decimal
<Enter new watch>					
GIE	bit	0xFF2	0x01	0x01	1

Watches   Output

Assignmnet (Build, Load, ...)   debugger halted   29:1   INS

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PC: 0x870   n ov z dc c : W:0xf : bank 0   How do I?

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```

SFRs ×

Address	Name	Hex	Decimal	Binary	Char
FB0	SPBRGH	0x00	0	00000000	'.'
FB1	T3CON	0x00	0	00000000	'.'
FB2	TMR3	0x0000	0	00000000 00000000	'..'
FB2	TMR3L	0x00	0	00000000	'.'
FB3	TMR3H	0x00	0	00000000	'.'
FB4	CMCON	0x07	7	00000111	'.'
FB5	CVRCON	0x00	0	00000000	'.'
FB6	ECCP1AS	0x00	0	00000000	'.'
FB7	ECCP1DEL	0x00	0	00000000	'.'
FB8	BAUDCON	0x40	64	01000000	'@'
FBA	CCP2CON	0x00	0	00000000	'.'
FBB	CCPR2	0x0000	0	00000000 00000000	'..'
FBB	CCPR2L	0x00	0	00000000	'.'
FBC	CCPR2H	0x00	0	00000000	'.'
FBD	CCP1CON	0x00	0	00000000	'.'
FBE	CCPR1	0x0000	0	00000000 00000000	'..'
FBE	CCPR1L	0x00	0	00000000	'.'
FBF	CCPR1H	0x00	0	00000000	'.'
FC0	ADCON2	0x00	0	00000000	'.'
FC1	ADCON1	0x0F	15	00001111	'.'
FC2	ADCON0	0x00	0	00000000	'.'
FC3	ADRES	0x0000	0	00000000 00000000	'..'
FC3	ADRESL	0x00	0	00000000	'.'
FC4	ADRESH	0x00	0	00000000	'.'
FC5	SSPCON2	0x00	0	00000000	'.'
FC6	SSPCON1	0x00	0	00000000	'.'
FC7	SSPSTAT	0x00	0	00000000	'.'
FC8	SSPADD	0x00	0	00000000	'.'
FC9	SSPBUF	0x00	0	00000000	'.'
FCA	T2CON	0x00	0	00000000	'.'
FCB	PR2	0xFF	255	11111111	'ÿ'
FCC	TMR2	0x00	0	00000000	'.'
FCD	T1CON	0x00	0	00000000	'.'
FCE	TMR1	0x0000	0	00000000 00000000	'..'

SFRs - Memory View

Memory SFRs   Format Individual

Filter Address

Variables ×

Name	Type	Address	Value	Hexadecimal	Decimal
<Enter new watch>					
GIE	bit	0xFF2	0x01	0x01	1

Watches   Output

Assignmnet (Build, Load, ...)   debugger halted   29:1   INS