GUG/S/15/3745

P.T.O

B.E.(with Credits)-Regular-Semester 2012-Electronics & Communication Engineering & (Telecom. Eng) Sem V

ET504 Micro Controller and its Applications

Applications					
P. Pag	es:4				
Time :	Three Hours	Max.	Marks: 80		
N	otes: 1. All questions carry mark 2. Assume suitable d necessary. 3. Illustrate your answers w necessary with the help	lata _V herev	wherever er		
1. a)	Explain special function register mention below. i) DPTR ii) SBUF iii) IE iv) IP	er as	8		
b	Draw and explain Architecture Microcontroller.	of 80	51 8		
2. a)	OR Draw internal data memory & bank register & stack in brief.	explai	in 10		

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b) Explain PSW or (Program status word) 6 register of 8051 µc. **3.** a) Explain Alternate functions of I/o ports of 8 8051 μc. b) Explain mode 1 and mode 2 of counters 8 and timers of 8051 microcontroller. OR **4.** a) Draw format of SCON Register. Explain 8 different bits in it. b) What is interrupt? Explain interrupt 8 structure of 8051 µc. **5.** a) Explain addressing modes of 8051 8 Microcontroller with the help of example. b) Write an ALP to transfer Data from 8 memory block B1. to memory block B2. Assume two blocks are non overlapped. OR **6.** a) Write a program to get the x value from 8 P1 & send x² to P2 continuously using Look - up table.

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	b)	Classify the Instraction Sets in 8051 μ c & explain the function of following instructions. i) SWAP; ii) DAA; iii) DJNZ	8			
7.		Interface 4x4 matrix keyboard with 8051 Microcontroller ports. Draw diagram and flowchart.	16			
		OR				
8.	a)	Write an ALP to display digit 0 to 9 on common cathode seven segment display with some interval of delay.	8			
	b)	Write an ALP to interface stepper motor with $8051~\mu c.$ Assume motor rotate in clockwise direction.	8			
9.	a)	Draw and explain Architecture of AT 89C51 Microcontroller.	8			
	b)	Explain the concepts of flash memory of AT 89C51.	8			
OR						
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- **10.** a) Explain the following of AT 89C2051 **8**
 - 1) Alternate functions of Port 3,
 - 2) Oscillator characteristics.
 - b) Explain features of At 89C2051.

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