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-: HAND WRITTEN NOTES:-

OF

# ELECTRONICS & COMMUNICATION ENGINEERING

1

-: SUBJECT:-

# ANALOG ELECTRONICS

2

## Syllabus :-

(3)

- 1) Op-Amp
- 2) Linear Wave Shaping circuit <Taub>
- 3) Schmitt Trigger
- 4) Waveform Generator
  - Multivibrators
  - Bistable Multivibrator.
  - Monostable "
  - Astable " (Square Wave Generator)
  - Triangular Wave Generator

## 5) Diode Circuits

- Rectifiers & filters
- Precision Rectifiers
- Clipper & clampers
- Voltage Doubters.

## 6) Bipolar Junction Transistor.

- Transistor Biasing & Stabilisation
- Current Mirror Circuit
- Voltage Regulator

## - Power Amplifiers

### 9) 555 - Timer

## - Multivibrators

## Books

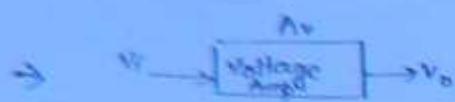
- Millman - Halkias - Yellow Pad.
- Pulse Digital & switching circuit  
- Millman & Taub.

## 8) Amplifiers -

- Low frequency Analysis of BJT
- High " " " "
- Multistage Amp.
- Feedback "
- Low frequency analysis of FET
- Oscillators (Sinusoidal)

## Operational Amplifier :-

(4)

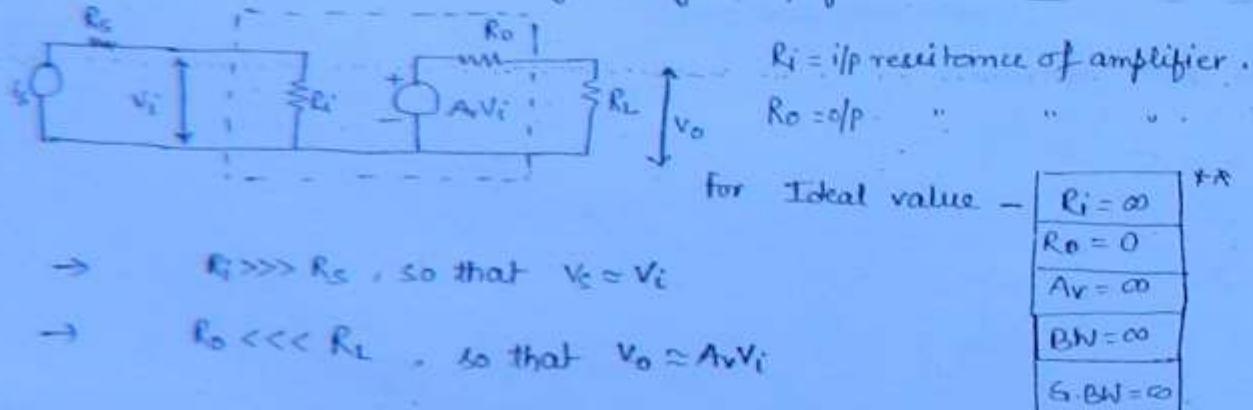


→  $A_v = \frac{V_o}{V_i} = \text{Gain}$

→ Op-Amp is a VCVS.

$$+ \text{ } A_v V_i$$

→ Equivalent circuit for any voltage amplifier-



\* To get  $A_v \rightarrow \infty$ , multistaging is done but the BW will  $\downarrow$ .

\* BW is defined as the freq. range for which gain is independent of frequency.

→ Gain of practical Op-Amp =  $10^6$ .

→ Op-Amp is a multistage amplifier.

→  $\boxed{\text{Gain} \times \text{BW} = \text{constant}}$ ; Ideally G.BW should be  $\infty$ .

\* BW cannot be  $\infty$  due to the presence of  $C_T$  &  $C_d$  in multistage amplifiers. (internal capacitance)

→ for practical Op-Amp; G.BW =  $10^6$  Hz.

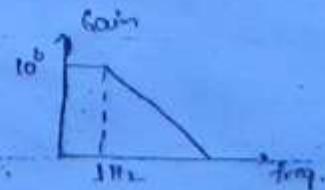
→ Max. possible BW =  $10^6$  Hz for gain = 1.

→ Negative feedback will ↓ the gain of the system and ↑ the O.P.I and hence ↑ the stability of the system.

(B)

### Op-Amp Inp. Points -

- (i) It is a monolithic IC or a semiconductor chip fabricated with VLSI by using epitaxial method.
- (ii) In epitaxial method, entire IC is fabricated on single crystal of Si.
- (iii) It is basically a voltage controlled device or voltage amplifier or VCVS.
- (iv) Popularly used Op-Amp is IC-741. For IC-741, maximum power supply is  $\pm 15V$ .
- (v) Op-Amp is versatile, predictable and economic system building block as small size, high reliability, reduced cost, low offset voltage & current and low power consumption.
- (vi) It is originally invented to execute the mathematical operations, Hence called op-amp.
- (vii) It is a direct coupled, high gain amplifier, i.e., open loop gain is very high, therefore frequency stability of the signal is less, and to compensate this, small amount of -ve feedback is added so that the gain is reduced & the frequency stability increases (since BW ↑).
- (viii) Op-Amps are generally operated under closed loop condition, i.e., by applying -ve feedback.
- (ix) In an Op-Amp, Gain  $\times$  BW = constant.



## Characteristic of Operational Amplifier

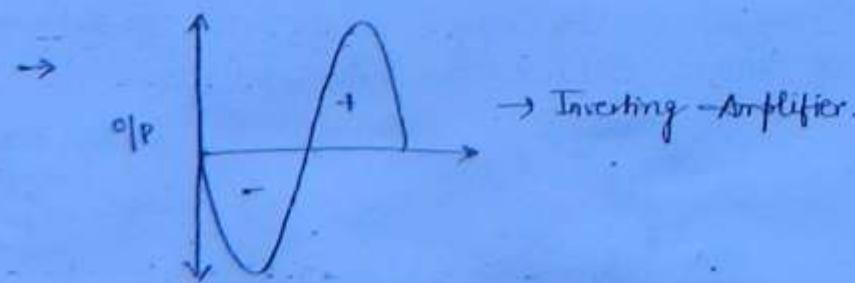
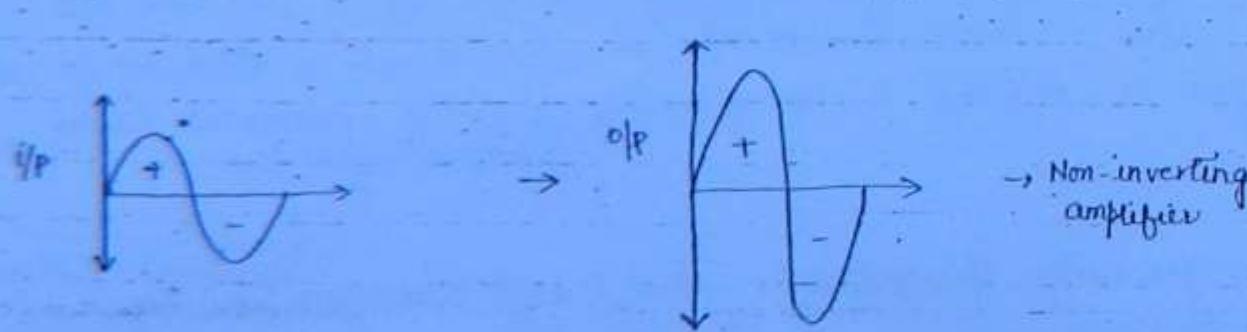
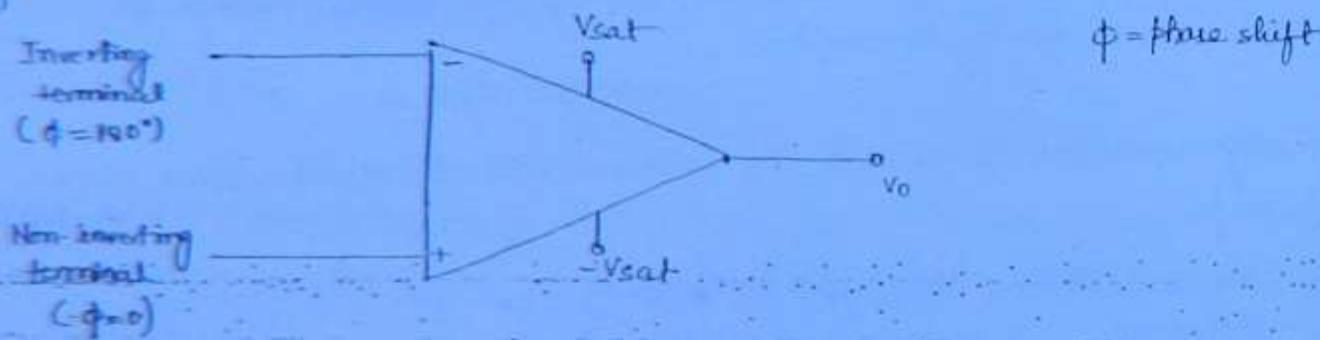
(6)

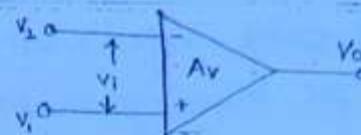
### Characteristic

	<u>Ideal</u>	<u>Practical</u>
- Voltage Gain, $A_v$	$\infty$	$10^6$
- Input Resistance, $R_i$	$\infty$	$1M\Omega$
- Output Resistance, $R_o$	0	$10 - 100 \Omega$
- G. BN	$\infty$	$10^6 \text{ Hz}$
- BW	$\infty$	$10^6 \text{ Hz (for Gain=1)}$
- CMRR	$\infty$	$10^6 \text{ or } 120 \text{ dB}$
- Slew Rate [SR]	$\infty$	$80 \text{ V}/\mu\text{sec.}$

→ It is also referred as Basic linear Integrated Circuit.

### Symbol :-





(7)

Case 1 :- When  $V_1 \neq 0, V_2 = 0$ , then  $V_0 > 0$

Case 2 :- When  $V_1 \neq 0, V_2 \neq 0$  and  $V_1 > V_2$ , then  $V_0 > 0$ .

Case 3 :- When  $V_1 = 0, V_2 \neq 0$ , then  $V_0 < 0$

Case 4 :- When  $V_1 \neq 0, V_2 \neq 0$  and  $V_2 > V_1$ , then  $V_0 < 0$ .

Representation of Gain -

$$|Av| = 10^6$$

Case 1 -



$$V_i' = V_1 - V_2 ; \quad V_0 = Av V_i' \Rightarrow \text{If we represent like this then} \\ = Av(V_1 - V_2) \quad Av = 10^6 \text{ i.e., } Av > 0$$

Case 2 -

$$V_i = V_2 - V_1$$

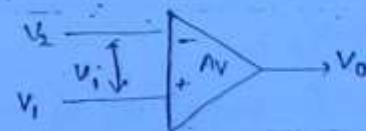
$$V_0 = Av V_i = Av(V_2 - V_1)$$



then for this representation,  $Av = -10^6$  i.e.,  $Av < 0$

When  $Av \rightarrow \infty$ :

$$Av \rightarrow \infty$$



$$V_i = V_1 - V_2$$

$$V_0 = \text{finite} \Rightarrow V_i = V_0/\infty = 0$$

$$\Rightarrow V_1 = V_2$$

→ There is finite o/p w/o any input.

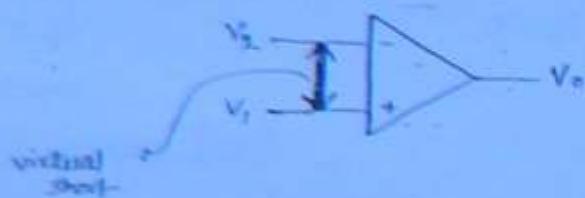
→ Now, if we attach any voltage source at  $V_1$ , the same will appear at  $V_2$  ( $\because V_1 = V_2$ ): but  $R_i = \infty$  (ideally) hence they should be 0.

but they are behaving as SC. This condition is called Virtual short, i.e., even though they are not physically short, they are behaving as short.

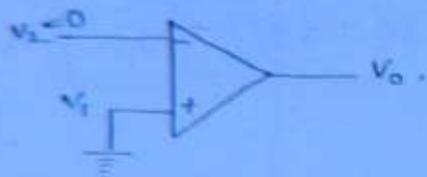


(8)

Symbol for virtual short -

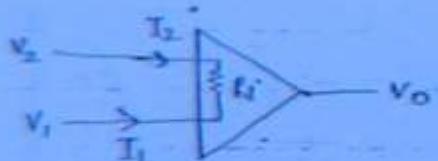


→ If we ground  $V_1$ , i.e., connect  $V_1 = 0$ , then  $V_2$  will also become 0V. This is called virtual ground. It is a special case of virtual short.



Virtual Ground Process

→ When  $R_i = \infty$  —



for  $R_i = \infty$ ,

$$I_1 = I_2 = 0.$$

→ Internal power consumption  $\approx 0$ .

Given  $V_o = 5V$  and  $A_v = 10^6$ ,  $R_i = 10^6 \Omega$ .

$$\Rightarrow V_i = \frac{V_o}{A_v} = \frac{5}{10^6} = 5\mu V \approx 0.$$

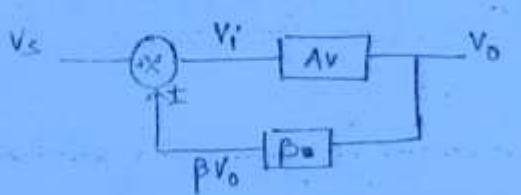
$$I_i = \frac{V_i}{R_i} = \frac{5}{10^6} = 5 \text{ pA} \approx 0$$

→ If the gain in the given problem is  $\uparrow$  (very high), then we can use the concept of virtual ground. It is an approximate concept.

(9)

$\rightarrow V_o = V_i \xrightarrow{Av} V_o \quad A_{OL} = Av \rightarrow$  open loop system.

$$A_{OL} = Av = \frac{V_o}{V_i} = \frac{V_o}{V_s} \quad ; \quad V_s = V_i$$



$$V_i = V_s \pm \beta V_o$$

$$V_i = \begin{cases} = V_s + \beta V_o & \rightarrow +ve \text{ feedback} \\ = V_s - \beta V_o & \rightarrow -ve \text{ feedback.} \end{cases}$$

$$V_o = (V_s \pm \beta V_o) \cdot Av$$

For +ve feedback -

$$\boxed{A_{OL} = \frac{V_o}{V_s} = \frac{Av}{1 - \beta Av} > Av} \rightarrow \text{closed loop gain for +ve feedback}$$

for -ve feedback -

$$\boxed{A_{OL} = \frac{V_o}{V_s} = \frac{-Av}{1 + \beta Av} < Av} \rightarrow \text{closed loop gain for -ve feedback}$$

→ +ve feedback is used in oscillators & -ve feedback is used in amplifier.

\* Op-Amp with -ve feedback

Op-Amp with +ve feedback.

$$\rightarrow |A_{OL}| \ll |A_{OL}|$$

$$\rightarrow |A_{OL}| \gg |A_{OL}|$$

we can assume  $A_{OL} = Av \rightarrow \infty$

we can assume  $A_{OL} = \infty$ , but we can't assume  $A_{OL} = \infty$ ,

∴ Virtual ground process is invalid.

∴ Virtual ground process is valid.

## Mode of Operation

(10)

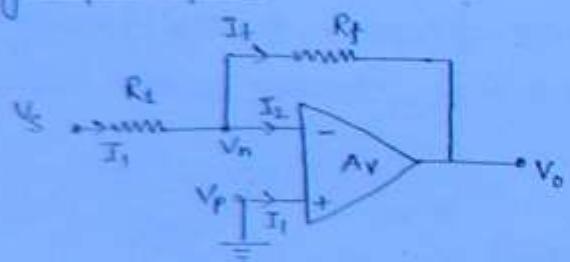
### i) Inverting Mode -

→ Phase shift =  $180^\circ$

ii) Non-Inverting Mode  $\Rightarrow (\phi = 0^\circ)$

iii) Differential Mode  $\Rightarrow V_o \propto [V_1 - V_2]$

### Inverting Op-Amp -



→ Negative feedback.

→  $|A_{OL}| \ll |A_{OL}| \Rightarrow$  we can apply V.G.P.  
 $\Rightarrow V_P = V_N = 0$ .

→  $\because R_I = \infty \Rightarrow I_2 = I_1 = 0$ .

→ Incoming current = outgoing current

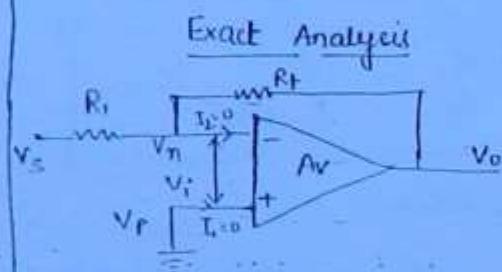
$$\Rightarrow I = I_F + I_2$$

$$\therefore \frac{V_S - V_N}{R_I} = \frac{V_N - V_O}{R_F} + 0$$

$$\therefore \frac{V_S}{R_I} = -\frac{V_O}{R_F}$$

$$\therefore A_{OL} = \frac{V_O}{V_S} = -\frac{R_F}{R_I}$$

$$\therefore \phi = 180^\circ$$



$$V_I = V_P = V_N ; \Rightarrow \left\{ A_V > 0 \right\}$$

$$V_O = A_V [V_P - V_N] \quad \text{--- (1)}$$

$$V_P = 0 \quad \text{--- (2)}$$

$$\begin{aligned} R_F &\frac{V_O}{V_N} \\ R_I &\frac{V_N - V_O}{R_F} + \frac{V_N - V_S}{R_I} = 0 \\ \Rightarrow V_N \left[ \frac{1}{R_I} + \frac{1}{R_F} \right] &= \frac{V_O}{R_F} + \frac{V_S}{R_I} \end{aligned}$$

$$\Rightarrow \boxed{V_N = \frac{V_O R_I}{R_I + R_F} + \frac{V_S R_F}{R_I + R_F}}$$

L  
continued

$$T_f = T_1 + T_2 + T_3$$

$$\frac{V_o - V_n}{R_f} = \frac{V_1 - V_n}{R_1} + \frac{V_2 - V_n}{R_2} + \frac{V_3 - V_n}{R_3}$$

//

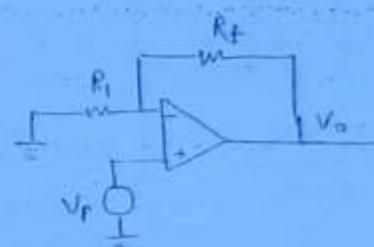
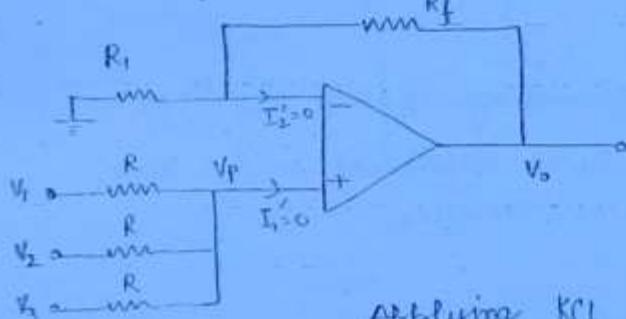
$$\Rightarrow V_o = -R_f \left[ \frac{V_1}{R_1} + \frac{V_2}{R_2} + \frac{V_3}{R_3} \right]$$

When  $R_f = R_1 = R_2 = R_3$  -

$$V_o = -[V_1 + V_2 + V_3]$$

$$\phi = 180^\circ$$

### Non-Inverting Summer



Applying KCL at non-inverting terminal -

$$\frac{V_p - V_1}{R_1} + \frac{V_p - V_2}{R_2} + \frac{V_p - V_3}{R_3} = 0$$

$$\Rightarrow V_p = \frac{V_1 + V_2 + V_3}{3}$$

Now,

$$\frac{V_o}{V_p} = 1 + \frac{R_f}{R_1}$$

$$\Rightarrow V_o = \left(1 + \frac{R_f}{R_1}\right) \left(\frac{V_1 + V_2 + V_3}{3}\right)$$

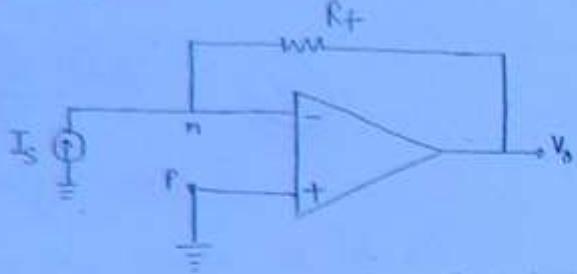
$$\phi = 0^\circ$$

If  $R_f = 2R_1$  ;

$$V_o = [V_1 + V_2 + V_3]$$

## Current to Voltage Converter :-

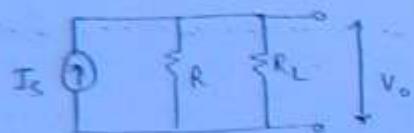
(12)



$$\rightarrow V_P = V_N = 0$$

$$\rightarrow \frac{V_N - V_o}{R_f} = I_S \Rightarrow V_o = -I_S \cdot R_f$$

$\rightarrow \left\{ \begin{array}{l} V_o \text{ is independent of } R_L \\ \text{hence it is a converter} \end{array} \right.$

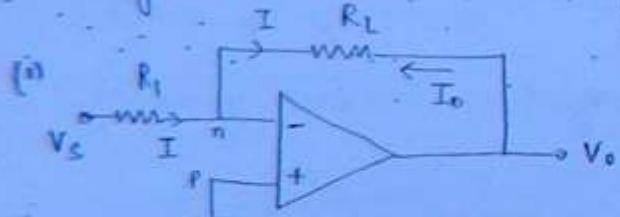


$$V_o = \frac{I_S \cdot R \cdot R_L}{R + R_L}$$

= but this is not converting  $I_S$  into a voltage source because  $V_o$  is dependent on  $R_L$ . Hence, given circuit is not a converter.

## Voltage to Current Converter :-

(a) Floating load :-

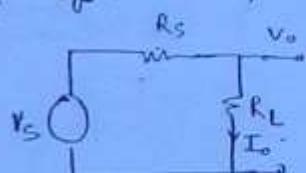


$$I = \frac{V_S - 0}{R_1} \Rightarrow I = \frac{V_S}{R_1}$$

$$I_o = -I = -\frac{V_S}{R_1}$$

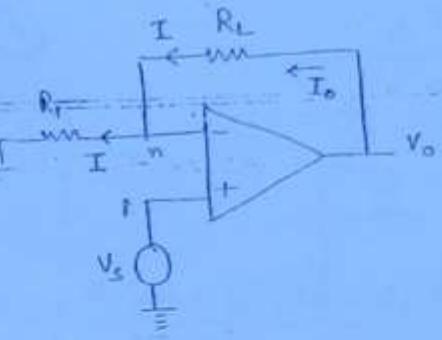
$\rightarrow I_o = \text{output current independent of } R_L$

{ It is standard convention to take load current  $I_o$  in direction away from output voltage  $V_o$ .  
i.e.,  $I_o$  leaving from  $V_o$ .



$$\frac{V_o}{R_L} = I_o = \frac{V_S}{R_S + R_L}$$

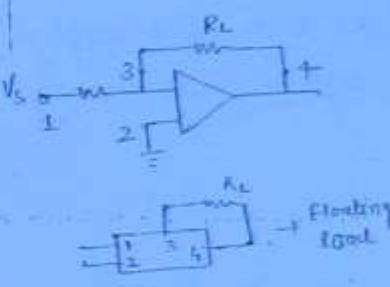
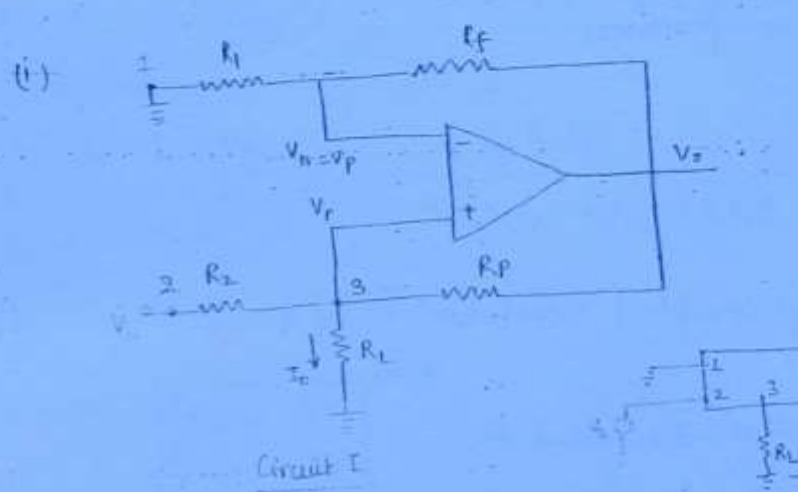
$I_o$  depends on  $R_L$ , hence not a converter.



$$\begin{aligned} V_n &= V_s - \textcircled{1} \\ \frac{V_s - 0}{R_1} &= I \\ \Rightarrow I_o &= I = \frac{V_s}{R_1} \end{aligned}$$

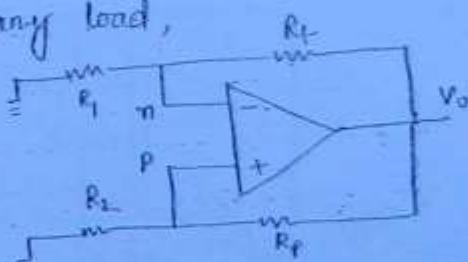
(13)

(b) Grounded Load :-



In above ckt, as no null sum -ve feedback is present for stability system should have -ve feedback and hence -ve feedback should be more than +ve feedback.

→ Without any load,



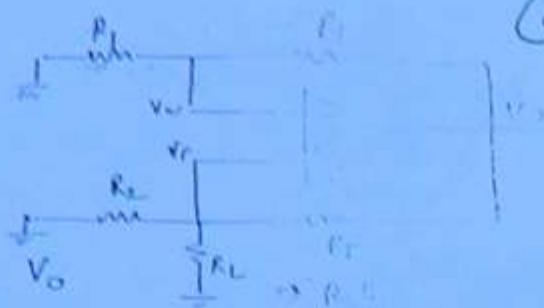
$$V_n = \frac{R_1}{R_1 + R_f} V_o$$

$$V_p = \frac{R_2}{R_2 + R_P} V_o$$

for stability,  $V_n > V_p$  (-ve feedback more than +ve feedback)

$$\Rightarrow \frac{R_1}{R_1 + R_f} \geq \frac{R_2}{R_2 + R_P}$$

(14)

After  $R_L$ ,

$$R_2' = R_2 \parallel R_L < R_2$$

$$V_F' = \frac{R_2'}{R_2 + R_F} V_o = \frac{1}{1 + R_F/R_2'} V_o$$

Now  $R_2' < R_2$  and hence  $V_F' < V_F$   $\Rightarrow$  +ve feedback  $\therefore$  which is favourable for stability.  
Hence, even after applying  $R_L$ , if original condition  $V_F > V_o$  then the system will remain in +ve feedback.

Now, from circuit 1 -

$$I_o = \frac{V_F}{R_L} \quad \text{--- (1)}$$

$$V_F = V_S \quad \text{--- (2)} \quad \left\{ \text{by concept of virtual short} \right\}$$

Applying KCL at inverting terminal -

$$\frac{V_F - V_S}{R_2} + \frac{V_F - 0}{R_L} + \frac{V_F - V_o}{R_F} = 0$$

$$\Rightarrow V_F \left[ \frac{1}{R_2} + \frac{1}{R_L} + \frac{1}{R_F} \right] - \frac{V_S}{R_2} - \frac{V_o}{R_F} = 0 \quad \text{--- (3)}$$

KCL at non-inverting terminal -

$$\frac{V_F - V_S}{R_2} + \frac{V_F - 0}{R_L} + \frac{V_F - V_o}{R_F} = 0$$

$$\Rightarrow V_F \left[ \frac{1}{R_2} + \frac{1}{R_L} + \frac{1}{R_F} \right] - \frac{V_S}{R_2} - \frac{V_o}{R_F} = 0 \quad \text{--- (4)}$$

from (3) &amp; (4) -

$$\Rightarrow V_F \left[ \frac{1}{R_2} + \frac{1}{R_L} + \frac{1}{R_F} \right] - \frac{1}{R_F} \left[ 1 + \frac{R_F}{R_1} \right] V_P = \frac{V_S}{R_2}$$

$$\Rightarrow V_p \left[ \frac{1}{R_2} + \frac{1}{R_L} + \frac{1}{R_P} - \frac{1}{R_F} - \frac{R_F}{R_1 R_P} \right] = \frac{V_S}{R_2} \quad (B)$$

$$\Rightarrow V_p \left[ \frac{R_1 R_P R_L + R_1 R_2 R_P - R_F R_2 R_L}{R_1 R_P R_2 R_L} \right] = \frac{V_S}{R_2}$$

$$\frac{V_F}{R_L} \Rightarrow V_p = \frac{V_S R_1 R_P R_L}{R_L [R_1 R_P - R_2 R_F] + R_1 R_2 R_P}$$

$$I_o = \frac{V_p}{R_L} = \frac{V_S R_1 R_P}{R_L [R_1 R_P - R_2 R_F] + R_1 R_2 R_P}$$

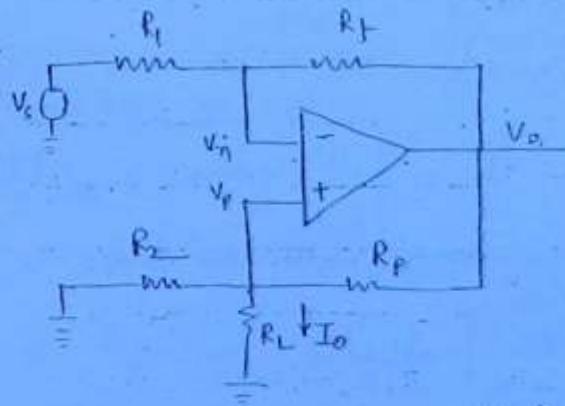
If  $R_1 R_P = R_2 R_F$

or  $\frac{R_P}{R_2} = \frac{R_F}{R_1}$  \*check  $\rightarrow$  Balanced Bridge condition

then,

$$I_o = \frac{V_S}{R_2}$$
 \*check

(ii)



Prove that if  $\frac{R_F}{R_1} = \frac{R_P}{R_2}$

then  $I_o = -\frac{V_S}{R_2}$

Applying KCL at  $V_p$  —

$$V_p \left[ \frac{1}{R_2} + \frac{1}{R_L} \right] = \frac{V_o}{R_P} \quad (1)$$

Sol<sup>n</sup>:  $V_n = V_p$  ;

$$\frac{V_p}{R_L} = I_o \quad (2)$$

Applying KCL at  $V_1$  -

$$\frac{V_p - V_1}{R_1} + \frac{V_p - V_o}{R_f} = 0 \quad \text{--- (3)}$$

$$\Rightarrow V_p \left[ \frac{1}{R_1} + \frac{1}{R_f} \right] = \frac{V_s}{R_1} + \frac{V_o}{R_f}$$

Putting  $V_o$  from eqn (1) -

$$\Rightarrow V_p \left[ \frac{R_f + R_1}{R_f R_1} \right] = \frac{V_s}{R_1} + \frac{R_p}{R_f} \cdot V_p \left[ \frac{1}{R_2} + \frac{1}{R_f} + \frac{1}{R_p} \right]$$

On simplifying -

$$V_p = \frac{-R_2 R_L R_p R_f V_s}{R_1 R_2 R_p^2 + R_1 R_p (R_p R_1 - R_2 R_f)} \quad \text{--- (4)}$$

From circuit -

$$I_o = \frac{V_p}{R_L} = \frac{-R_2 R_p R_f \cdot V_s}{R_1 R_2 R_p^2 + R_1 R_p (R_p R_1 - R_2 R_f)}$$

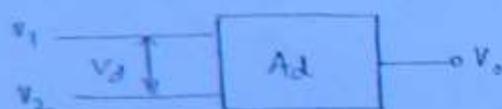
If  $R_p R_1 = R_2 R_f$  -

$$\text{or } \frac{R_f}{R_1} = \frac{R_p}{R_2} \Rightarrow \frac{1}{R_2} = \frac{R_1 R_p}{R_1 R_f}$$

$$I_o = -\frac{V_s \cdot R_f}{R_1 R_p} = -\frac{V_s}{R_2}$$

## Differential Amplifier

Ideal

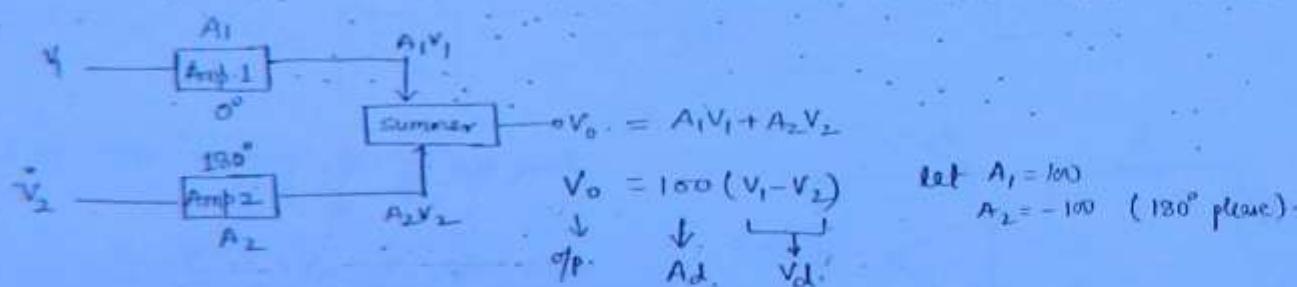


$$V_d = A_d \cdot V_d$$

$$V_d = V_1 - V_2 = \text{difference voltage}$$

$A_d$  = Difference Gain.

Practical



→ To write the above eqn,  $A_1$  and  $A_2$  should be equal with  $180^\circ$  phase diff. But it is not possible to have identical amplifiers.

$$\text{eq. } V_o = 100V_1 - 90V_2 = 90(V_1 - V_2) + 10V_1 \text{ Noise.}$$

→ If there is some noise signal is present at both terminal and ideally it should cancel out but for unidentical amplifiers -

$$V_o = 100(V_1 + V_n) - 90(V_1 + V_n)$$

$$= 90(V_1 - V_2) + (10V_1 + 10V_n) \text{ Noise.}$$

(12)

For Practical Amplifier,

$$V_o = A_d V_d + A_c V_c \quad \text{--- (1)}$$

where  $V_d = V_1 - V_2 \quad \text{--- (2)}$

$$V_c = \frac{V_1 + V_2}{2} = \text{common mode signal} \quad \text{--- (3)}$$

$A_c$  = common mode gain

→ Ideally  $A_c \rightarrow 0$

Practically  $A_c \rightarrow$  very small

→ Common Mode Rejection Ratio -

e.g. for Diff Amp 1  $\rightarrow A_c = 10, A_d = 1000 \rightarrow \frac{A_d}{A_c} = 100$

" " 2  $\rightarrow A_c = 1, A_d = 10 \rightarrow \frac{A_d}{A_c} = 10$

→ Amp 1 is better than Amp 2.

$$\boxed{\text{CMRR} = \beta = \frac{|A_d|}{|A_c|}}$$

ideally  $\text{CMRR} = \infty$

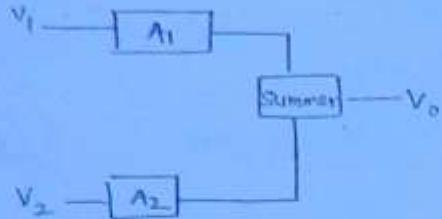
Practically  $\text{CMRR} = 10^6 = 120 \text{ dB}$

$$\boxed{[\text{CMRR}]_{\text{dB}} = 20 \log \frac{|A_d|}{|A_c|}}$$

→ CMRR is figure of merit of practical op-amp.

from diagram -

$$V_o = A_1 V_1 + A_2 V_2 \quad \text{--- (4)}$$



(18)

Adding (2) and (3) -

$$2V_c + V_d = 2V_1$$

$$\Rightarrow V_1 = V_c + \frac{V_d}{2} \quad \text{--- (5)}$$

Subtracting (2) from (3) -

$$2V_c - V_d = 2V_2$$

$$\Rightarrow V_2 = V_c - \frac{V_d}{2} \quad \text{--- (6)}$$

But

Putting 5 & 6 in (4) -

$$V_o = A_1 \left( V_c + \frac{V_d}{2} \right) + A_2 \left( V_c - \frac{V_d}{2} \right)$$

$$V_o = \left[ \frac{A_1 - A_2}{2} \right] V_d + (A_1 + A_2) \cdot V_c \quad \text{--- (7)}$$

Comparing (4) and (7) -

$A_d = \frac{A_1 - A_2}{2}$	$A_c = A_1 + A_2$
-----------------------------	-------------------

Here  $A_2 = -ve$  due to  $180^\circ$  phase diff  
and hence  $A_d > A_c$ .

2nd Method :-

Calculation of  $A_c$  -

$$\text{Put } V_1 = V_2 = V_s \Rightarrow V_c = V_s$$

$$\because V_d = 0, \Rightarrow V_o = A_d V_d + A_c V_c$$

$$\Rightarrow V_o = 0 + A_c \cdot V_s$$

$$\Rightarrow A_c = \frac{V_o}{V_s}$$

Calculation of  $A_d$  -

$$\text{Put } V_1 = V_s/2 \text{ and } V_2 = -V_s/2$$

$$\Rightarrow V_d = V_s \text{ and } V_c = 0$$

$$\Rightarrow A_d = \frac{V_o}{V_s'}$$

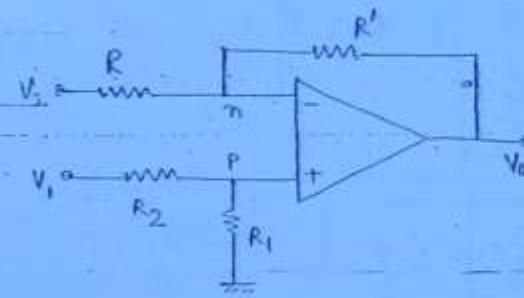
Ques: The circuit shown is a differential amplifier using an ideal op-amp

(a) Find the opf voltage  $V_o$ .

(b) Find CMRR.

(c) Show that if  $\text{CMRR} = \infty$  if  $\frac{R'}{R} = \frac{R_1}{R_2}$ .

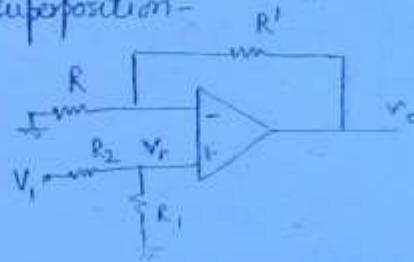
(19)



Soln: (a) By applying superposition-

(i) Taking  $V_2 = 0$ -

$$V_P = \frac{V_1 R_1}{R_1 + R_2}$$

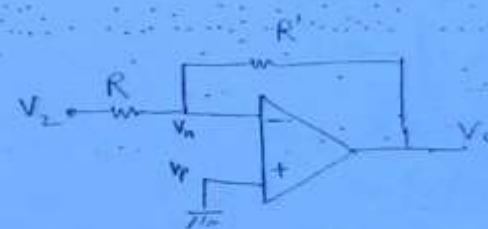


$$V_{01} = \left[ 1 + \frac{R'}{R} \right] V_P$$

$$V_{01} = \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] V_1$$

(ii) Taking  $V_1 = 0$ -

$$V_{02} = -\frac{R'}{R} V_2$$



$$\therefore V_0 = V_{01} + V_{02} = \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] V_1 - \frac{R'}{R} V_2$$

$$(b) V_0 = A_D V_d + A_C V_c = A_1 V_1 + A_2 V_2$$

$$A_C = A_1 + A_2 \Rightarrow A_C = \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] - \frac{R'}{R}$$

$$A_D = \frac{A_1 - A_2}{2} \Rightarrow A_D = \frac{1}{2} \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] + \frac{R'}{2R}$$

$$CMRR = \frac{1}{2} \left[ \frac{\left\{ 1 + \frac{R'}{R} \right\} \left\{ \frac{R_1}{R_1 + R_2} \right\} + \frac{R'}{R}}{\left\{ 1 + \frac{R'}{R} \right\} \left\{ \frac{R_1}{R_1 + R_2} \right\} - \frac{R'}{R}} \right]$$

(20)

(c) when  $\frac{R'}{R} = \frac{R_1}{R_2}$  —

$$CMRR = \frac{1}{2} \left[ \frac{\left\{ 1 + \frac{R_1}{R_2} \right\} \left\{ \frac{R_1}{R_1 + R_2} \right\} + \frac{R_1}{R_2}}{\left\{ 1 + \frac{R_1}{R_2} \right\} \left\{ \frac{R_1}{R_1 + R_2} \right\} - \frac{R_1}{R_2}} \right]$$

$$\Rightarrow CMRR = \frac{1}{2} \left[ \left\{ \frac{2R_1}{R_2} \right\} \div \left\{ 0 \right\} \right]$$

$$\Rightarrow CMRR = \infty$$

$$\therefore CMRR = \infty \Rightarrow |A_C| = C \quad \text{or} \quad |A_{C1}| = |A_{C2}|$$

$$\Rightarrow \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] - \frac{R'}{R} = 0$$

$$\Rightarrow \frac{R_1}{R_1 + R_2} + \frac{R'}{R} \left[ \frac{R_1}{R_1 + R_2} - 1 \right] = 0$$

$$\Rightarrow \frac{R_1}{R_1 + R_2} - \frac{R_2 \cdot R'}{R(R_1 + R_2)} = 0$$

$$\Rightarrow \frac{R_1}{R_2} = \frac{R'}{R} \quad \text{Hence Proved}$$

3rd method :-

$$\text{Put } V_1 = V_2 = V_C \Rightarrow V_A = 0, V_C = V_S$$

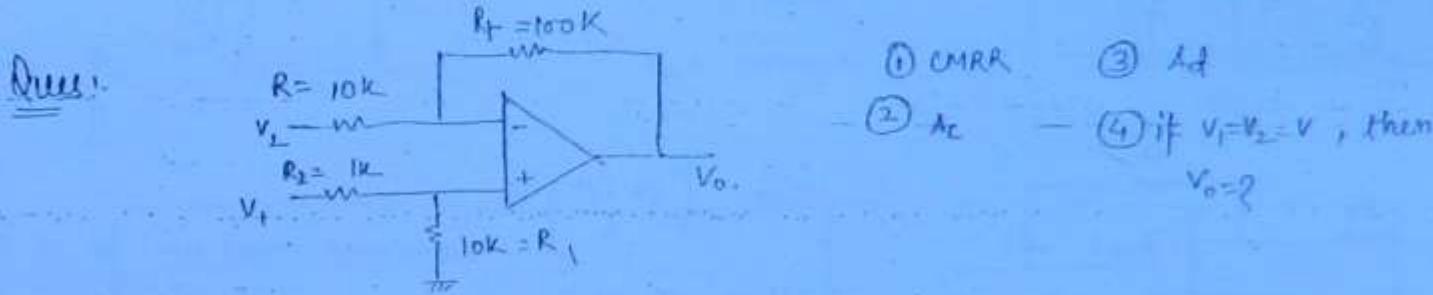
$$A_C = \frac{V_O}{V_C} \quad \text{--- (1)}$$

$$V_O = \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] V_S - \frac{R'}{R} V_S \Rightarrow A_C = \frac{V_O}{V_S} = \left[ 1 + \frac{R'}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] - \frac{R'}{R}$$

For  $A_d$ ,  $V_1 = \frac{V_s'}{2}$ ,  $V_2 = -\frac{V_s'}{2} \Rightarrow A_d = \frac{V_o}{V_s'} \text{ and } A_c = 0.$

$$\therefore V_o = \left[ 1 + \frac{R_f}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] \cdot \frac{V_s'}{2} + \frac{R_f}{R} \frac{V_c}{2}. \quad (2)$$

$$A_d = \frac{V_o}{V_s} = \frac{1}{2} \left[ \left[ 1 + \frac{R_f}{R} \right] \left[ \frac{R_1}{R_1 + R_2} \right] + \frac{R_f}{R} \right].$$



Soln: → for objective, first check  $\frac{R_f}{R} = \frac{R_1}{R_2}$

$$\Rightarrow \frac{100}{10} = \frac{10}{1} \Rightarrow \text{Since ratio is equal} \Rightarrow \text{CMRR} = \infty$$

$$\Rightarrow A_c = 0$$

①  $\rightarrow V_o = A_d V_d + A_c V_c$

③  $A_d : ?$

$$\Rightarrow V_o = A_d (V_1 - V_2)$$

$$\because A_c = 0 \Rightarrow |A_1| = |A_2|$$

$$\therefore V_o = 0$$

$$V_o = A_1 V_1 + A_2 V_2$$

$$\Rightarrow V_o = A_1 [V_1 - V_2]$$

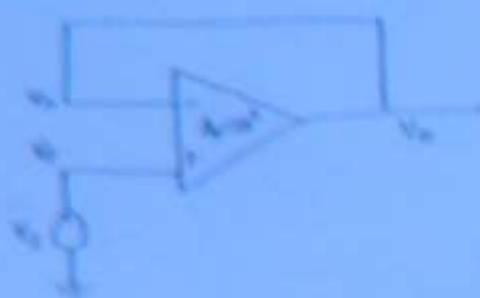
from  
previous ques

$$\boxed{A_2 = \frac{-R_f}{R}} = -10 \Rightarrow A_1 = 10$$

$$\therefore A_d = \frac{A_1 - A_2}{2} = \frac{10 - (-10)}{2} = 10$$

28 August 2019

## Voltage follower -



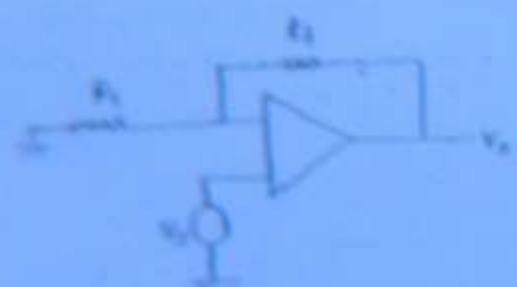
$$V_f = V_{in} + V_b$$

22

$$V_B = V_{in} + V_b$$

→ Output follows the input hence called voltage follower.

$$\rightarrow A_{v_f} = \frac{V_{in}}{V_f} = 1 \quad \boxed{\beta = 0}$$



$$V_B = \left( 1 + \frac{R_f}{R_i} \right) V_{in}$$

when  $R_f \ll R_i$ ,

$$\boxed{V_B = V_{in}} = \text{Add as voltage follower}$$

→ Voltage follower is voltage series feedback.

→ For voltage series feedback,  $R_f \gg R_o$ :

→ For voltage follower,  $\boxed{R_f \approx 10^6 \Omega}$  and  $\boxed{R_o = 0 \Omega = 0}$

→ Because of no feedback,  $|A_{v,f}| = 1 \rightarrow \boxed{R_f \cdot R_o = 10^6 \Omega \cdot 1 \Omega = 1 \text{ MHz}}$

## Application -

- It is used in designing of simple and half circuit.

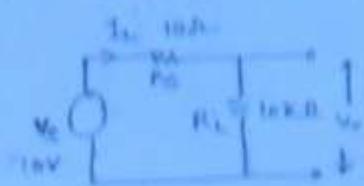
- first order biquad, notch filter

- as a buffer, i.e., impedance matching device. Has high

- resistance and low resistance.

→ Application as a buffer -

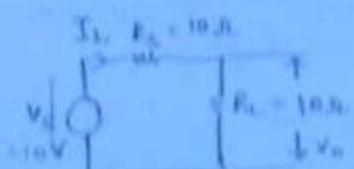
(23)



$$V_O = \frac{I_L}{R_F + R_L} V_S \approx V_S \quad \because R_L \gg R_F$$

$$I_L = \frac{V_S}{R_F + R_L} \leq \frac{15}{10+10} \text{ mA} \quad \therefore R_L \downarrow \text{ and } I_L \uparrow$$

There is low loading effect.



$$V_O = \frac{10}{10+10} V_S = 15V \quad \therefore R_L \downarrow \rightarrow I_L \uparrow$$

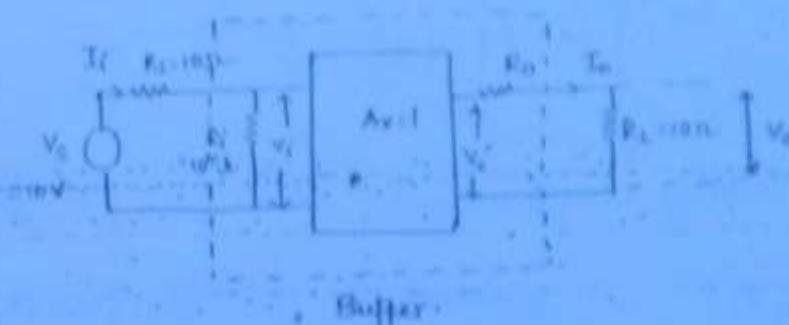
$$I_L = \text{constant} \quad \text{There is high loading effect.}$$

Low Load Resistance :-

→  $R_L = \text{very low}$  → Loading effect  
 $I_L = \text{very high}$  → high.

Low Load :-

- Loading effect
- $I_L = \text{fixed}$
- $R_L = \text{high}$



$$\because R_L \gg R_F \Rightarrow V_O \approx V_S$$

$$\Delta V = 1 \quad V_O' \approx V_O \approx V_S \quad \text{and} \quad \because R_F = 0 \quad \therefore V_O = V_O' = V_S$$

$$I_L = \frac{10}{10+10} = 1A \quad \text{very small.}$$

$$I_B = \frac{10}{10} = 1A \quad \rightarrow \text{this extra current is given by buffer.}$$

Other Buffers :-

- Voltage follower by using op-amp → VCVS → Buffer follower using BJT
- Emitter follower → BJT → CCVS → Common collector using  $\frac{1}{2}$  (common drain - VCVS - config).

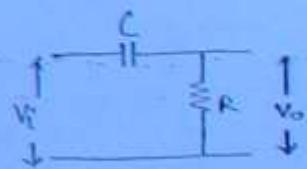
## Linear Wave Shaping circuit :-

(24)

- High Pass RC  $\rightarrow$  Differentiator.
- Low Pass RC  $\rightarrow$  Integrator.

The process whereby form of a non-sinusoidal signal is altered by transmission through a linear filter is called linear wave shaping.

### i) High Pass RC circuit -



$$\text{Gain } A = \frac{V_o}{V_i}$$

$$V_o = \frac{R}{R + 1/Cs} \cdot V_i$$

$$\Rightarrow \frac{V_o}{V_i} = \frac{1}{1 + \frac{1}{RCS}} \Rightarrow A = \frac{1}{1 + j \frac{1}{\omega RC}} \quad \text{--- (1)}$$

$$\rightarrow |A| = \frac{1}{\sqrt{1 + 1/\omega^2 R^2 C^2}}, \quad \boxed{\phi \text{ shift} = -\tan^{-1}\left(\frac{1}{\omega RC}\right)} \quad \text{--- (3)}$$

$\rightarrow$  Since  $\phi$  shift = +ve, it is called leading circuit.

$\rightarrow$  From eqn (2)  $\rightarrow$  as  $\omega \uparrow$ , gain  $\uparrow$

$\rightarrow$  At  $\omega = 0$ ,  $|A| = 0$

$\rightarrow$  At  $\omega = \infty$ ,  $|A| = 1 = A_{\max}$

$\rightarrow$  At  $\omega = \omega_L$ ,  $|A| = \frac{A_{\max}}{\sqrt{2}}$ ;  $\omega_L$  = cut-off frequency, freq. at which gain reduces to  $\frac{1}{\sqrt{2}}$  of max. value (3dB).

$$\Rightarrow \frac{1}{\sqrt{2}} = \frac{1}{\sqrt{1 + 1/\omega_L^2 R^2 C^2}}$$

$$\Rightarrow \boxed{\omega_L = 2\pi f_L = 1/RC} \quad \text{--- (4)} \quad \text{--- } \omega_L = 3\text{dB frequency}$$

from (1) and (4) -

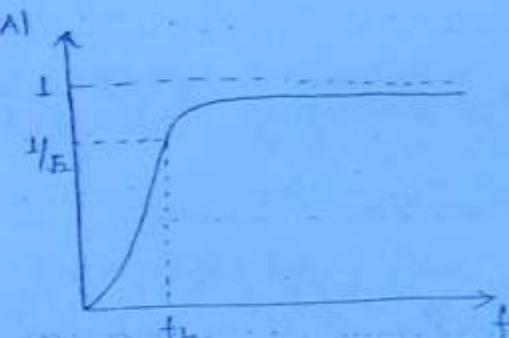
$$A = \frac{1}{1 - j/\omega_0 RC} \Rightarrow A = \frac{1}{1 - j\omega_0/\omega_0} = \frac{1}{1 - j\tau_L/f}$$

(25)

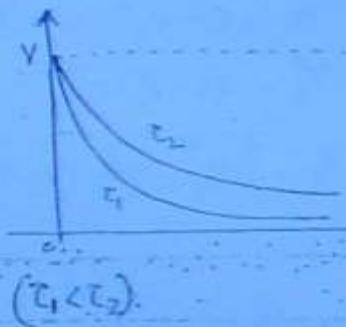
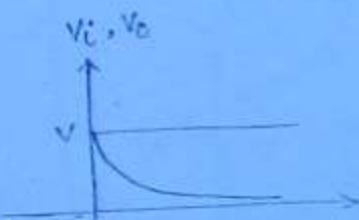
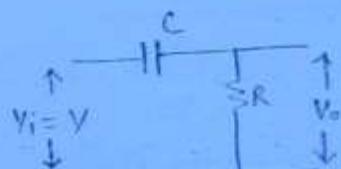
$$|A| = \frac{1}{\sqrt{1 + (\tau_L/f)^2}} \quad \text{where } f = \text{instantaneous frequency}$$

$$\begin{aligned} \Rightarrow f=0 &; |A|=0 \\ \tau_L = f_L &; |A|=1/\sqrt{2} \\ f=\infty &; |A|=1 \end{aligned}$$

$$\Rightarrow \boxed{\text{B.W.} = \infty}$$



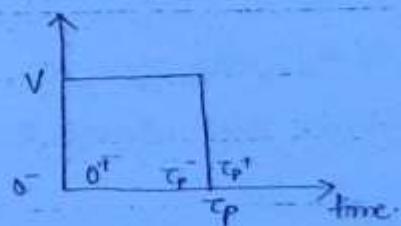
Step Response :-



$$\Rightarrow V_o = V_R = V_i e^{-t/RC} \rightarrow \text{exponentially}$$

$$\Rightarrow V_C = V_i [1 - e^{-t/RC}] \rightarrow \text{exponentially}$$

Pulse Response



$$\text{Case 1} \quad \frac{T_p}{RC} \ll 1$$

$T_p$  = Pulse width.

$$\text{Case 2} \quad \frac{T_p}{RC} \gg 1$$

$$\Rightarrow V_i(0^-) = 0, \quad V_i(0^+) = V$$

$$V_i(\tau_p^-) = 0V, \quad V_i(\tau_p^+) = 0$$

(26)

$$\rightarrow V_C(0^-) = 0 = V_C(0^+), \quad \Rightarrow V_R(0^+) = V$$

$$V_C = V [1 - e^{-t/\tau_{RC}}]; \quad V_R = V e^{-t/\tau_{RC}}$$

Case 1  $RC \gg \tau_p$

$\rightarrow V_R$  will start discharging till  $0 < t < \tau_p$

$$\text{At } t = \tau_p^-, \quad -V_o = V e^{-\tau_p/RC} = V'$$

$$\therefore V_C = V - V' = V (1 - e^{-\tau_p/RC}) = V_C(\tau_p^-)$$

At  $t = \tau_p^+$  -

$$V_C(\tau_p^+) = V_C(\tau_p^-) = V (1 - e^{-\tau_p/RC}) = (V - V')$$

$\Rightarrow V_i = 0$  at  $\tau_p(0^+)$

$$V_R = 0 - V_C' = -V (1 - e^{-\tau_p/RC}).$$

$$V_C = (V - V') e^{-\tau_p/RC}$$

$$V_o = -V_C = -(V - V') e^{-\tau_p/RC}, \quad (t' = t - \tau_p)$$

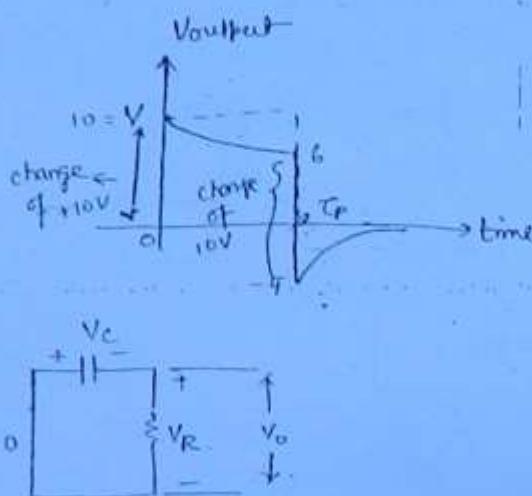
$\rightarrow$  When there is sudden change in i/p,  
the same change will occur at the o/p.

$\rightarrow$  If the ~~output~~ input is maintaining some constant level, then output will tend towards zero. (between 0 to  $\tau_p$ ) and from ( $\tau_p$  to  $\infty$ ), the o/p is tending towards 0.

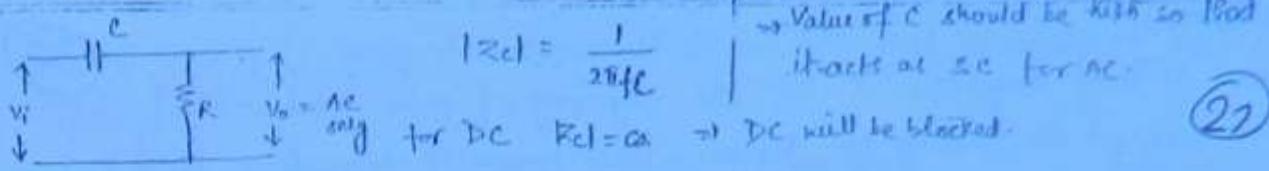
$\rightarrow$  Area of the pulse =  $V \tau_p = +ve = \text{average DC level.}$

$$\text{For Output, } A_+ \text{ area} = \int_0^{\tau_p} V e^{-t/\tau_{RC}} dt, \quad A_- = \int_{\tau_p}^{\infty} (V - V') e^{-|t-\tau_p|/\tau_{RC}} dt$$

$$A_+ = A_- = (\text{as charging} = \text{discharging})$$



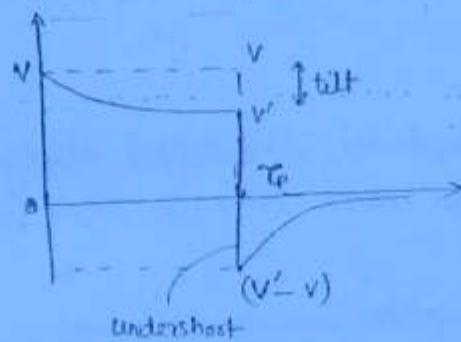
Time	$V_o$
$t < 0$	0
$t = 0^+$	$V$
$0 < t < \tau_p$	$V_o = V e^{-t/\tau_{RC}}$
$t = \tau_p^-$	$V' = V e^{-\tau_p/RC}$
$t = \tau_p^+$	$-(V - V')$
$t > \tau_p$	$-(V - V') e^{- t-\tau_p /\tau_{RC}}$
	$t' = t - \tau_p$



$V_i = A_{\text{c}} + \text{DC}$        $\Rightarrow \text{DC level or avg level of o/p} = 0$   
 $\Rightarrow \text{Total o/p area} = 0$   
 $\Rightarrow A^+ = A^-$

Area gives  
 avg value of signal

$\rightarrow$  Avg. level of o/p in high pass RC signal is always 0 irrespective of the avg. level of i/p.



Tilt  $\rightarrow$  at the top of pulse  
 Undershoot  $\rightarrow$  at the end of pulse

Case 2:  $R_C \ll \tau_p$

$$V_C(0^+) = 0 = V_C(t_1) \Rightarrow V_o = V$$

$$V_R = V e^{-t/R_C} \text{ for } 0 < t < t_1$$

At  $t = t_1$ ,

$$V_C = V \Rightarrow V_R = 0$$

At  $t = \tau_p^{+}$ ,

$$V_C = V$$

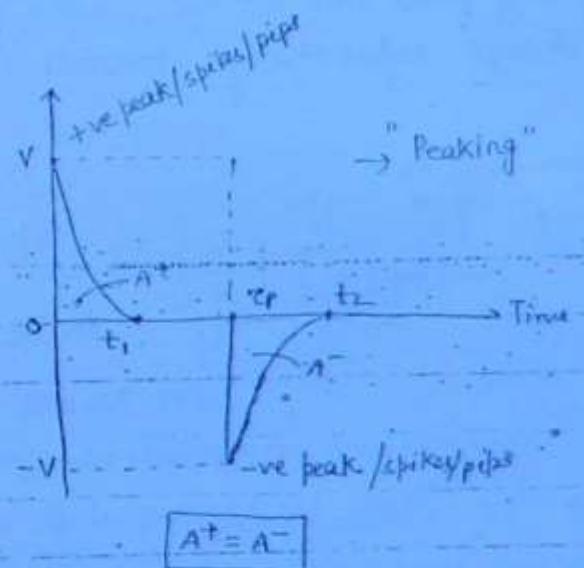
At  $t = \tau_p^+$ ,  $V_C = V$ , hence

$$V_i = 0 \Rightarrow V_R = -V$$

Now, a capacitor will start discharging,  $V_C = V e^{-t/R_C}$ , hence

$$V_R = V_o = -V e^{-t/R_C} \Rightarrow (t' = t - \tau_p)$$

At  $t = t_2$ ,  $V_C = 0$  and  $\Rightarrow V_R = V_o = 0$ .



$$(t - \tau_p)$$

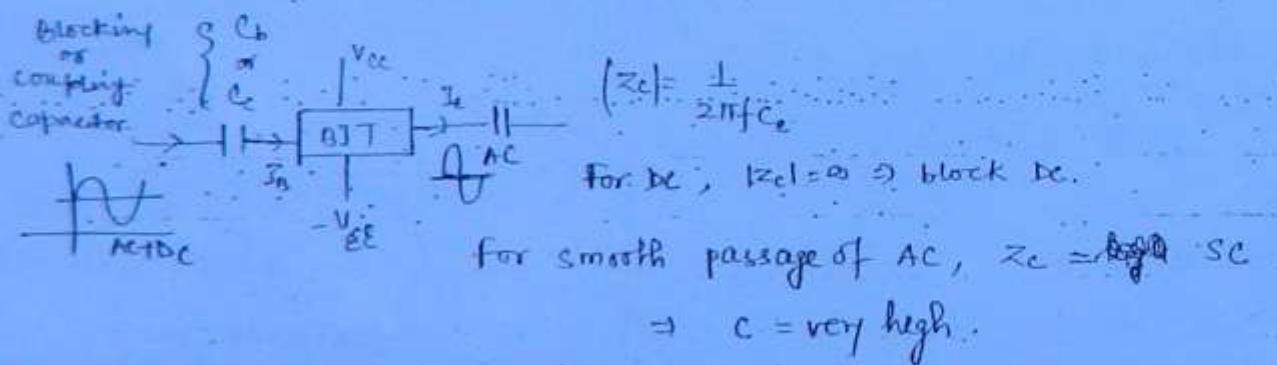
$$-t/R_C$$

## Conclusion-

- Positive spike of amplitude 'V' at the beginning of pulse and -ve of same size at the ending of pulse. This process of converting pulse into spike by means of a high pass RC circuit of short time constant is called Peaking.

For HPF -

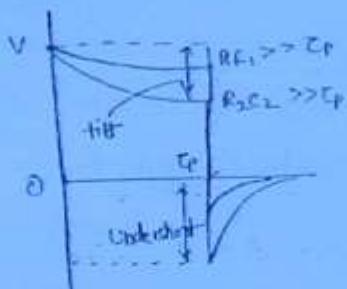
- Average level o/p is always zero, independent of average level of i/p, i.e.,  $A_+ = A_-$
- When input changes discontinuously by amount 'V', the output changes discontinuously by an equal amount and in same direction.
- During any finite time interval, when input maintains a constant level, o/p decays exponentially towards '0' voltage.



But if AC  $\rightarrow 20\text{Hz} - 20\text{kHz}$ ,  
the low freq. components will not pass smoothly as for low frequencies  
 $|Z_C| = \text{high}$ . Hence there will be distortion in the o/p. Whereas,  
the high freq. component will be received accurately at the o/p.

→ This capacitor provides DC isolation to the BJT. The DC is blocked as it will interfere with the biasing condition of the BJT.

→ For  $RC \ggg \tau_p$ , pulse distortion will ↓ as tilt and undershoot. (L29)

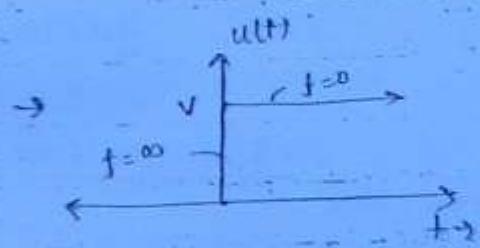
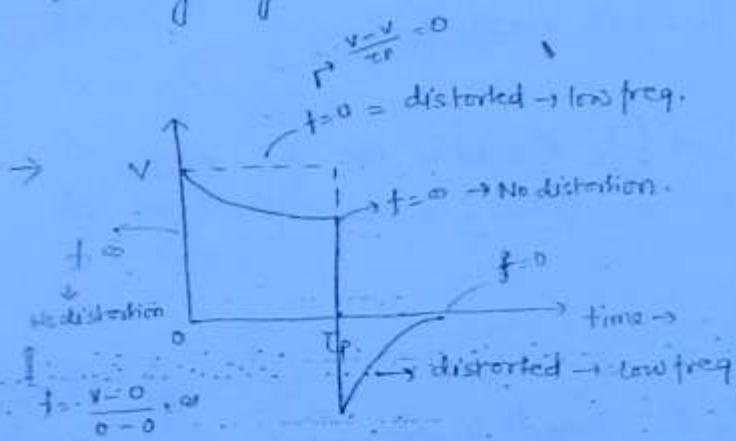


$R_1 C_1 > R_2 C_2$ . If we keep ↑  $RC$ , distortion will keep ↓. Tilt and undershoot are distortions.

→ C should be high for minimum distortion. (same as previous discussion).

$C_e$  →  $R_i$  should also be high.  
= Similar to HPF

→ For better coupling or minimum distortion,  $R_i$  and  $C_e$ , both should be very high.



→ It has both max. as well as min. freq. signals and hence it is preferred as test signal.

### Tilt or Sage :-

→  $RC \ggg \tau_p$ .

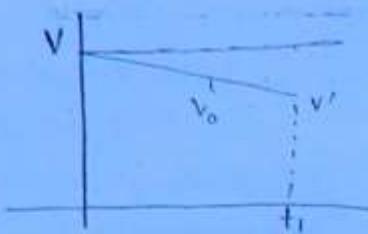
→  $V_o = V e^{-t/RC}$

$$\chi = t/RC = \text{very small.}$$

$$e^{-\chi} = 1 - \chi + \frac{\chi^2}{2!} - \frac{\chi^3}{3!} \dots$$

$$\therefore e^{-\chi} = 1 - \chi$$

$$\Rightarrow V_0 = V \left[ 1 - \frac{t}{RC} \right] \quad \text{---(1)}$$



$$\text{Tilt at } t=t_1 = V - V' \quad \text{---(2)}$$

$$\% \text{ tilt} = P\% = \frac{V - V'}{V} \times 100 \quad \text{---(3)}$$

$$V' = V \left[ 1 - \frac{t_1}{RC} \right]. \Rightarrow V - V' = \frac{V t_1}{RC}$$

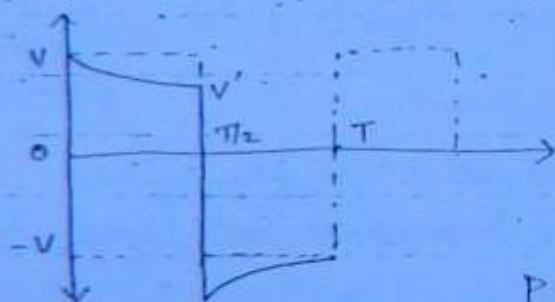
$$\Rightarrow \frac{V - V'}{V} = \frac{t_1}{RC}$$

$$\Rightarrow P\% = \frac{t_1}{RC} \times 100\% \quad \rightarrow (\text{When } RC \text{ is } \uparrow, \text{ tilt will } \downarrow.)$$

Since,  $2\pi f_L = \omega_L = 1/RC$  — {  $t_1 = 3\text{dB}$  cut-off frequency }.

$$\boxed{P\% = 2\pi f_L t_1 \times 100\%} \quad \rightarrow (f_L \text{ should be low for smaller tilt})$$

= Tilt for symmetrical Square Wave —



$$f = \frac{1}{T} = \text{freq. of sq. wave}$$

$$t_1 = T/2 = \frac{1}{2f}$$

$$P\% = 2\pi f_L \left( \frac{1}{2f} \right) \times 100\%$$

$$\Rightarrow \boxed{P\% = \pi \left( \frac{f_L}{f} \right) \times 100\%}$$

$\hookrightarrow$  (for high frequency signals,  
 $P\% = \text{low.}$ ) and vice versa.

16<sup>th</sup> August, 2012.

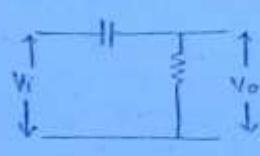
### High pass RC circuit as a differentiator :-

(31)

- When time constant,  $RC$ , is very very small as compared to time period of input signal, the circuit is called differentiator.

Criteria for good differentiator -

- Ideally,  $RC = 0$ .



$$\frac{V_o}{V_i} = |A| = \frac{1}{1 - j\omega RC}$$

$$|A| = \frac{1}{\sqrt{1 + \omega^2 R^2 C^2}} ; \quad \phi = \tan^{-1} \left( \frac{1}{\omega RC} \right)$$

for  $V_i = V_{ms} \sin \omega t$

$$V_o = |A| \cdot V_{ms} \sin(\omega t + \phi)$$

$$= \frac{V_m}{\sqrt{1 + \frac{1}{\omega^2 R^2 C^2}}} \cdot \sin(\omega t + \phi)$$

for ideal differentiator,  $\phi = 90^\circ \Rightarrow \omega RC = 0$ . (which is practically not possible).

	$\phi$	$\omega RC$
Ideal Diff.	$90^\circ$	0
Best Diff.	$89.4^\circ$	0.01
Good diff	$84.3^\circ$	0.1

for Best diff -

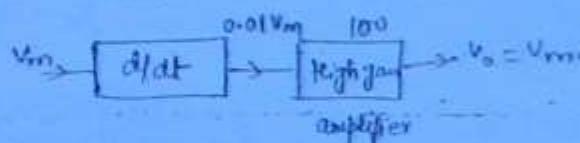
$$\omega RC = 0.01$$

$$\Rightarrow |A| = \frac{1}{\sqrt{1 + \omega^2 R^2 C^2}} \quad \text{but } \frac{V_m}{\omega^2 R^2 C^2} \ggg 1$$

$\rightarrow V_o = (0.01)V_m \sin(\omega t + 89.4^\circ) \rightarrow$  Amplitude is very small effect differentiation.

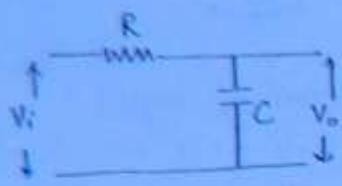
$\rightarrow$  To get amplitude,  $V_m$ , we have to follow it with a high gain amplifier

$\rightarrow$  A high pass RC differentiator is always followed by a high gain amplifier



## Low Pass RC circuit :-

(32)



$$V_o = \frac{1/Cs}{R + 1/Cs} \cdot V_i \Rightarrow A = \frac{1}{1 + RCS}$$

$$\Rightarrow A = \frac{1}{1 + j\omega RC}$$

$$\rightarrow |A| = \frac{1}{\sqrt{1 + \omega^2 R^2 C^2}} ; \quad \phi = -\tan^{-1}(\omega RC) \rightarrow (\text{lagging})$$

$\phi = -ve$  circuit

$$\rightarrow \text{for } \omega = 0 ; \quad |A| = 1$$

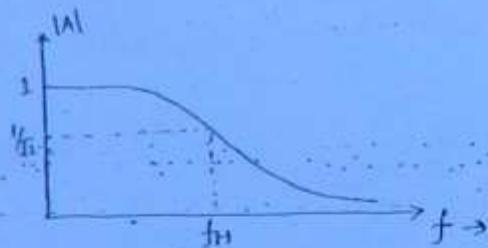
$$\rightarrow \omega = \infty ; \quad |A| = 0$$

$\omega \uparrow ; \quad |A| \downarrow \rightarrow \text{Low pass filter}$

$$\rightarrow \text{At } \omega = \omega_H ; \quad |A| = \frac{|A_{max}|}{\sqrt{2}} = \frac{1}{\sqrt{2}}$$

$$\Rightarrow \omega_H = \frac{1}{RC} \quad \text{or} \quad 2\pi f_H = \frac{1}{RC} \Rightarrow f_H = \frac{1}{2\pi RC} \rightarrow \text{3dB cutoff frequency}$$

$$\rightarrow A = \frac{1}{1 + j(f/f_H)}$$



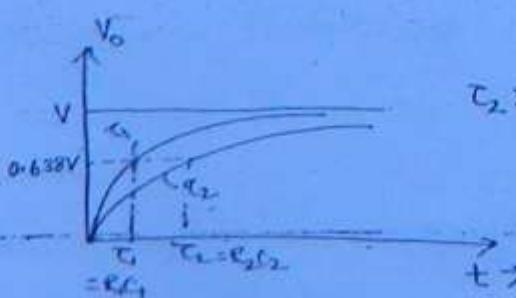
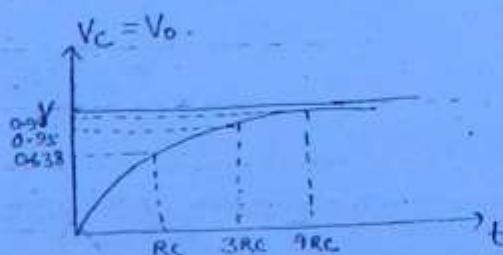
## Step Response :-

$$V_c(0^-) = V_c(0^+) = 0$$

$$\text{At } t=0^+, \quad V_c(0^+) = 0$$

$$\text{At } t=\infty, \quad V_c = V$$

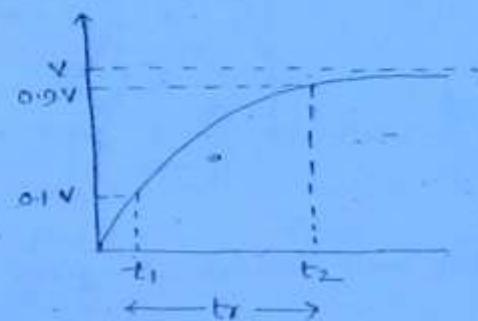
$$V_c = V [1 - e^{-t/RC}]$$



(33)

④ Rise Time :-

It is the time taken by the signal to rise from 10% to 90% of its final value.



$$\text{At } t = t_1, \quad V_0 = 0.1V \Rightarrow 0.1V = V [1 - e^{-t_1 RC}]$$

$$\Rightarrow t_1 \approx 0.1RC$$

$$\text{At } t = t_2, \quad V_0 = 0.9V \Rightarrow 0.9V = V [1 - e^{-t_2 RC}]$$

$$\Rightarrow t_2 \approx 2.3RC$$

$$\boxed{\text{Rise time} = t_2 - t_1 = 2.2RC}$$

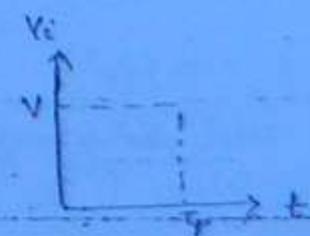
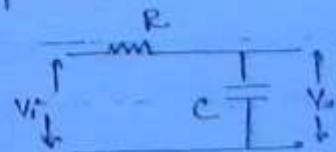
$$\rightarrow \omega_{HP} = 2\pi f_H = \frac{1}{RC}$$

$$\therefore t_R = \frac{2.2}{2\pi f_H} \Rightarrow \boxed{t_R = \frac{0.35}{f_H}}$$

→ Rise time of the circuit should be low for fast response.

→  $f_H$  should be high

⑤ Pulse Response :-



$$V_c(0^-) = V_c(0^+) = 0V$$

$$\therefore V_c(\infty) = 0$$

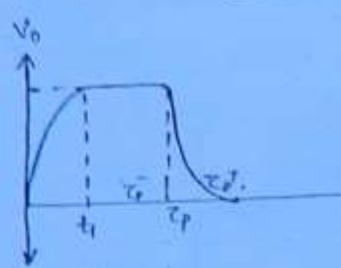
Case I :  $\frac{t_p}{RC} \gg 1$  (or)  $RC \ll \tau_p$

When  $RC$  small, rate of charging is very fast.

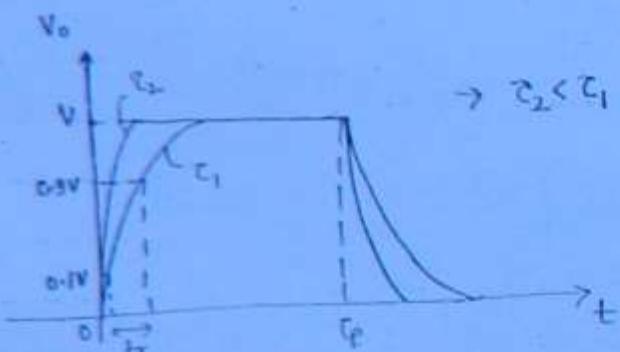
$$V_c = V \text{ and will remain } V \text{ till } t = \tau_p$$

$V_c(\tau_p^-) = V_c(\tau_p^+) = V$ , and now the capacitor will start discharging through  $R$ .

$$V_0 = V_c = V e^{-(t-\tau_p)/RC} \quad \text{for } t > \tau_p$$



ideally (practically very small)



$\rightarrow \tau_2 < \tau_1$ , when  $RC = 0$ , the capacitor will charge instantly and pulse shape will be preserved if

$$f_H \geq \frac{1}{\tau_p}$$

$$\rightarrow t_r \geq \frac{1}{\tau_p} \rightarrow \frac{0.35}{t_r} \geq \frac{1}{\tau_p}$$

$$\Rightarrow t_r \leq 0.35 \tau_p$$

Case II :  $\frac{\tau_p}{RC} \ll 1$  or  $RC \gg \tau_p$

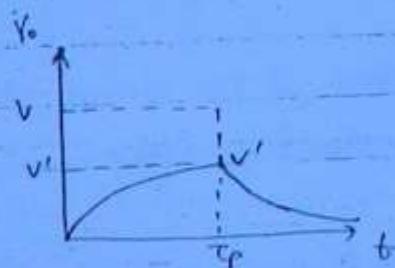
$RC$  is high, hence rate of charging is very slow.

$$\text{At } t = \tau_p,$$

$$V' = V \left[ 1 - e^{-\tau_p/RC} \right] = V_c(\tau_p^+)$$

After  $t = \tau_p$ ,  $V_c$  will start discharging,

$$V_0 = V' e^{-(t-\tau_p)/RC} \quad \text{for } t > \tau_p$$



When  $RC$  is very high, then

$$\alpha = \frac{t}{RC} \ll 1$$

$$\therefore e^{-\alpha} = 1 - \alpha + \frac{\alpha^2}{2!} - \frac{\alpha^3}{3!} \dots \Rightarrow e^{-\alpha} = [1 - \alpha]$$

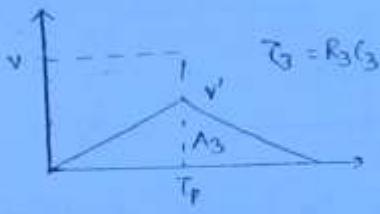
$$\therefore V_C = V \left[ 1 - \frac{t}{RC} \right] \Rightarrow V_C = \frac{V \cdot t}{RC} \quad (\text{linear equation}).$$

for discharging -

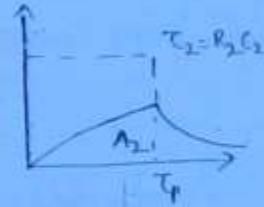
$$V_O = V' e^{-(t-t_0)/RC} = V' e^{-t'/RC}, \text{ for } RC \ggg t'$$

$$V_O = V' \left[ 1 - \frac{t'}{RC} \right] \quad (\text{linear eqn})$$

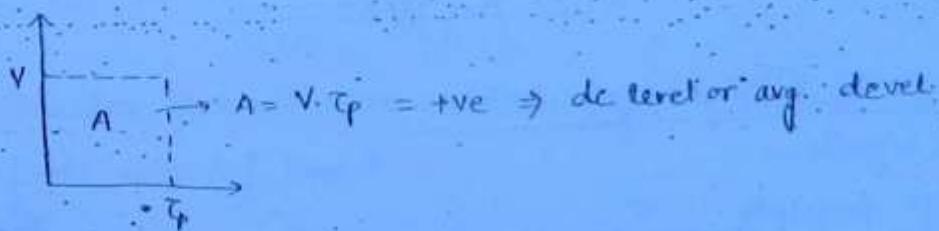
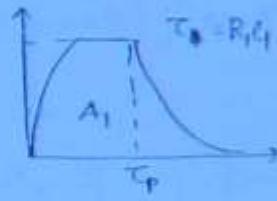
\* RC low pass circuit will act as an integrator when  $RC$  is very high.



$$T_3 > T_2 > T_1$$



$$A = A_1 = A_2 = A_3$$



\* for low pass RC circuit, dc level of output is always equal to dc level of input dc level.

### Low Pass RC as an integrator

⇒ When the time constant is very large as compared to time period of ip signal, the circuit is called integrator.

for  $V_i = V_m \sin \omega t$  —

$$V_o = |A| \cdot V_m \sin(\omega t + \phi)$$

$$V_o = \frac{V_m}{\sqrt{1+\omega^2 R^2 C^2}} \cdot \sin(\omega t + \phi)$$

$\phi$

$wRC$

(36)

Ideal

$-90^\circ$

$\infty$  (practically  
not possible)

Best

$-89.4^\circ$

$RC > 151T$

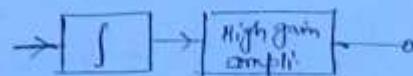
for Best  $\int$  —

$$-\tan^{-1}(wRC) = -89.4^\circ$$

$$\Rightarrow wRC = \tan(89.4^\circ)$$

$$\Rightarrow RC \times \frac{2\pi}{T} = \tan(89.4^\circ)$$

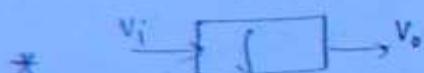
$$\Rightarrow RC = 151T$$



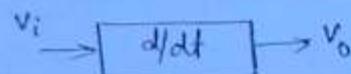
$$V_o = \frac{V_m}{wRC} \sin(\omega t - 89.4^\circ) \quad \text{for } wRC > 1$$

In this case also, amplitude is very low.

→ The op-amp is followed by a high gain amplifier.



$$\text{replace } \int \rightarrow \frac{1}{s} = \frac{1}{j2\pi f}$$



$$\text{Laplace } \frac{d}{dt} = s = j2\pi f$$

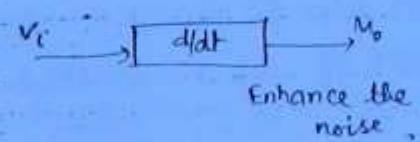
$\Rightarrow f \uparrow \text{ then } IN \downarrow$

$\Rightarrow f \uparrow \text{ then } IAT \uparrow$

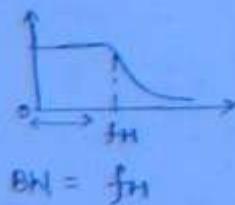
→ Integrator is preferred over differentiator because —

i) for spurious signals/noise signals —

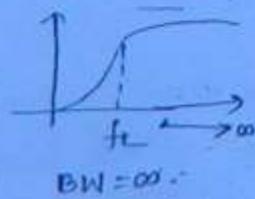
They are of high frequency.



2) for LPF,



for HPF,



→ Due to  $\infty$  BW, some unwanted signals will also come above the req. signal band.

Noised BW

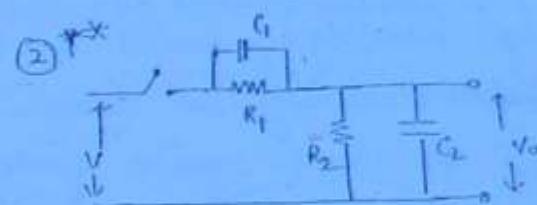
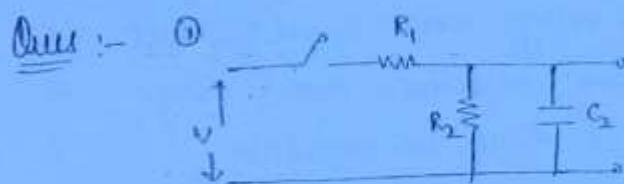
→ LPF is placed at the last stage of multi-stage amplifier so as to prevent the noise to reach the output.

(37)

→ Integrator is almost preferable over differentiator for following reasons-

① Since gain of  $\int$   $\downarrow$  with  $f$ , whereas gain of  $d/dt \uparrow$  with  $f$   
therefore, it is easier to stabilize  $\int$  than  $d/dt$  w.r.t spurious oscillations (high freq. noise).

② As a result of its limited BW, an  $\int$  is less sensitive to offset noise voltage than a  $d/dt$ .

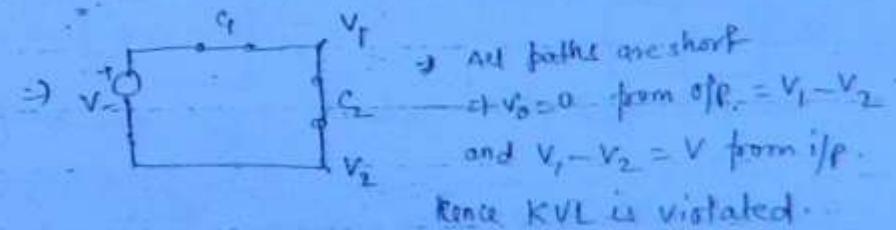


Switch is closed at  $t=0$ .  $V_0(0^+)$  = ?

Soln, ① At  $V_C(0^-) = 0 = V_C(0^+)$   $\Rightarrow V_0 = 0$  at  $t = 0^+$ . {  $I(0^+) = V/R_1 = \text{finite}$  }  
 $V_0(\infty) = VR_2/R_1 + R_2$

② Capacitor does not allow sudden change in voltage but only for finite value of current

~~Wrong result~~  
 $V_{C_1}(0^-) = 0 = V_{C_1}(0^+)$ .  
 $V_{C_2}(0^-) = 0 = V_{C_2}(0^+)$



KVL is violated.

$V_{C_2} = V_0 = 0$  is a wrong result.

~~Correct result~~

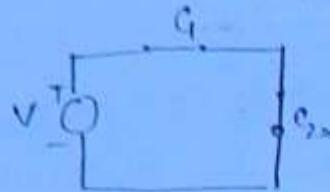
Due to  $V_{C_2} = 0$ , the current in the circuit will be  
for  $V_0 = 0$  &  $V_C = 0$ ,  $V/I_0 = I(0^+) = \infty$ . for  $I(0^+) = \infty$ , there will be  
a finite voltage in Capacitors,

charge

$$\int_{0^-}^{0^+} I(t) dt = q(0^+) = \text{finite}$$

{  $I(t)$  will behave as impulsive current & hence can't allow sudden change of  $V(t)$

$$q(0^+) = C_{eq} \cdot V = \frac{C_1 C_2}{C_1 + C_2} V.$$



(38)

$$V_o(0^+) = V_{C_2}(0^+) = \frac{q(0^+)}{C_2}$$

$$V_o(0^+) = \frac{C_1}{C_1 + C_2} V; \quad V_1(0^+) = \frac{q(0^+)}{C_1} = \frac{C_2 V}{C_1 + C_2}$$

$\Rightarrow$  voltages will be distributed b/w  $C_1$  and  $C_2$ .

At  $t=0$ ,  $C_1$  &  $C_2$  will be o.e.

$$V_o = \frac{R_2}{R_1 + R_2} V.$$

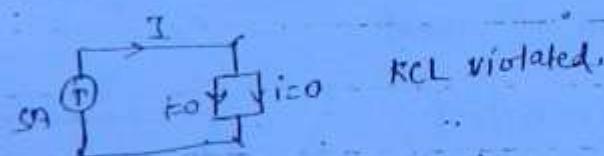
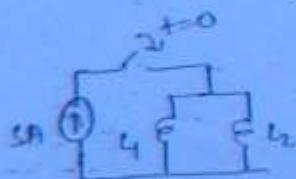
Conclusion :-

at  $t=0$

$\rightarrow$  As input changes abruptly by amount  $V$ , then voltage across  $C_1$  and  $C_2$  must also change discontinuously but voltage across capacitor cannot change instantaneously if current remains finite and hence an impulsive current must flow in the circuit.

$\rightarrow$  an infinite current exists for  $t=0^+$ , so that a finite charge  $q(0^+)$  is delivered to each capacitor and capacitor allows sudden change of voltage.

Ques.



$$Z_p = \infty (\because i=0)$$

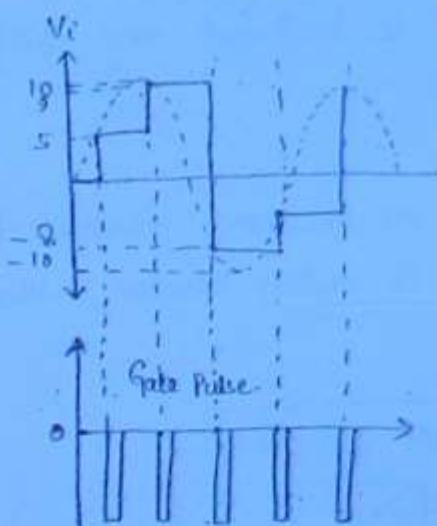
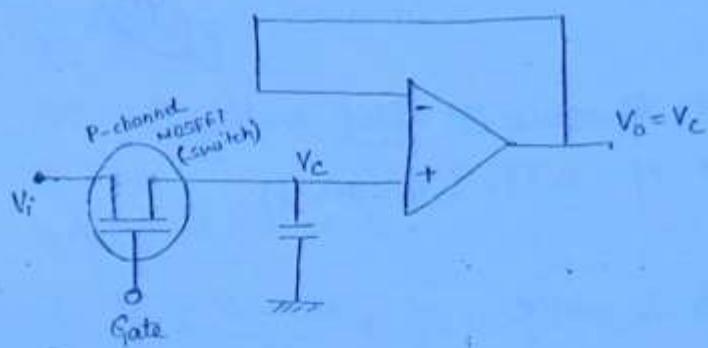
$\Rightarrow V = 5 \times \infty = \infty$  = impulsive voltage.

Hence it will allow sudden change in current.

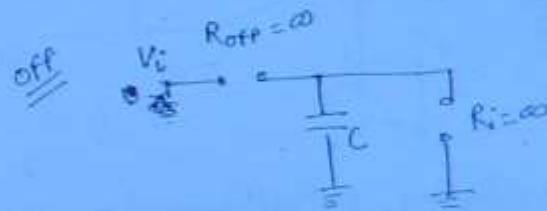
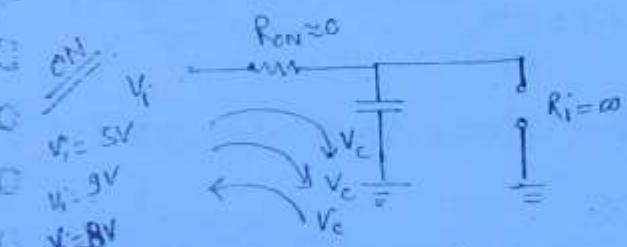
$$I_{L_2} = \frac{5i}{4+i_2}, \quad I_{L_1} = \frac{5i_2}{4+i_2}$$

## Sample and Hold Circuit :-

(34)



Switch	$R_{\text{switch}}$	Time constant	Remark
ON	$R_{\text{on}} \approx 0$	$R_{\text{on}}C \approx 0$	Capacitor will suddenly charge upto instantaneous value of $V_i$ .
OFF	$R_{\text{off}} \approx \infty$	$R_{\text{off}}C \approx \infty$	C will hold the value of $V_i$ .

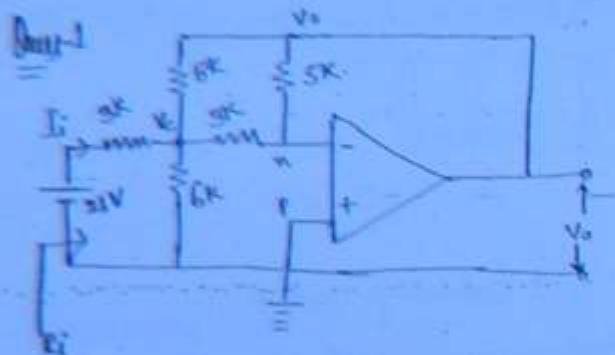


- \* When  $V_i$  is < the value held by capacitor.
- \* In that case C will discharge or C will charge towards value of  $V_i$  which is smaller than its previous value.
- \* -ve triggered P-MOSFET is used as trigger because -ve triggered pulses will not generate spikes/noise.
- The op-amp is used (voltage follower), because it will make the  $R_i = \infty$  which will help capacitor to hold the value, and C will not discharge through it. (If  $R_i$  = some finite value, the hold value will discharge through it).

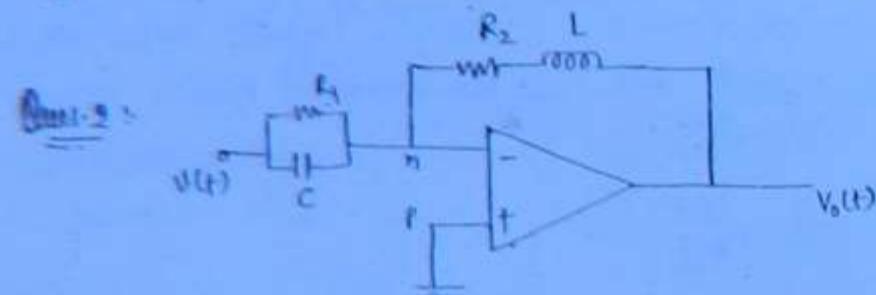
- Also, MOSFET is preferred over BJT as a switch because of its zero offset voltage.

(40)

- MOSFET makes an excellent chopper (switch) because its offset voltage ( $\approx 5\mu V$ ) is much smaller than that of BJT. ( $V_T = 0.5V$ ).



Find  $V_C$ ,  $I_f$  and  $R_i$



$$-V_o = \frac{R_2}{R_1} V + \left[ R_2 C + \frac{L}{R_1} \right] \frac{dV}{dt} + LC \frac{d^2V}{dt^2}$$

Sol(2) :- By applying virtual ground,  $V_p = V_n = 0$

Now applying KCL at  $V_n$  -

$$\frac{V_n - V}{Z_{R_1C}} + \frac{V_n - V_o}{Z_{R_2L}} = 0$$

$$\Rightarrow \frac{V}{Z_{R_1C}} + \frac{V_o}{Z_{R_2L}} = 0$$

$$\Rightarrow \frac{V}{\frac{R_1 + R_1Cs}{R_1 + R_1Cs}} + \frac{V_o}{R_2 + Ls} = 0$$

$$\Rightarrow \frac{V(1 + R_1Cs)}{R_1} + \frac{V_o}{R_2 + Ls} = 0$$

$$\Rightarrow V(1 + R_1Cs)(R_2 + Ls) + V_o R_1 = 0$$

$$\Rightarrow -V_o R_1 = V \left[ R_2 + LS + R_1 R_2 Cs + \frac{R_1 L C s^2}{R_1 + R_1 Cs} \right]$$

$$\Rightarrow -V_o = \frac{VR_2}{R_1} + \left[ \frac{L}{R_1} + R_2 C \right] SV + L C s^2 V$$

$$\Rightarrow -V_o = \frac{R_2}{R_1} V + \left[ \frac{L}{R_1} + R_2 C \right] \frac{dV}{dt} + L C \frac{d^2V}{dt^2}$$

Solve by virtual ground method,  $V_P = V_N = 0$

(4)

Applying KCL at 'n' -

$$\frac{0 - V_C}{3} + \frac{0 - V_0}{5} = 0 \Rightarrow 5V_C + 3V_0 = 0 \quad \text{--- (1)}$$

Applying KCL at  $V_C$  -

$$\frac{V_C}{3} + \frac{V_C - 21}{3} + \frac{V_C}{6} + \frac{V_C - V_0}{8} = 0$$

$$\Rightarrow \frac{V_C}{3} + \frac{V_C - 21}{3} + \frac{V_C}{6} + \frac{V_C + 5V_3}{8} = 0 \quad \left. \begin{array}{l} \\ \end{array} \right\} \text{from (1)}$$

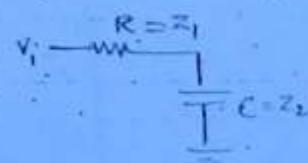
$$\therefore V_C = 6V$$

$$\therefore I_i = \frac{21 - V_C}{3k} = 5.8 \text{ mA} ; \quad V_0 = \frac{-5}{3} \times 6 = -10V ; \quad R_f = \frac{V_0}{I_i} = \frac{21}{5} = 4.2 \Omega$$

17<sup>th</sup> August, 2012

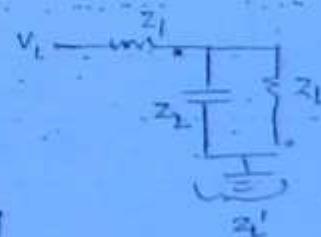
First order Butterworth filter

- Loading Effect -



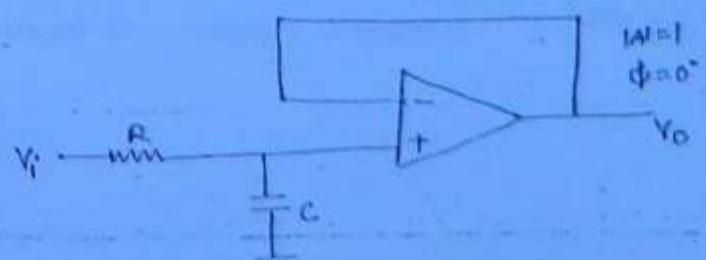
$$T = \frac{V_i}{z_1 + z_2}$$

$$T' < T$$

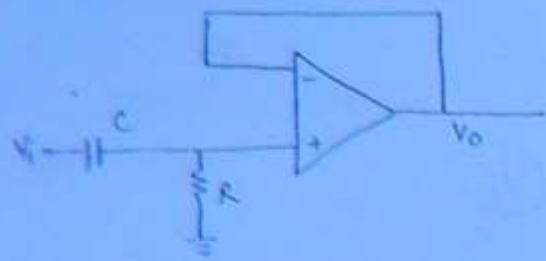


$$T' = \frac{V_i}{z_1 + z_L}$$

→  $T'$  will keep on ↑ as  $Z_L$  is ↑. Hence, there will be loading effect and parameters of filter will change. To avoid this, voltage follower circuit is used.



→ first order butterworth filter  
LPF

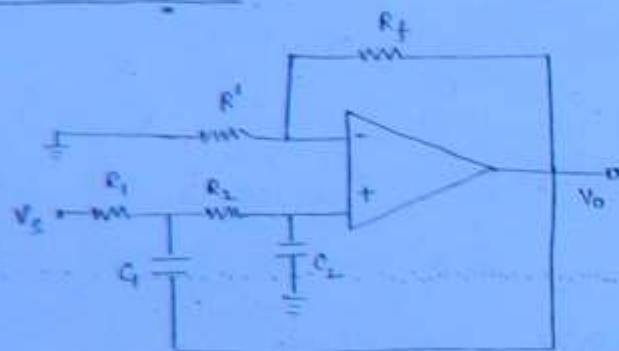


→ HPF - First order BWF.

$$f_c = \frac{1}{2\pi RC}$$

(42)

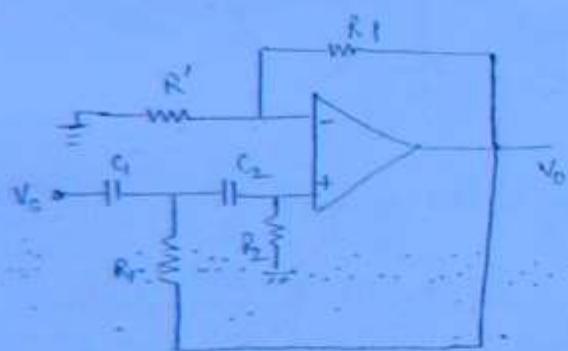
2<sup>nd</sup> order LP BWF -



$$f_c = \frac{1}{2\pi \sqrt{R_1 R_2 C_1 C_2}} = \frac{1}{2\pi R C}$$

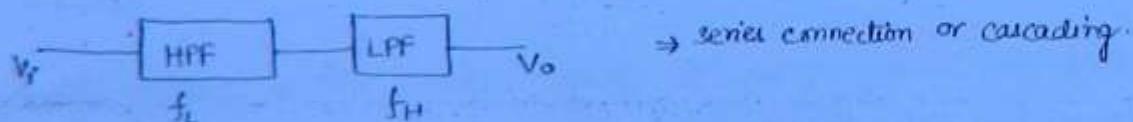
if  $R_1 = R_2 = R$   
&  $C_1 = C_2 = C$

2<sup>nd</sup> order HP BWF -



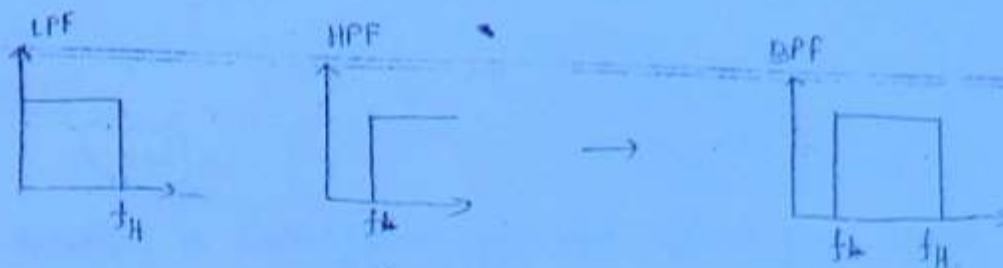
$f_c$  = same as above.

Band Pass filter :-



$f_H$  = high 3dB cutoff frequency [for LPF]

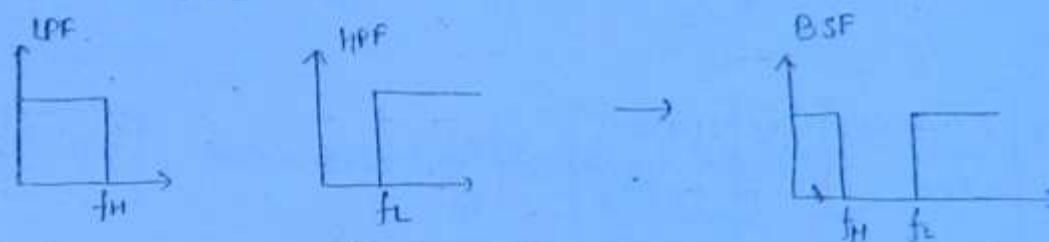
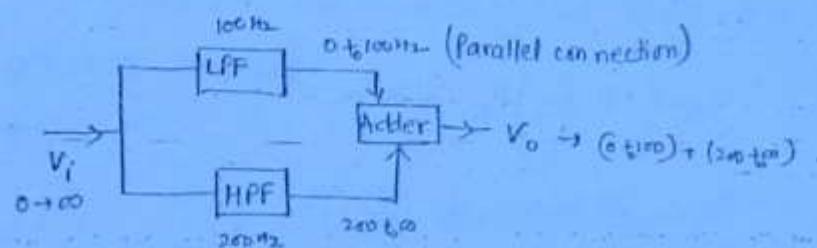
$f_L$  = low " " " " [for HPF]



→ for BPF,  $f_H > f_L$

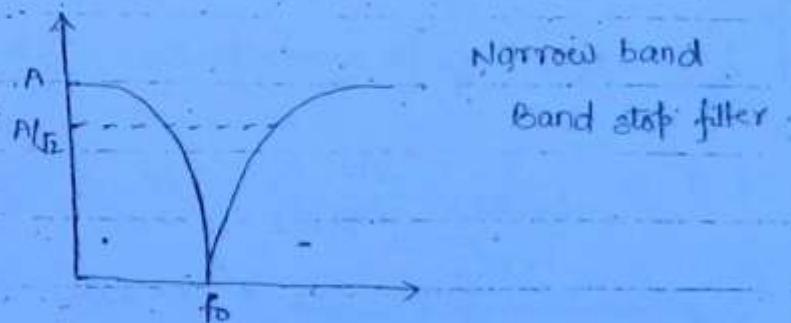
(43)

### Band Reject (or stop or Rejection) filter



→ for BSF,  $f_H < f_L$

### Notch filter

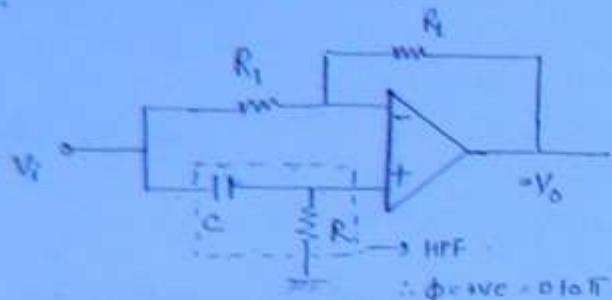


- Used in communication systems to eliminate power supply noise.
- Notch frequencies are 50Hz, 100Hz, 60Hz etc.
- Also used to remove harmonics.

### All-Pass Filter

44

-It allows all input signal freq. to pass w/o any amplification or attenuation.



Applying superposition -

for non-inverting terminal -

$$V_P = \frac{R}{R+L} \cdot V_C$$

for inserting terminal —

$$V_{D_1} = \left(1 + \frac{R_1}{R_2}\right) V_P - 2V_P$$

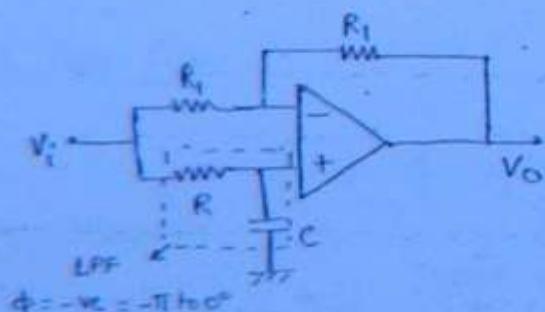
$$V_{01} = \frac{2 \cdot R \cdot C \cdot s}{R \cdot C \cdot s + 1}, V_1 = 0$$

$$\therefore V_o = V_{o1} + V_{o2} = V_i \left[ \frac{2RCs}{1+RCs} - 1 \right] \Rightarrow \left[ \frac{-1 + RCs}{1+RCs} \right] = \frac{V_o}{V_i}$$

$$\Rightarrow A = -\frac{1 - RCS}{1 + RCS}, \quad ; \quad |A| = 1$$

$$\Rightarrow \boxed{\phi = 180 - 2 \tan^{-1}(\omega RC)} \quad \xrightarrow{\text{as } \omega \rightarrow 0} \quad \text{for } \omega=0 \quad \phi = 180 \text{ or } \pi$$

∴ Range of phase  $\rightarrow [0^\circ \leq \phi \leq 180^\circ]$

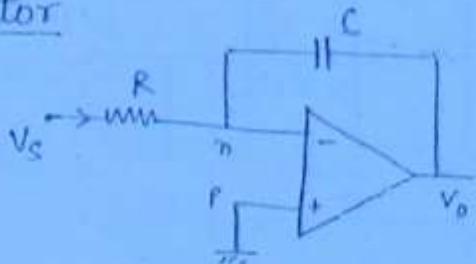


$$A = \frac{1 - RCs}{1 + RCs} ; |A| = 1$$

$$\phi = \pm \tan^{-1}(\omega R C)$$

$$\omega \rightarrow 0 \quad \phi = 0$$

## Integrator



$$C \frac{d(V_m - V_o)}{dt} = \frac{V_t - V_m}{R} \quad (45)$$

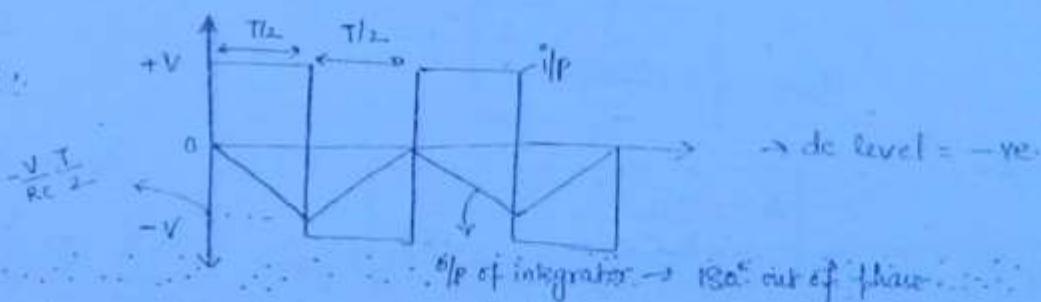
$$\Rightarrow \frac{V_s}{R} = -C \frac{dV_o}{dt}$$

\* Linear charging of capacitor is possible when we are providing constant current in the circuit and this can be achieved through current mirror circuit.

$$\Rightarrow \frac{dV_o}{dt} = -\frac{V_s}{RC} \Rightarrow V_o = -\frac{1}{RC} \int_0^t V_s dt + V_o(0^+) \downarrow \text{initial value.}$$

→  $\phi = 180^\circ$ , hence called as **Inverting Integrator** \*\*\*

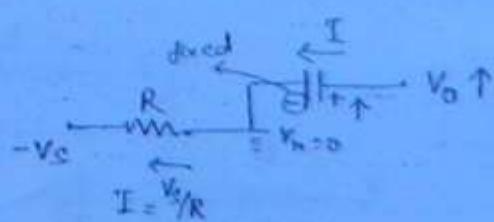
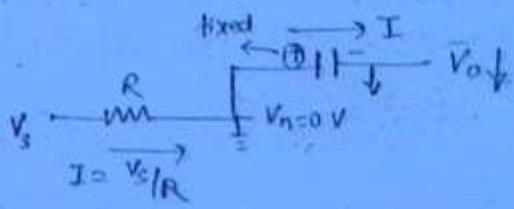
Output:



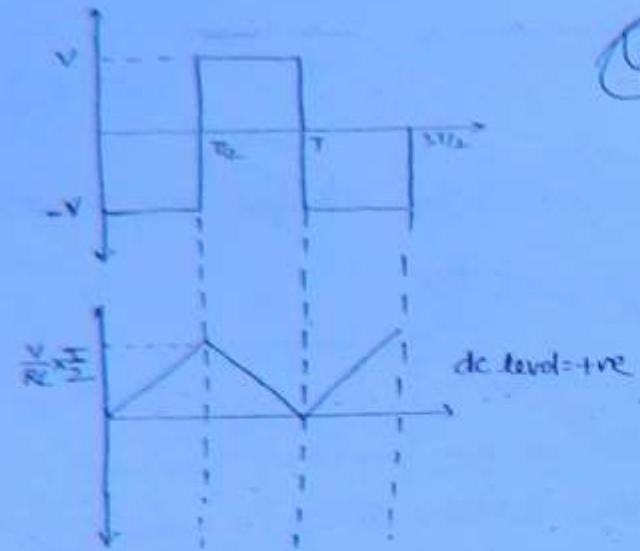
$$\frac{dV_o}{dt} : \text{rate of change of o/p} = \text{slope} = -\frac{V_s}{RC}$$

$$\text{for } V_s = +V \Rightarrow \frac{dV_o}{dt} = -\frac{V}{RC} \Rightarrow V_o \downarrow$$

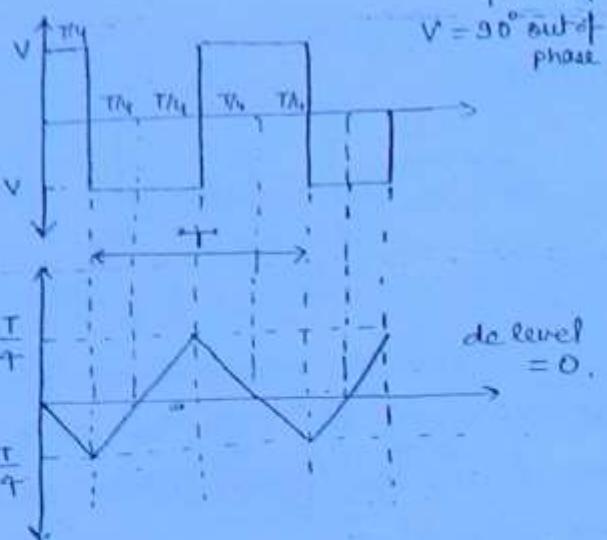
$$\text{for } V_s = -V \Rightarrow \frac{dV_o}{dt} = \frac{V}{RC} \Rightarrow V_o \uparrow$$



Eq.



(Q6)



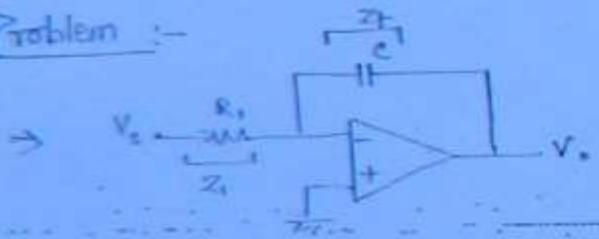
leading  
 $V = 90^\circ$  out-of-phase

- Time period of output = time period of input in all the three cases.
- only change is in the dc levels of off.

\* Swing =  $V_{max} - V_{min} = \frac{V}{RC} \times \frac{T}{2}$  is same in all the three cases.

$$\text{swing} = \frac{V}{2RCf}$$

Problem :-



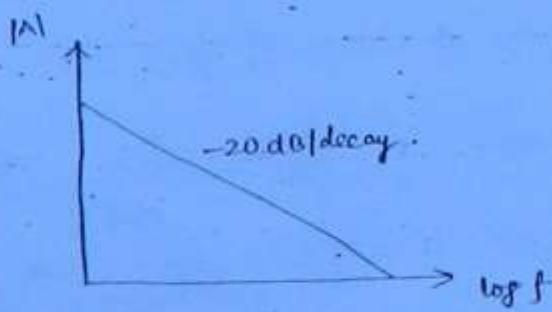
$$\text{Gain} \rightarrow A = -\frac{Z_f}{Z_1}$$

$$A = -\frac{1}{R_1 C s} = -\frac{1}{j \omega R C}$$

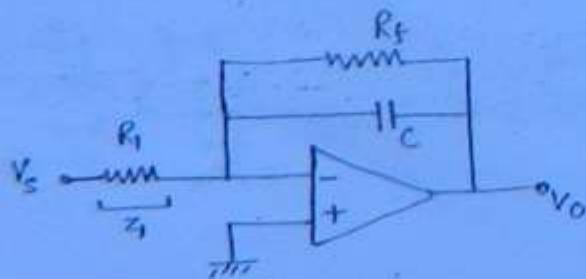
$$\rightarrow |A| = \frac{1}{\omega R C}$$

$$\rightarrow \text{At } \omega=0, |A|=0$$

→ Gain is not stable for entire freq range, hence no freq. stability. This is called Roll off problem.



Practical Integrator :-



$$Z_f = Z_C \parallel R_f = \frac{R_f \cdot Y_{CE}}{R_f + Y_{CE}} = \frac{R_f}{1 + R_f C_s}$$

(42)

$$Z_1 = R_1 ; \quad \therefore \text{Gain, } A = -\frac{I_f}{Z_1}$$

$$\Rightarrow A = -\frac{R_f / R_1}{1 + R_f C_s}$$

$$\Rightarrow A = \frac{-R_f / R_1}{1 + j\omega R_f C_s}$$

$$\rightarrow |A| = \frac{R_f / R_1}{\sqrt{1 + \omega^2 R_f^2 C_s^2}}$$

$$\rightarrow \text{At } \omega=0, |A|_{\max} = \frac{R_f}{R_1}$$

$$\rightarrow \text{3dB cut-off freq.}, \omega_a = 2\pi f_a = \frac{1}{R_f C_s}$$

$$\Rightarrow f_a = \frac{1}{2\pi R_f C_s}$$

$$\rightarrow \text{At } \omega=\omega_b, |A| = 1$$

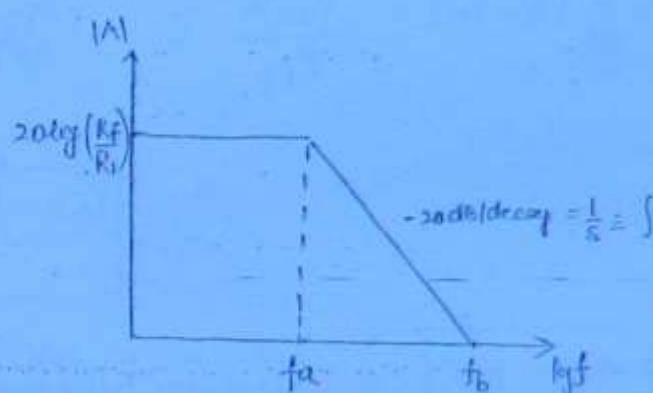
$$\Rightarrow \frac{R_f^2}{R_1^2} = 1 + \omega_b^2 R_f^2 C_s^2 \Rightarrow \omega_b^2 R_f^2 C_s^2 \approx \frac{R_f^2}{R_1^2} \quad \left. \begin{array}{l} \text{Neglecting 1} \\ \text{?} \end{array} \right\}$$

$$\Rightarrow \omega_b = \frac{1}{R_1 C_s} \quad \text{or} \quad f_b = \frac{1}{2\pi R_1 C_s}$$

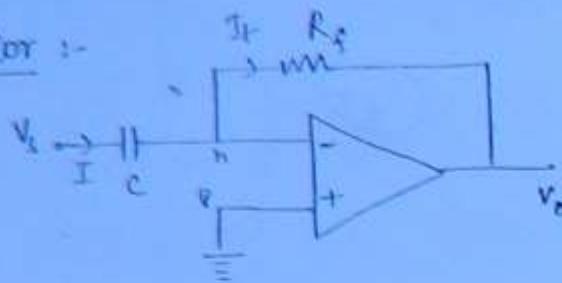
$$\left. \begin{array}{l} \text{?} \\ f_b = 6.4 \text{ Hz} \\ \text{at } |A|=1 \text{ at this freq.} \end{array} \right\}$$

$\rightarrow$  Circuit acts as integrator between  $f_a$  and  $f_b$

$$\Rightarrow f_a < f_b \Rightarrow \frac{1}{2\pi R_f C_s} < \frac{1}{2\pi R_1 C_s} \Rightarrow R_1 < R_f$$



Differentiator :-



$$V_P = V_N > 0$$

(48)

$$I = I_f$$

$$C \frac{d}{dt} (V_s - V_o) = \frac{V_o - V_s}{R_f} \Rightarrow \frac{CdV_s}{dt} = -\frac{V_o}{R_f}$$

$$\Rightarrow V_o = -R_f C \cdot \frac{dV_s}{dt}$$

$\rightarrow$  Φ shift =  $180^\circ$   $\Rightarrow$  Inverting Differentiator

$$\rightarrow A = -\frac{R_f}{BZ_1} \Rightarrow A = -R_f C s. = -j\omega R_f C.$$

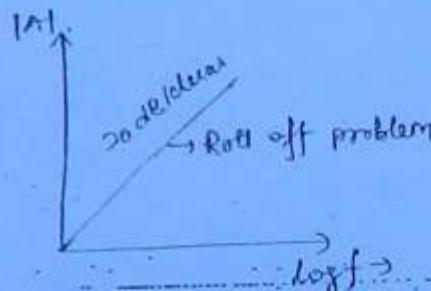
$$\rightarrow |A| = \omega R_f C$$

Problem -

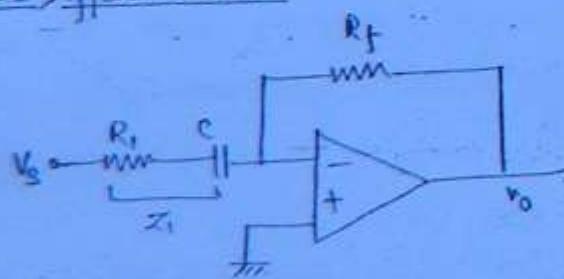
$$\rightarrow \text{At } \omega = 0, |A| = \infty$$

$\rightarrow$  Frequency stability is less.

$\rightarrow$  Roll off problem:



Practical Differentiator :-



$$Z_f = R_f$$

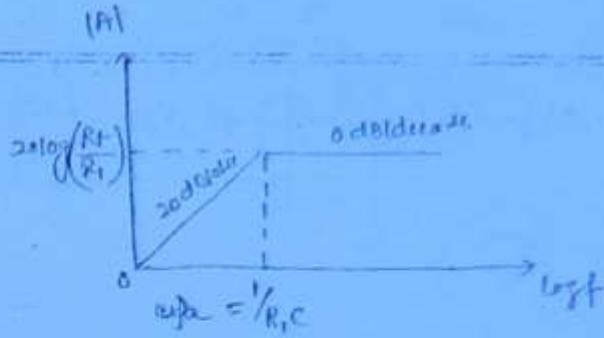
$$Z_1 = R_1 + j\omega C s = \frac{R_1 C s + j}{C s}$$

$$\therefore A = \frac{-R_f}{R_1 + j\omega C s} = \frac{-R_f / R_1}{1 + \frac{j}{R_1 C s}} = \frac{-R_f / R_1}{1 - j / \omega R_1 C}$$

$$A = \frac{-R_f / R_1}{1 - j / \omega R_1 C}$$

$$\rightarrow |A| = \frac{R_f/R_1}{\sqrt{1 + 1/\omega^2 R_1^2 C^2}}$$

(49)



$$\rightarrow \text{As } \omega \rightarrow \infty, |A|_{\infty} = \frac{R_f}{R_1}$$

$\rightarrow$  Circuit is stable at high frequency.

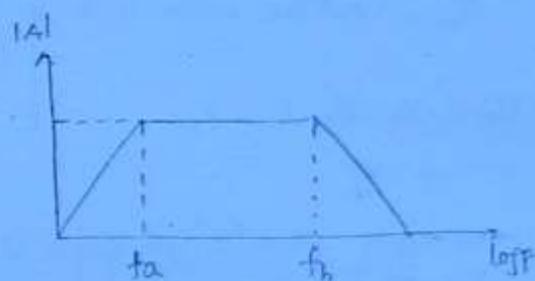
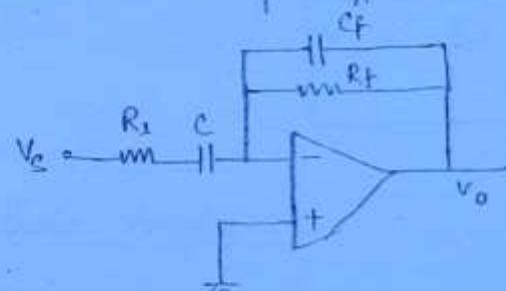
$$\rightarrow A = -\frac{sCR_f}{1+sCR_1} \rightarrow \text{Plotting Bode plot}$$

$$\rightarrow f_a = \frac{1}{2\pi R_1 C}$$

$\rightarrow$  Circuit will act as differentiator if  $\omega < \omega_a$

$\rightarrow$  But, BW =  $\omega_a$ .

To limit the BW of Differentiator -

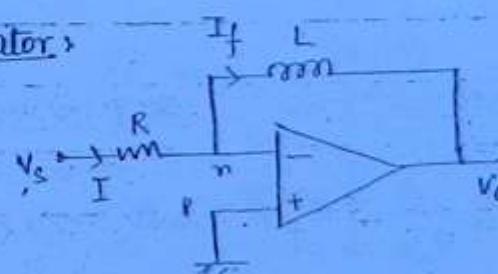


$\rightarrow C_f$  will limit the bandwidth of differentiator.

$\rightarrow$  BPF is also called as Practical differentiator.

$\rightarrow R_1$  is added to increase the frequency stability of the output.

Differentiator



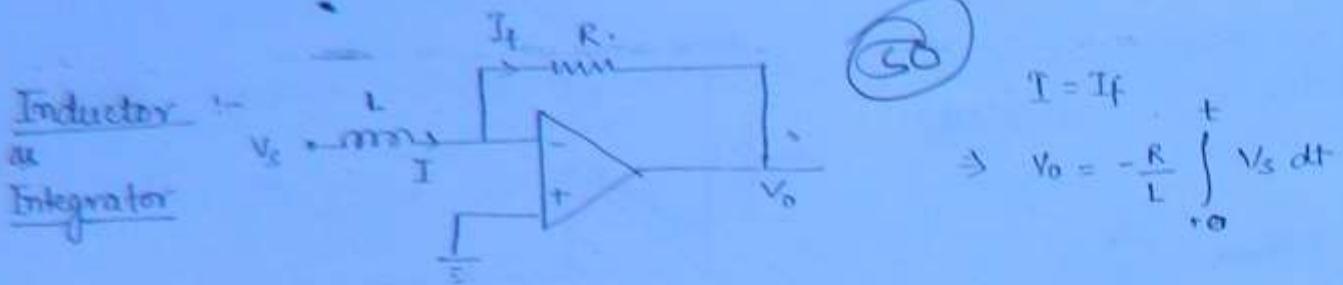
$$V_p = V_n = 0$$

$$I = I_f$$

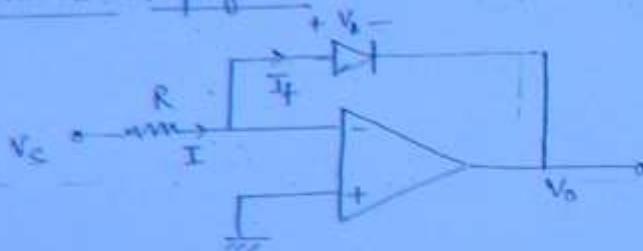
$$\therefore \frac{V_s}{R} = -\frac{1}{L} \int V_o dt$$

$$\Rightarrow \frac{-L}{R} \frac{dV_s}{dt} = V_o$$

$\rightarrow$  Bulky and Heavy due to L.



### Logarithmic Amplifier



$$V_p = V_n = 0$$

$$I = I_f = I_b$$

$$I_b = I_0 \left[ e^{\frac{V_p}{\eta V_T}} - 1 \right]$$



$I_0$  = reverse saturation current

$$V_D = V_n - V_o = -V_o$$

$$I = I_b$$

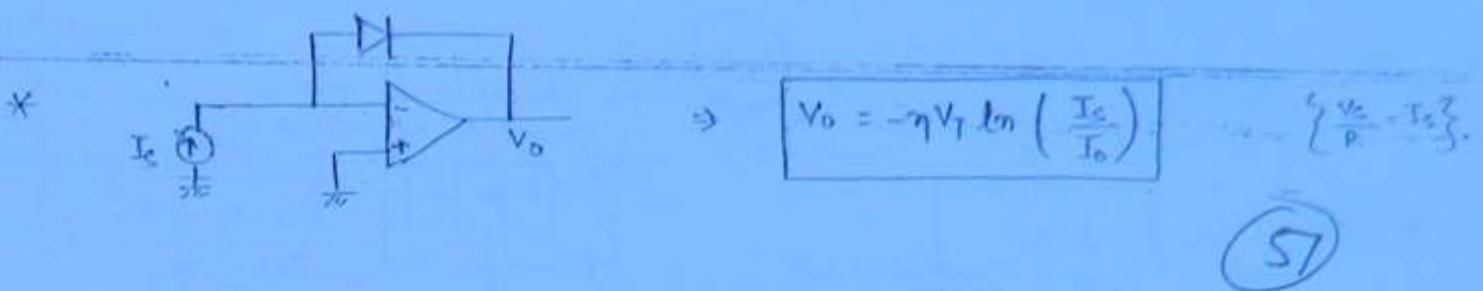
$$\Rightarrow \frac{V_s}{R} = I_0 \left[ e^{-\frac{V_o}{\eta V_T}} - 1 \right]$$

$$\Rightarrow \frac{V_s}{I_0 R} + 1 = e^{-\frac{V_o}{\eta V_T}} \Rightarrow \frac{V_o}{\eta V_T} = -\ln \left[ \frac{V_s}{I_0 R} + 1 \right]$$

$$\Rightarrow V_o = -\eta V_T \ln \left[ \frac{V_s}{I_0 R} + 1 \right]$$

$\Rightarrow I_0$  is very small  $\Rightarrow \frac{V_s}{I_0 R} \gg 1$

$$\therefore V_o = -\eta V_T \ln \left[ \frac{V_s}{I_0 R} \right]$$



### Anti-logarithmic Amplifier

$$V_D = V_S - V_n = V_S ; \quad T = I_E = I_D$$

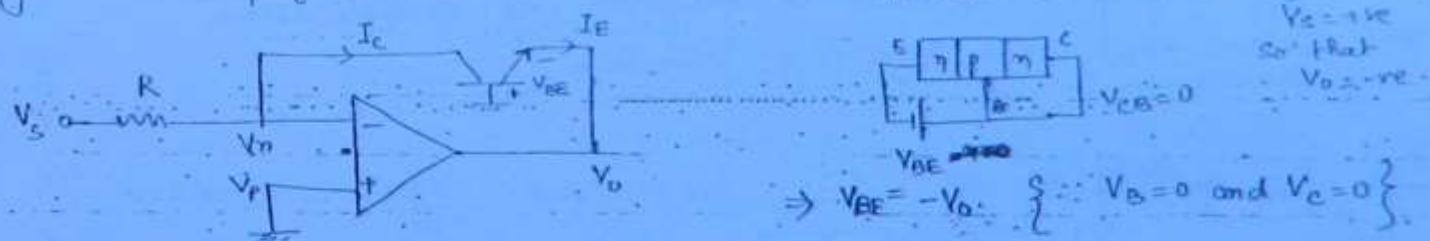
$$I_0 \left[ e^{\frac{V_S - V_n}{\eta V_T}} - 1 \right] = \frac{V_n - V_O}{R}$$

$\Rightarrow \because V_S > 0 \text{ and if } e^{\frac{V_S - V_n}{\eta V_T}} \gg 1$

$$\Rightarrow V_O = -I_0 R e^{\frac{V_S - V_n}{\eta V_T}}$$

$$\Rightarrow V_O = -I_0 R \text{ antilog} \left( \frac{V_S}{\eta V_T} \right)$$

### Logarithmic Amplifier :-



$$T_D = I_C \approx T_E = I_{C0} \left[ e^{\frac{V_{BE}}{\eta V_T}} - 1 \right] = \frac{V_S}{R}$$

$$\Rightarrow \frac{V_S}{I_{C0} R} + 1 = e^{\frac{V_{BE}}{\eta V_T}}$$

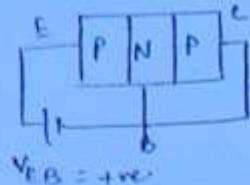
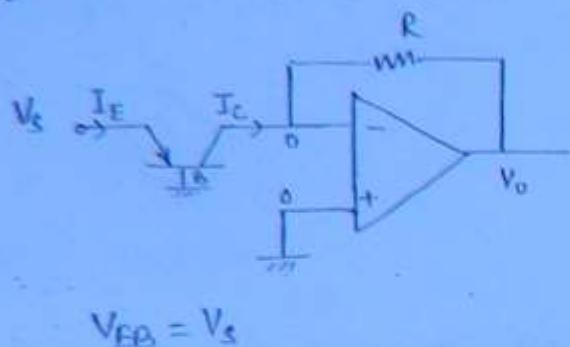
$$\Rightarrow V_O = -V_T \ln \left[ \frac{V_S}{I_{C0} R} + 1 \right] \quad \text{for } \eta = 1$$

$$\Rightarrow V_O = -V_T \ln \left[ \frac{V_S}{I_{C0} R} \right]$$

## Anti-logarithmic Amplifier :-

(52)

Tr. should be in active region,  
hence  $V_B = +ve$ , so that  $V_{EB} = +ve$ .



$$\begin{aligned}V_{CB} &= 0, \rightarrow R_B \\V_{EB} &= +ve \\&\rightarrow f.e.\end{aligned}$$

$$I_D = I_C \approx I_E = I_{CO} \left[ e^{\frac{V_S}{\eta N_T}} - 1 \right]$$

$$I_E = I_C = I_f$$

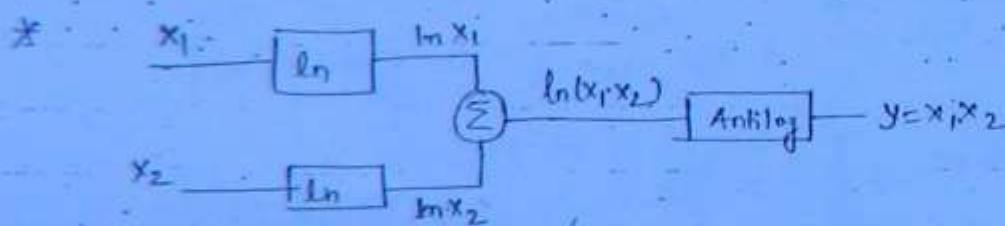
$$\Rightarrow I_{CO} \left[ e^{\frac{V_S}{\eta N_T}} - 1 \right] = \frac{V_o - V_0}{R}$$

$$\Rightarrow V_o = -I_{CO} R \left[ e^{\frac{V_S}{\eta N_T}} - 1 \right] \Rightarrow$$

$$V_o \cong -I_{CO} R \text{ antilog} \left( \frac{V_S}{\eta N_T} \right)$$

## Applications

-log and Antilog amplifiers are used in designing of multiplication, division, square root and squaring circuits.

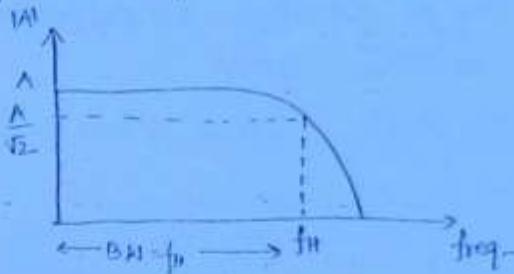


→ If the -ve sign is not set for o/p, then pass o/p through a inverting amplifier with gain = 1.

20<sup>th</sup> August, 2012

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### Frequency response of Practical op-amp -



$$|Z_L| = \frac{1}{2\pi f_C} ; f_C, \text{ rad/s}$$

- Op-Amp is basically a dc amplifier. It can amplify a dc signal and also an ac signal in a wide band, extending from 0-1 MHz.

### Slew Rate (SR) -

- It is the time rate of change of closed loop amplifier o/p voltage under large signal condition. (Typical value = 100 V/μsec). Unit → V/μsec.

$$\rightarrow SR = \left. \frac{dV_o}{dt} \right|_{max} \Rightarrow SR = \frac{dV_o}{dV_i} \times \left. \frac{dV_i}{dt} \right|_{max}$$

$$SR = |A_{CL}| \times \left. \frac{dV_i}{dt} \right|_{max}$$

$$Vi = V_m \sin \omega t \quad \therefore \frac{dV_i}{dt} = V_m \omega \cdot \cos \omega t \quad \therefore SR = |A_{CL}| \times V_m \omega_m$$

$$\Rightarrow \left. \frac{dV_i}{dt} \right|_{max} = V_m \omega_m \quad \Rightarrow \quad \boxed{\omega_m = 2\pi f_m = \frac{SR}{|A_{CL}| \cdot V_m}}$$

$\omega_m$  or  $f_m$  → Max freq. of operation.

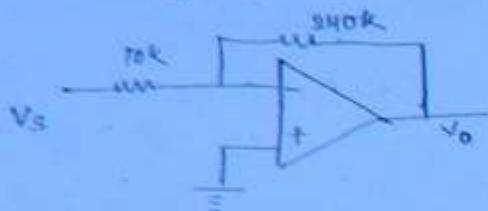
- for  $f \leq f_m \rightarrow$  o/p without distortion
- $f > f_m \rightarrow$  " with "

- Ques - for an operational amplifier having a SR of 2V/μsec, for what is the max. closed loop voltage gain that can be used when if?

signal changes by  $0.5V$  in  $10\mu sec$ ?

(54)

Ques for the given circuit, determine the max freq. of operation in rad/sec  
that can be used by taking  $SR = 0.5V/\mu sec$  and  $V_m = 0.02V$ .



Soln (1)  $SR = 2V/\mu sec \quad \therefore SR = |A_{CL}| \times \frac{dV_i}{dt}$   
 $\frac{dV_i}{dt} = 0.05 V/\mu sec \Rightarrow 2 = |A_{CL}| \times 0.05$   
 $\Rightarrow |A_{CL}| = 40.$

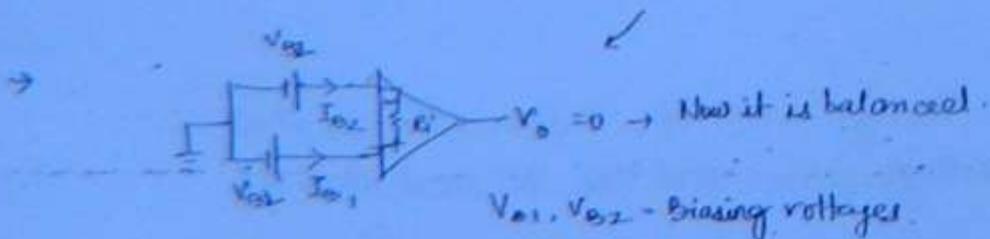
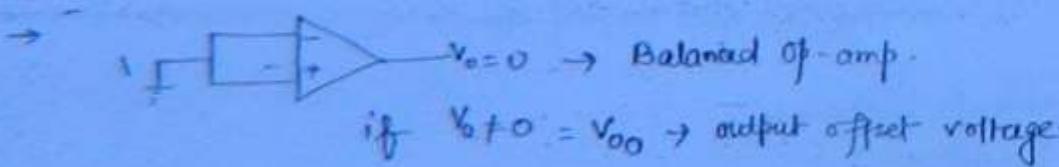
Soln (2)  $|A_{CL}| = \left| \frac{240}{10^3} \right| = +24.$

$$\omega_m = \frac{240 \times 0.5 \times 10^6}{24 \times 0.02} = \frac{2.08 \text{ rad/sec} \times 10^6}{2} = 1.04 \text{ rad/sec} \times 10^6$$

Note - If  $A_{CL}$  is not given, take  $A_{CL} = 1$ .

\* Slow-rate is limited by internal capacitances of op-amp. hence it is not  $\infty$ .

Offset voltages and currents :-

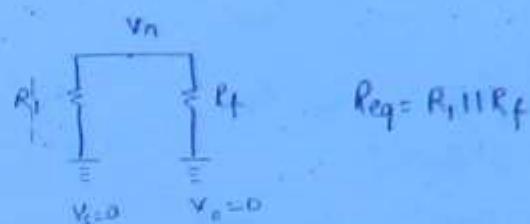
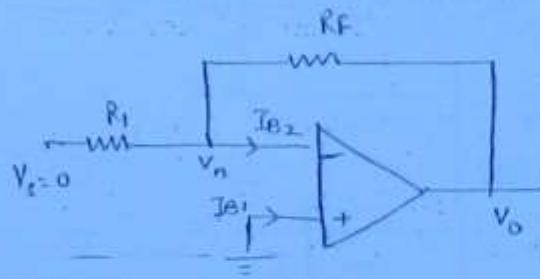


→ i/p bias current =  $I_B = \frac{I_{B1} + I_{B2}}{2}$ , when  $V_o = 0$ .

(55)

→ i/p offset current =  $I_{BO} = I_{B1} - I_{B2}$  when  $V_o = 0$ .

→ i/p offset voltage =  $V_{IO} = I_{BO} \cdot R_i = V_{B1} - V_{B2}$  when  $V_o = 0$ .



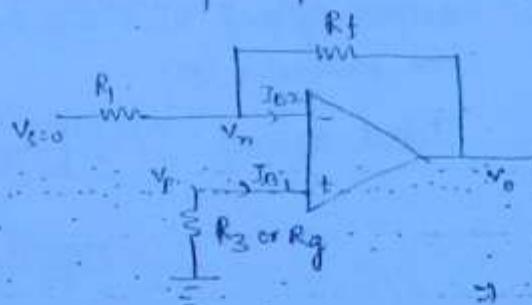
$$|V_n| = R_{eq} \cdot I_{B2} = I_{B2} [R_1 || R_f]$$

$$|V_p| = 0$$

∴ Amplifier is again unbalanced when  $V_s = 0$  (ie without signal) & feedback is connected.

→ Normally,  $I_{B1} \approx I_{B2}$ .

→ To balance the op-amp,  $R_3$  is connected to non-inverting terminal.



$$\text{Now, } |V_p| = I_{B1} \cdot R_3.$$

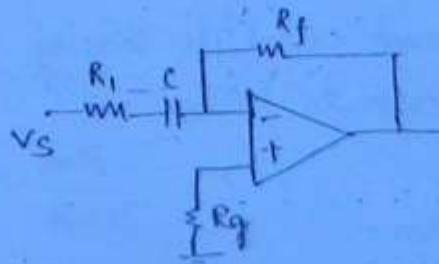
Hence, for balanced condition-

$$|V_p| = |V_n|$$

$$\Rightarrow R_3 = [R_1 || R_f]$$

\* To minimise the effect of i/p bias current, one should place in non-inverting terminal, a resistance equal to dc resistance seen by inverting terminal.

Ques What is  $R_g$ ?



$$\text{Ans} \quad R_g = [R_1 + z_c] || R_f.$$

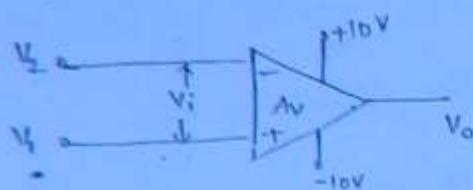
$$z_c = \frac{1}{2\pi f C} \Rightarrow z_c = \infty \text{ for DC.}$$

$$\Rightarrow R_g = [R_1 + \infty] || R_f = R_f. \text{ Ans}$$

## Transfer Characteristics of Op-Amp :-

(56)

### i) Practical Op-amp :-



$$A_v = 10^6$$

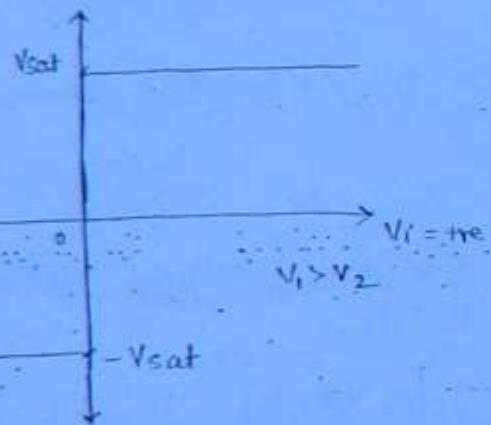
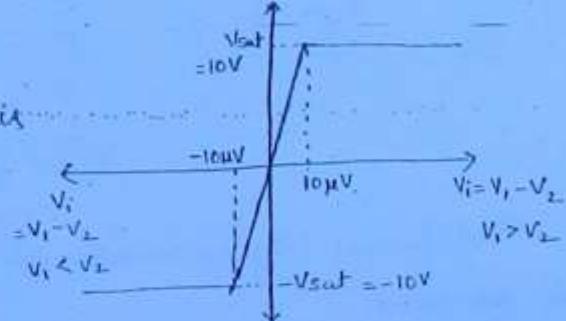
$$V_i' = V_1 - V_2$$

$$\Rightarrow V_o = 10^6 (V_1 - V_2). \quad \text{--- (1)}$$

Now, (i)  $V_1 = 10\mu V$ ,  $V_2 = 5\mu V$   
 $\Rightarrow V_o = 1V$

(ii)  $V_1 = 100\mu V$ ,  $V_2 = 90\mu V$   
 $\Rightarrow V_o = 10V$

(iii)  $V_1 = 120\mu V$ ,  $V_2 = 100\mu V$   
 $\Rightarrow V_o = 20V$  but  $> 10V$   $\rightarrow$  Op-amp is  
 $\Rightarrow V_o = 20\mu V$  saturated.



Practical Op-amp with sufficient  
+ve feedback. (or)

Ideal Op-amp, i.e.,  $[A_v \approx \infty]$

$\rightarrow$  As  $|A_{vo}| \uparrow$ , for a very small ifp, output will shoot up to  $+V_{sat}$  or  $-V_{sat}$  depending on ifp  $V_1 - V_2$  to be +ve or -ve.

$\rightarrow$  In a practical op-amp, ifp voltage cannot exceed its biasing voltage, i.e., range of ifp voltage is from  $-V_{sat}$  to  $+V_{sat}$ .

$\rightarrow$  Op-amp can enter into saturation when -

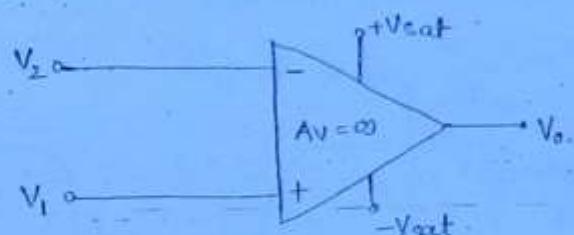
- (a) Large ifp signals are applied. ( $>$  than few  $\mu V$ ).

(i) When sufficient +ve feedback is provided.

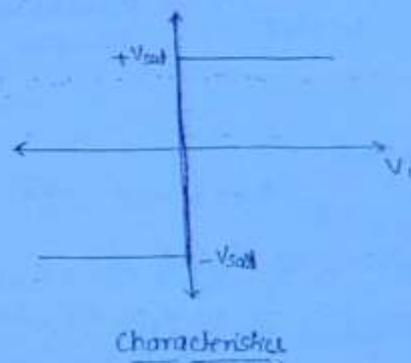
(S1)

### Comparator :-

#### Ideal comparator :-



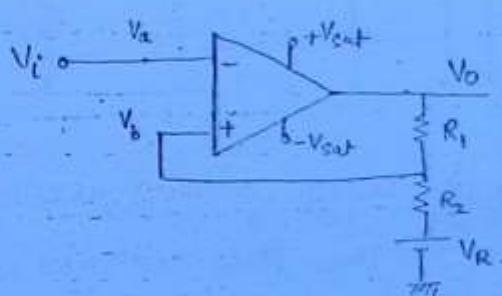
$V_1 > V_2$	$V_0$	
	+V <sub>sat</sub>	1
$V_1 < V_2$	-V <sub>sat</sub>	0



→ It is operated under open loop condition

### Practical Comparator :-

#### Schmitt Trigger :-



$|V_R| \rightarrow$  Reference voltage  $< |V_{sat}|$ .

$V_b > V_a$	$V_0$	
	+V <sub>sat</sub>	1
$V_b < V_a$	-V <sub>sat</sub>	0

$$\Rightarrow V_b = \frac{V_0 R_2}{R_1 + R_2} + \frac{V_R R_1}{R_1 + R_2}$$

(58)

$$\textcircled{1} \quad V_0 = +V_{\text{sat}}$$

$$V_{b1} = \frac{V_{\text{sat}} \cdot R_2}{R_1 + R_2} + \frac{V_R \cdot R_1}{R_1 + R_2} = V_{\text{um}} \rightarrow \text{Upper Threshold}$$

$$\textcircled{2} \quad V_0 = -V_{\text{sat}}$$

$$V_{b2} = -\frac{V_{\text{sat}} \cdot R_2}{R_1 + R_2} + \frac{V_R \cdot R_1}{R_1 + R_2} = V_{\text{LTH}} \rightarrow \text{Lower Threshold}$$

**Assumption:** let  $V_{\text{um}} = V_b = 6V$  > These are set before applying  $V_i$ , on the basis of  $\pm V_{\text{sat}} \& V_R$ .  
 $V_{\text{LTH}} = V_{b2} = 3V$ .  
 $V_m = 10V$ .

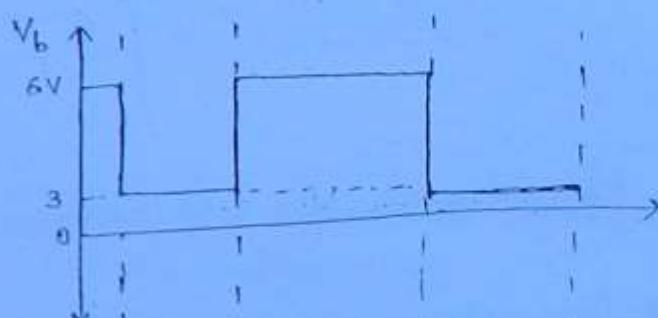
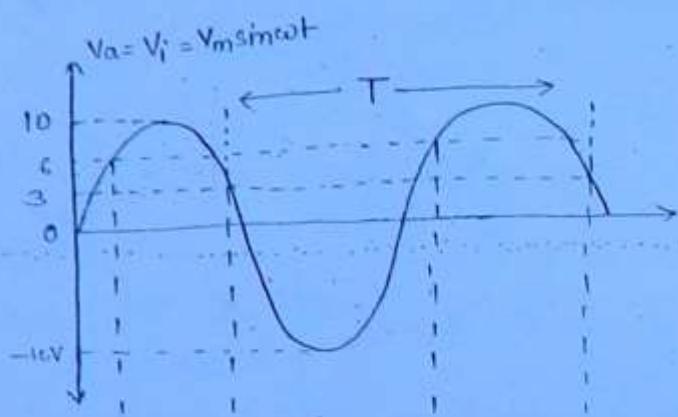
$$\textcircled{1} \text{ At } t=0,$$

$$\text{let } V_0 = +V_{\text{sat}}$$

$$\therefore V_b = V_{b1} = 6V$$

$$V_i = V_a = 0 \Rightarrow V_b > V_a \Rightarrow V_0 = +V_{\text{sat}}$$

Hence, our assumptions are right.



$$\textcircled{2} \text{ let } V_0 = -V_{\text{sat}}$$

$$\therefore V_b = V_{b2} = 3V$$

$$\therefore V_i = V_a = 0 \Rightarrow V_b > V_a \Rightarrow V_0 = +V_{\text{sat}}$$

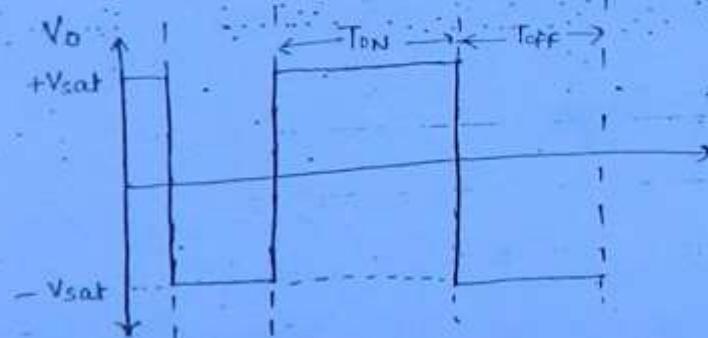
Hence our assumption was wrong.

$$V_0 = +V_{\text{sat}}$$

$$\textcircled{1} \text{ If } V_a \text{ ; } V_b \text{ & when } V_a \geq V_{b1} = 6V,$$

then  $V_0$  switches from  $+V_{\text{sat}}$  to  $-V_{\text{sat}}$

and  $V_b$  " "  $+6V$  "  $+3V$ .



$$\textcircled{2} \text{ If } V_a \text{ ; } V_b \text{ & when } V_a \leq V_{b2} = 3V,$$

then  $V_0$  switches from  $-V_{\text{sat}}$  to  $+V_{\text{sat}}$ .

and  $V_b$  " "  $+3V$  to  $+6V$ .

Thus these two steps will be repeated.

(59)

\* Necessary condition -

- (a)  $V_i$  should  $\uparrow$  and cross  $V_{thm}$ ; so that  $V_o$  switches from  $+V_{sat}$  to  $-V_{sat}$
- (b)  $V_i$  should  $\downarrow$  and cross  $V_{thL}$ ; ...  $V_o$  " " " $-V_{sat}$ " " $+V_{sat}$ .

\* Time period of off  $\Rightarrow T_0 = T_{ON} + T_{OFF} = T$  time period of op.

$$\rightarrow \because T_{ON} > T_{OFF}; \text{ Duty Cycle} = \frac{T_{ON}}{T_{ON} + T_{OFF}} \times 100\%$$

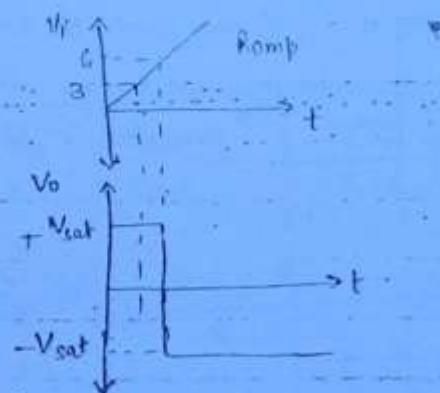
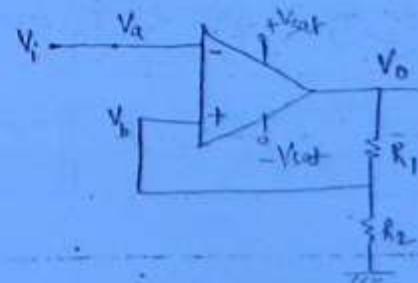
 $\Rightarrow D > 50\%$ ,  $\Rightarrow$  Asymmetrical square wave $\rightarrow$  It is a square wave converter.

$$\text{Eq} \leftarrow (1) V_i = 2\sin \omega t \quad \& \quad V_{b1} = 6V, V_{b2} = 3V$$
 $\therefore V_o = +V_{sat} \text{ always.}$

$$(2) V_i = 4 \text{ to } 5$$

$V_o$  depends on initial condition  
 $\text{if } +V_{sat} \text{ then will remain } +V_{sat}$   
 $" -V_{sat} " " -V_{sat}$

$$(3) V_i = > 6 \text{ volts.}$$

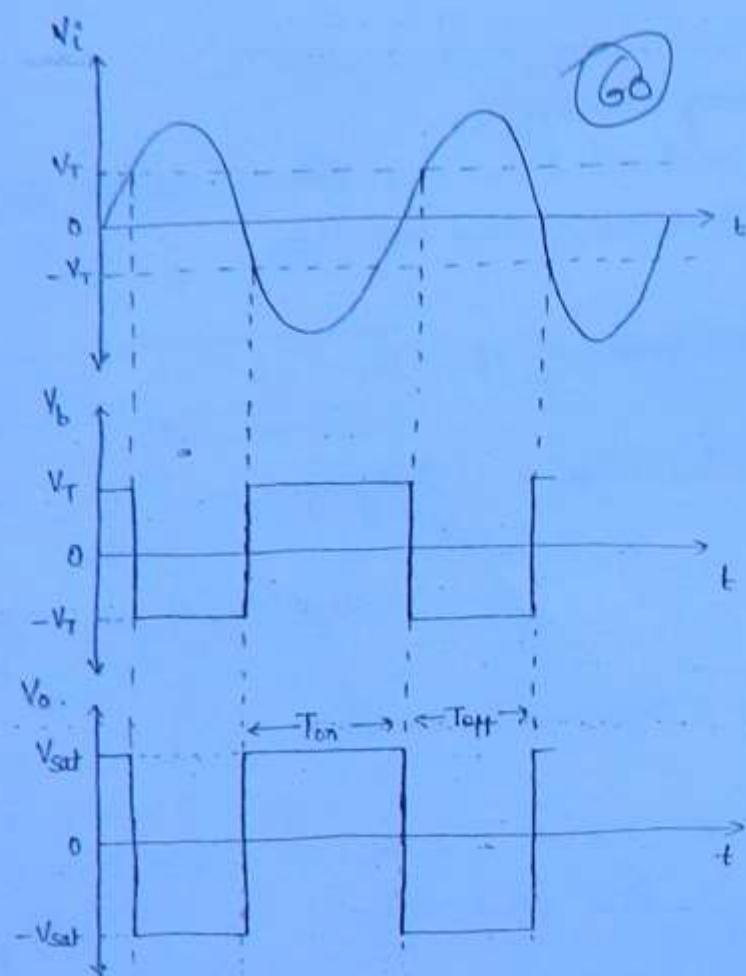
 $\therefore V_o = -V_{sat} \text{ always.}$ 
Duty cycle  $\leftarrow$  for  $D = 50\%$ ,  $V_R = 0$ .

$$V_{b1} = \frac{+V_{sat} \cdot R_2}{R_1 + R_2} = +V_T$$

$$V_{b2} = \frac{-V_{sat} \cdot R_2}{R_1 + R_2} = -V_T$$

→ Time period of o/p = same as time period of i/p. and hence by changing  $V_R$  we cannot change the frequency of output, we can only change the duty cycle, in turn, the avg. dc level (the area) of o/p will change.

$V_R = +ve$	$D > 50\%$
$V_R = 0$	$D = 50\%$
$V_R = -ve$	$D < 50\%$

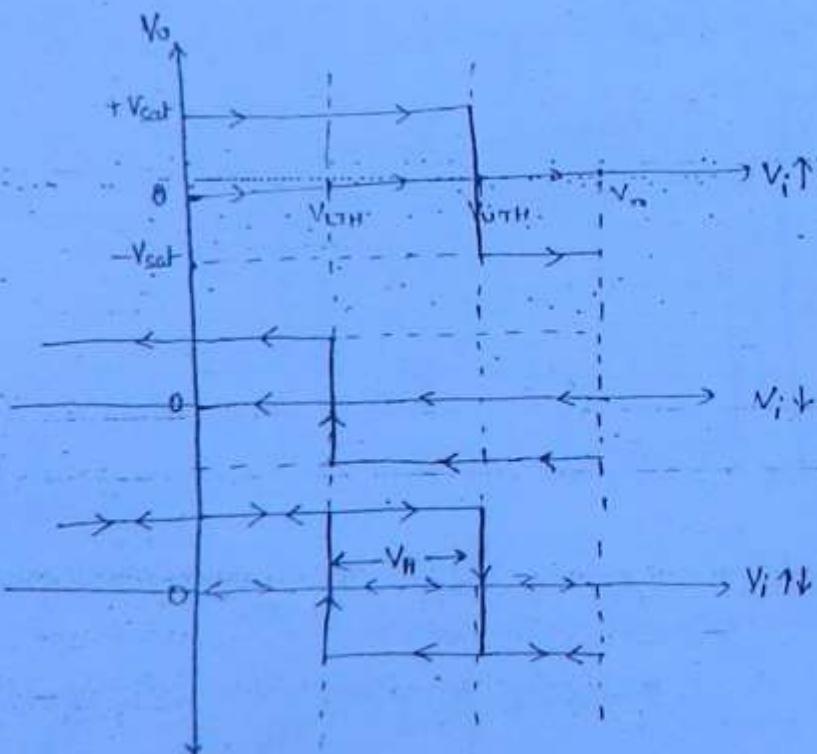


### Hysteresis Loop :-

i) for Asymmetrical Wave →

$V_H$  = Hysteresis voltage

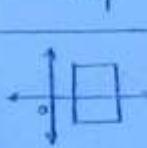
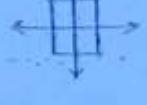
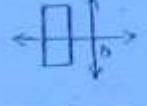
$$= V_{UTH} - V_{LTH}$$

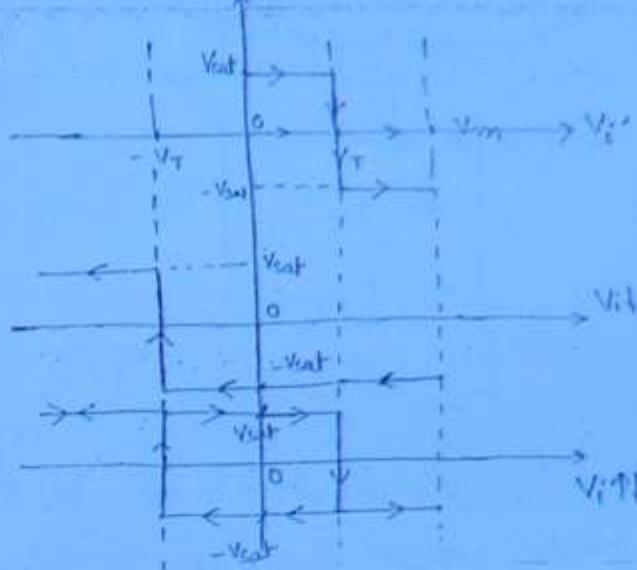


② for symmetrical wave-

(61)

$$V_H = 2V_T$$

$V_R$	Duty cycle	Avg. DC level	Hysteresis loop
$V_R > 0$	$D > 50\%$	+ve	
$V_R = 0$	$D = 50\%$	= 0	
$V_R < 0$	$D < 50\%$	<0 or -ve	



→ This table is valid only for the circuit discussed earlier.

$$\rightarrow V_R = \frac{R_2}{R_1+R_2} \cdot V_{sat} = k \cdot V_{sat}; \quad k = \frac{R_2}{R_1+R_2} < 1.$$

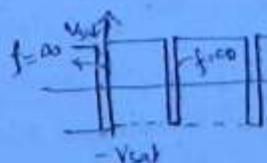
Hence, on non-inverting terminal, we are getting attenuated square wave.

$$\rightarrow V_H = \left( \frac{V_{sat} \cdot R_2}{R_1+R_2} + \frac{V_R \cdot R_1}{R_1+R_2} \right) - \left( \frac{-V_{sat} \cdot R_2}{R_1+R_2} + \frac{V_R \cdot R_1}{R_1+R_2} \right)$$

$$\Rightarrow V_H = \frac{2V_{sat} \cdot R_2}{R_1+R_2}$$

Hysteresis voltage is independent of  $V_R$ ; only the position of loop will change.

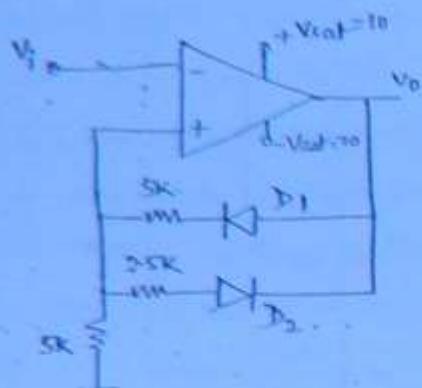
→ A slow moving waveform (sinl) can be converted into a fast moving waveform (square wave) by using schmitt trigger.



→ By adjusting duty cycle-

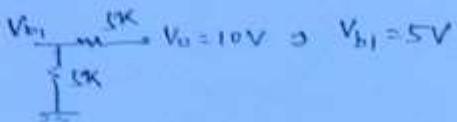
→ Slew Rate should be high, so that the triggering pulse reaches  $+V_{sat}$  or  $-V_{sat}$  very fast.

Hence, SR ↑ for triggering op-amp.

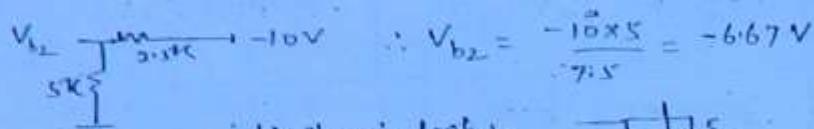


Consider schmitt-trig

Soln for  $+V_{sat} = 10V \Rightarrow D_1 = ON, D_2 = OFF$

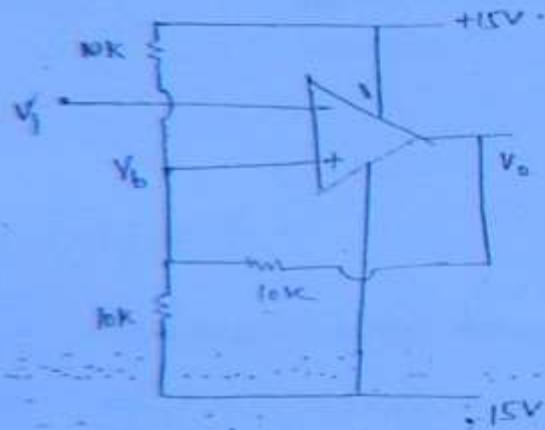


For  $-V_{sat} = -10V \Rightarrow D_1 = OFF, D_2 = ON$



Hysteresis loop  $\frac{10}{5} : \frac{-10}{-6.67}$

Ques Consider schmitt trigger ckt. A  $\Delta$  wave which goes from  $-15V$  to  $+15V$  is applied to inverting i/p of op-amp. Assume that op swings from  $+15$  to  $-15V$ . The voltage at non-inverting i/p switches b/w



Soln KCL at  $V_b$  -

$$V_b \left[ \frac{1}{10} + \frac{1}{10} + \frac{1}{10} \right] - \frac{15}{10} + \frac{15}{10} - \frac{V_o}{10} = 0$$

$$\therefore 3V_b = V_o$$

$$\Rightarrow V_b = V_o/3$$

$$V_b = +5V \text{ to } -5V$$

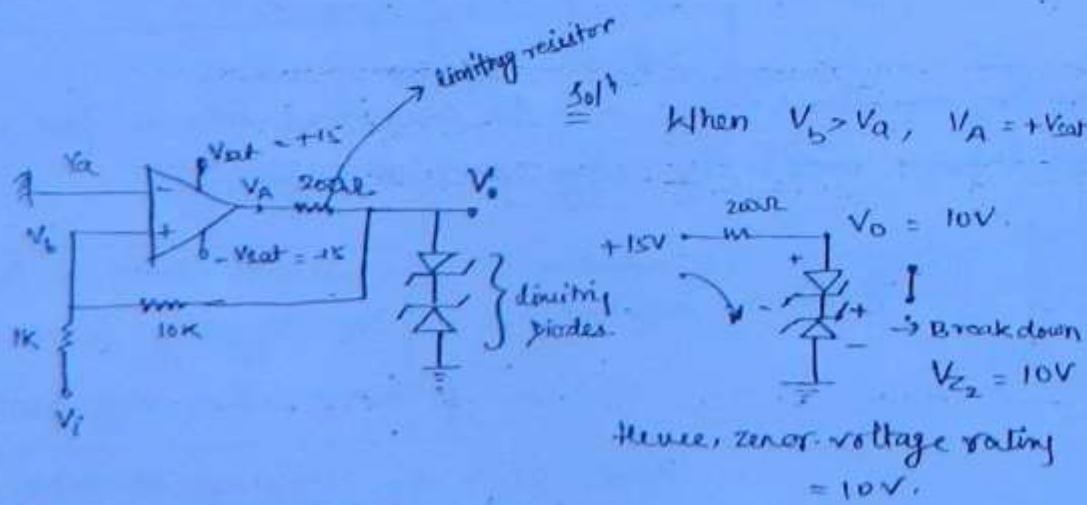
Find range -

- (a)  $-12$  to  $12V$  (b)  $-5$  to  $+5V$
- (c)  $-7.5$  to  $7.5V$  (d)  $0$  to  $5V$

21<sup>st</sup> August, 2012

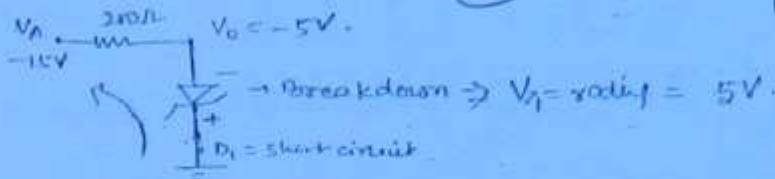
workbook Pg-62

Ans



When  $V_b < V_a$ ,  $V_h = -V_{sat} = -15V$ .

(63)



$\rightarrow 2k\Omega$  is connected to dissipate the extra voltage.

Applying KCL at  $V_b$  -

$$V_b - V_i + \frac{V_b}{10} - \frac{V_0}{10} = 0$$

$$\Rightarrow V_b = \frac{V_0 + 10V_i}{11}$$

$V_b = 0 \Rightarrow$  the o/p will switch from  $+10V$  to  $-5V$ .

$$\therefore V_b = \frac{10V_i + 10}{11} = 0 \Rightarrow V_i = -1V = V_{TH}$$

$$\text{At } t=0, V_i=0, V_b = \frac{V_0}{11}, \text{ but } V_0=-5V \quad \text{from } -B \text{ to } 0V \Rightarrow V_b = -5/11$$

case I

when  $V_0 = +10V \Rightarrow V_b > V_a \Rightarrow V_b > 0$

$$V_b = \frac{V_i \cdot 10 + 10}{11}$$

As  $V_i \uparrow$ ,  $V_b$  will also increase.

and when  $V_0 \nparallel V_b \nparallel V_a$

Case II  $V_0 = -5V \Rightarrow V_b < V_a \Rightarrow V_b < 0$

$$\therefore V_b = \frac{10V_i - 5}{11}$$

As  $V_i \uparrow$ ,  $V_b$  will also  $\uparrow$ .

and when  $V_b = 0$ ,  $V_0$  will switch

$$10V_i - 5 = 0$$

$$\therefore V_i = 0.5V = V_{TH}$$

## Multivibrator

$\rightarrow$  It is a device whose o/p vibrates two levels, i.e., low level and high level (or 0 and 1).

$\rightarrow$  These are of three types -

(i) Bistable

0	stable
$\downarrow$ with trigger	
1	stable
$\downarrow$ with trigger	
0	

(ii) Monostable

0	stable
$\downarrow$	
$\downarrow$	with trigger
0	1 (Quasi stable)
$\downarrow$	
1	0

(iii) Astable  $\rightarrow$  Free running Multivibrator

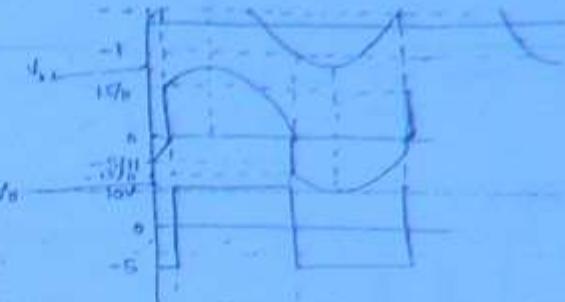
0	Quasi stable
$\downarrow$	0.5V trigger
1	Quasi stable
$\downarrow$	0.5V trigger
0	

Eg flip flop, binary, schmitt trigger

one shot or uni-vibrator

multivibrator

Square wave generator



For practical diodes

$$V_{ac} = 0.7V$$

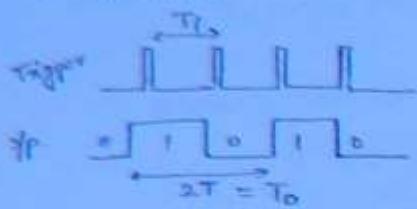
$$\therefore V_{2f} = 4.3V$$

with  $V_b$  not very high.

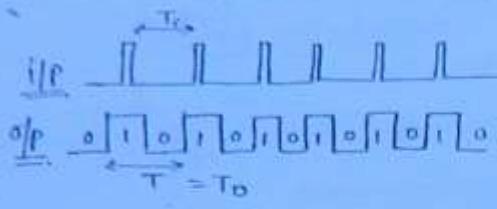
- \* when  $V_i \uparrow$  then  $V_b \downarrow$  and vice versa  $\rightarrow$  -ve feedback.
  - $V_i \uparrow \rightarrow V_b \uparrow$  and  $\downarrow$   $\rightarrow$  +ve feedback.

(64)

$\rightarrow$  Bistable :-



- Monostable :-



$\rightarrow$  Bistable and Monostable are simply converters and not square wave generators whereas for Astable Multivibrator, no need of an input / trigger to generate square wave.

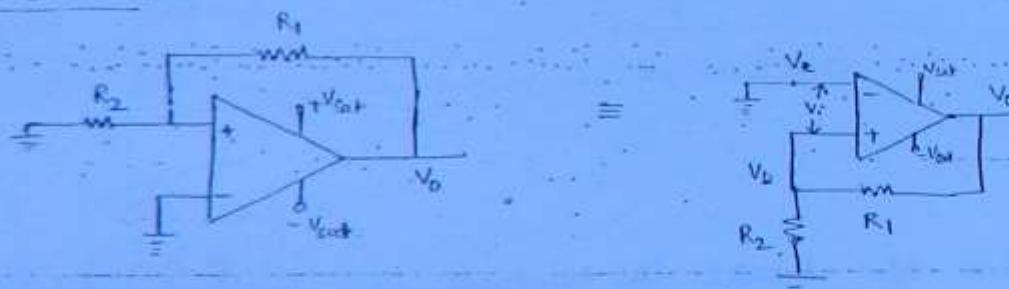
$\rightarrow$  Fastest o/p will be given by astable multivibrator as it is not limited by frequency of input trigger.

Note

- Astable multivibrator is often used as fastest waveform generator.

Multivibrator by using operational Amplifier :-

Bistable Multivibrator :-



$$V_b = \frac{R_2}{R_1+R_2} V_o$$

Initially,  $V_o = 0, V_b = 0, V_a = 0$

$\Rightarrow V_i = 0 \Rightarrow V_o = 0$  (Ideally).

but because of noise

$$V_b \uparrow \Rightarrow V_i = V_b - V_a \uparrow \Rightarrow V_o = \text{Av}_i \uparrow$$

$\Rightarrow V_b$  again  $\uparrow$  due to  $V_o$  and it will keep on  $\uparrow$  till it reaches  $+V_{sat}$  and then etc. will remain in one stable state.

→ Because of noise, op-amp initially can be at  $+V_{sat}$  or  $-V_{sat}$  depending on initial noise effect.

(65)

Eg Let  $R_1 = R_2$  and  $V_{sat} = 10V \Rightarrow V_b = 5V$ .

Now, to change the stable state of  $V_b$  from  $+V_{sat}$  to  $-V_{sat}$  -

(a) Positive trigger can be applied at  $V_a$  (or)

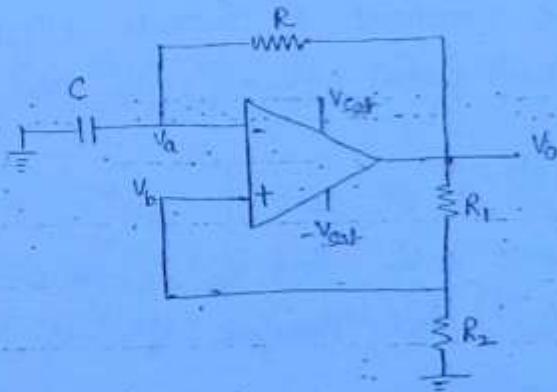
(b) Negative " " " " "  $V_b$  and trigger value should be more than the present  $V_b$  value. (i.e., more than 5V)

i.e; Eg  $\boxed{+5.5V}$  at b or  $\boxed{-2.5V}$  at a and the circuit will

switch to its other stable state.

- It has volatile memory i.e., memory is lost when power supply is interrupted.

### Astable Multivibrator / Square Wave Generator :-



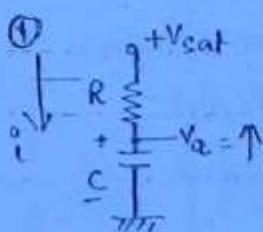
$$V_b = \frac{R_2}{R_1 + R_2} V_0 \quad \text{--- (1)}$$

At  $t=0$ ,

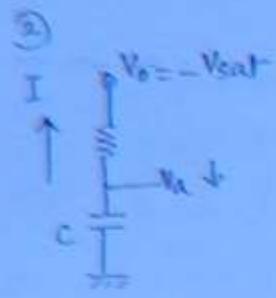
$V_0 = +V_{sat}$  (by noise).

$$V_b = \frac{R_2 V_{sat}}{R_1 + R_2} = V_T$$

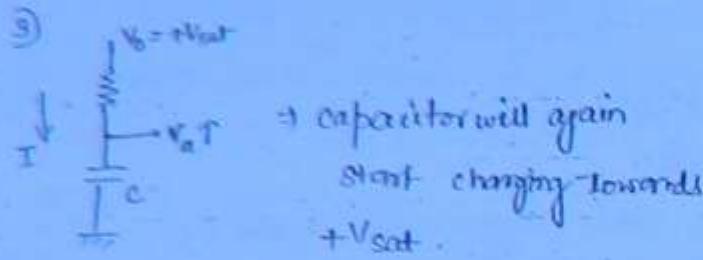
$\therefore V_b > V_a \Rightarrow V_0 = +V_{sat}$   $\Leftarrow$  but  $V_a = V_0 = 0$



→ capacitor will start charging and the voltage of terminal  $V_a$  will start increasing. As soon as  $V_a$  reaches  $V_T$ ,  $V_0$  will switch to  $-V_{sat}$ .



$\Rightarrow$  Capacitor will start discharging or start charging towards  $-V_{sat}$



$\Rightarrow$  Capacitor will again start charging towards  $+V_{sat}$ .

$\rightarrow V_o \rightarrow$  square wave

$V_b \rightarrow$  attenuated square wave.

$V_a \rightarrow$  approximate triangular wave

$\rightarrow$  Swing of  $V_o = +V_{sat}$  to  $-V_{sat}$

" "  $V_a \& V_b = -V_T$  to  $+V_T$

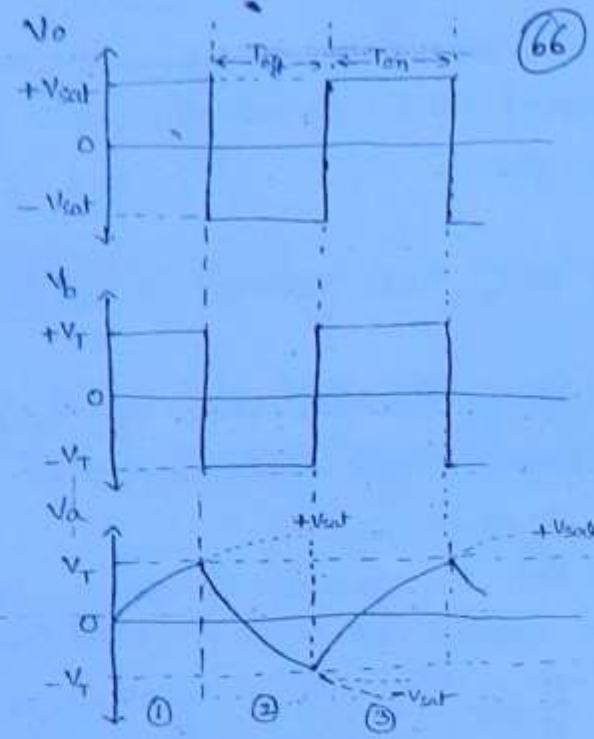
$\rightarrow$  At initial condition, consider  $V_o = +V_{sat}$ ,  $V_b = +V_T$  and  $V_a = V_c = 0$ .

Now, capacitor will charge by time constant  $RC$  towards  $+V_{sat}$ .

Capacitor will charge upto  $+V_T$  at this point  $V_a = V_b = +V_T$  and op-amp comes out of saturation.

$\rightarrow$  When the capacitor further charges above  $+V_T$  then  $V_a > V_b$  as a result of which  $V_o$  switch over to  $-V_{sat}$  and therefore  $V_b = -V_T$ .

$\rightarrow$  Now, capacitor starts discharging from  $+V_T$  to  $-V_T$  towards  $-V_{sat}$  with time constant  $RC$ . Thus, when capacitor discharge upto  $-V_T$ , then  $V_a = V_b = -V_T$  and op-amp comes out of saturation. When the capacitor further discharges below  $-V_T$ , then  $V_a < V_b$ , as a result of which  $V_o$  will switch over to  $+V_{sat}$  and again  $V_b = +V_T$ . and thus, the cycle will repeat.



Derivation of  $T_{on}$ :

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capacitor charges from  $-V_T$  to  $V_T$  in time  $T_{on}$ .

$$V_C = V_a = V_f - [V_f - V_i] e^{-t/RC}$$

$$V_i = -V_T \text{ at } t=0, \quad V_f = +V_{sat} \text{ at } t=\infty.$$

$$\therefore V_C = V_{sat} - [V_{sat} + V_T] e^{-t/RC} \rightarrow \text{charging eqn}$$

At  $t = T_{on}$ :

$$V_T = V_{sat} - [V_{sat} + V_T] e^{-T_{on}/RC}$$

$$\Rightarrow T_{on} = RC \ln \frac{V_{sat} + V_T}{V_{sat} - V_T}$$

Derivation of  $T_{off}$ :

capacitor discharges from  $V_T$  to  $-V_T$  in time  $T_{off}$ .

$$V_i = V_T \text{ at } t=0, \quad V_f = -V_{sat} \text{ at } t=\infty.$$

$$\therefore V_C = -V_{sat} - [-V_{sat} - V_T] e^{-t/RC}$$

$$\Rightarrow V_C = -V_{sat} + [V_{sat} + V_T] e^{-t/RC} \rightarrow \text{discharging eqn.}$$

At  $t = T_{off}$ ,  $V_C = -V_T$ ,

$$\therefore -V_T = -V_{sat} + [V_{sat} + V_T] e^{-T_{off}/RC}$$

$$\Rightarrow T_{off} = RC \ln \frac{V_{sat} + V_T}{V_{sat} - V_T}$$

$$T_{off} = T_{on}$$

$$\Rightarrow \text{Duty cycle} = 50\%$$

Square wave generator.

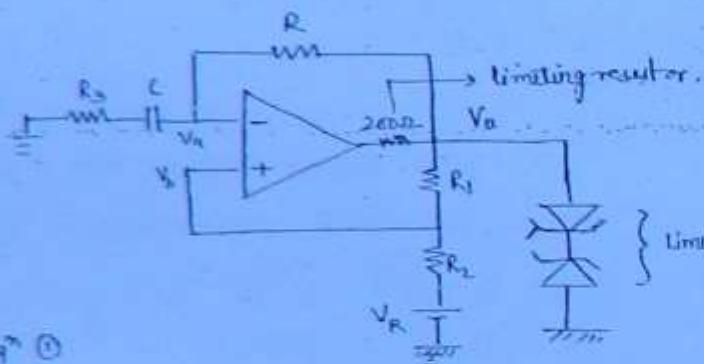
→ Astable multivibrator does not have any stable states due to continuous charging & discharging of  $C$ .

→ Time period,  $T = 2RC \ln \frac{V_{sat} + V_T}{V_{sat} - V_T}$  — [Eqn ①]

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$$\because V_T = \frac{R_2}{R_1 + R_2} V_{sat} \Rightarrow T = 2RC \ln \left[ 1 + \frac{2R_2}{R_1} \right] \quad \text{eqn ②}$$

→  $f = \frac{1}{T}$  → frequency of square wave generated.



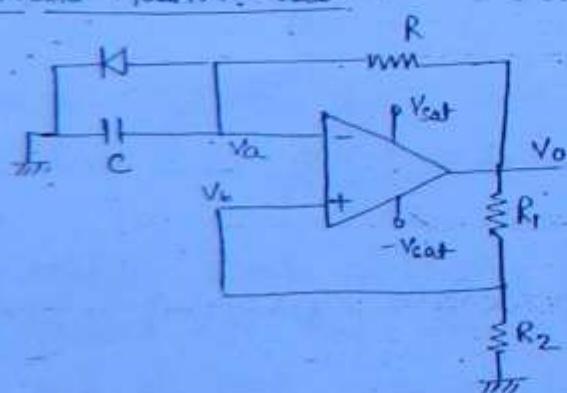
- \* eqn ①
- \* Time period / charging of  $C$  is non linear, therefore to make it linear, we can use a current mirror circuit in place of 'R'.

→ New time constant for charging / discharging of  $C$  —  $\tau = (R+R_3) \cdot C$ .

Limiting diode → Duty cycle can be altered by  $V_R$ .

$V_o = \pm V_{sat}$  can be altered using limiting diodes; i.e., final voltage states of charging & discharging of  $C$  can be changed.

### Monostable Multivibrator



$t < 0$  → ckt is in stable state.

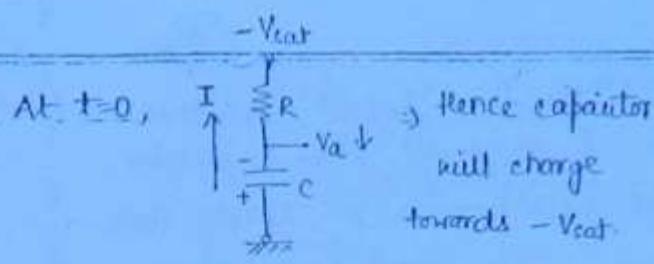
Let  $V_o = +V_{sat} \Rightarrow$  Diode → ON

$\Rightarrow V_a = V_c = 0$

$$V_b = \frac{R_2}{R_1 + R_2} V_{sat} = +V_T \gg V_a = 0 \Rightarrow \therefore V_o = +V_{sat}$$

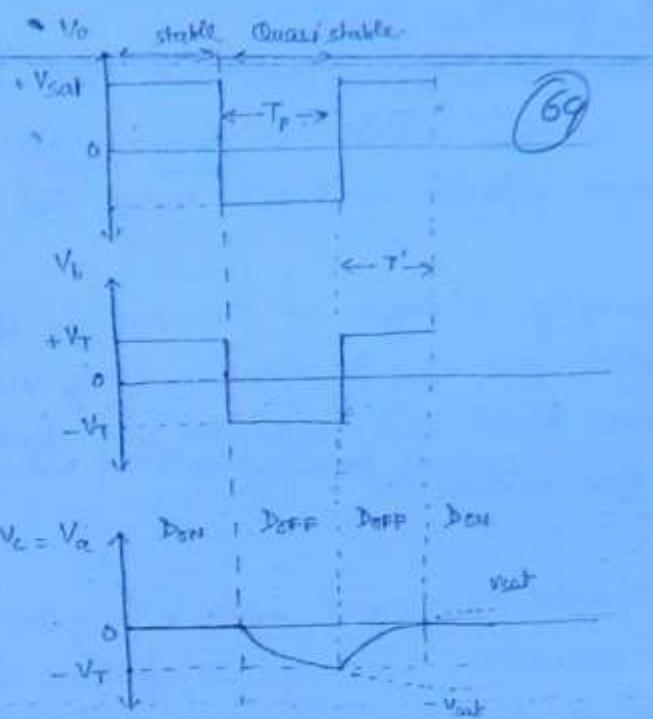
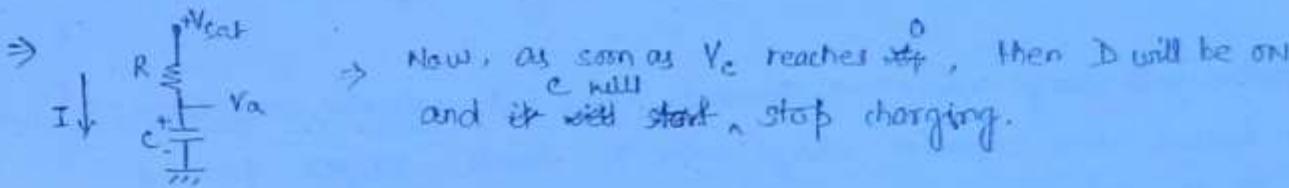
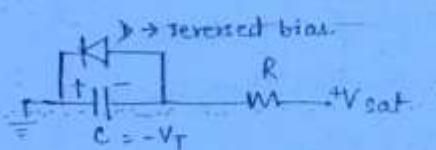
→ At  $t=0$ , if at  $b$  is given. so that  $V_b < V_a$

$\Rightarrow V_o = -V_{sat}$  and Diode = OFF



Once,  $V_c$  reaches  $-V_T$ ,  $V_o$  will switch to  $+V_{sat}$  but capacitor does not allow sudden change of voltage.

and it will start charging towards  $+V_{sat}$ , since diode will be 'off'.



22<sup>nd</sup> August, 2012 :-

Derivation of  $T_p$  - (Pulse width) -

C discharges from  $0$  to  $-V_T$ .

$$-V_c = V_a \Rightarrow -V_{sat} = [-V_{sat} - e^{-tRC}]^{t=0}$$

$$\Rightarrow V_c = V_{sat} [1 + e^{-tRC}]$$

At  $t = T_p$ ,  $V_c = -V_T$

$$\Rightarrow -V_T = -V_{sat} [1 - e^{-T_p RC}]$$

$$\Rightarrow T_p = RC \ln \frac{V_{sat}}{V_{sat} - V_T}$$

$$\Rightarrow T_p = RC \ln \left[ 1 + \frac{R_2}{R_1} \right]^{**}$$

when  $R_2 = R_1$ ,

$$\text{then } T_p = RC \ln 2 = 0.63 RC$$

Exp<sup>n</sup> for  $T'$  :-

$$T' = RC \ln \frac{V_{sat} + V_T}{V_{sat}}$$

when  $R_1 = R_2$ ,

$$T' = RC \ln (3/2).$$

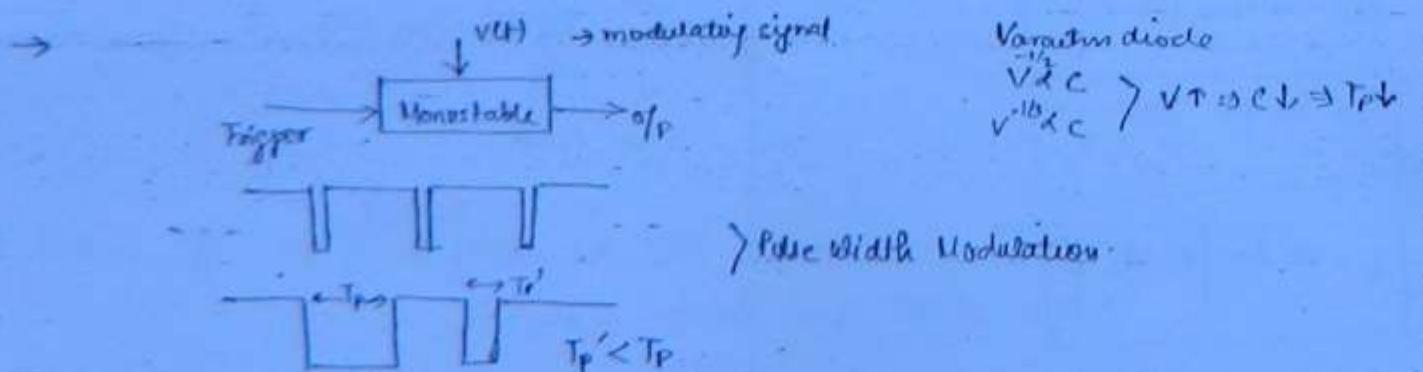
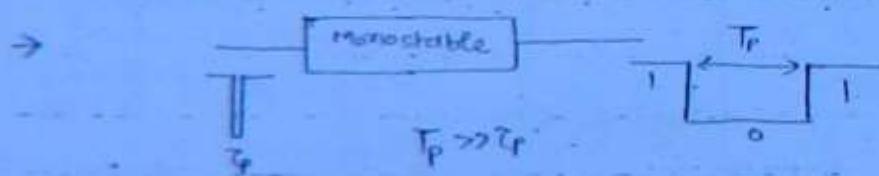
### Circuit Spentien.

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- For TCO, circuit is in stable state with  $V_o = +V_{sat}$ ,  $V_b = +V_T$ ,  $V_c = V_a = 0$ .
  - Since  $V_b = +V_{sat}$ , diode is forward biased & short ckt the capacitor, therefore capacitor will not charge and ckt will remain in stable state.
  - Now we apply -ve trigger at  $t=0$  and for short interval,  $V_b < V_a$  and  $V_o$  will switch from  $+V_{sat}$  to  $-V_{sat}$  and  $V_b$  switch from  $+V_T$  to  $-V_T$ . Now diode is reverse biased and capacitor will discharge below 0 towards  $-V_{sat}$  with a time constant  $RC$ .
  - When capacitor discharged upto  $-V_T$ , then  $V_a = V_b = -V_T$  and op-amp comes out of saturation, when capacitor further discharges below  $-V_T$ , then  $V_a < V_b$  as a result of which  $V_o$  switch over to  $+V_{sat}$  and again  $V_b = +V_T$ .
  - Now the capacitor will charge above  $-V_T$  towards  $+V_{sat}$  but capacitor can charge only upto 0 because when capacitor charge above 0, diode becomes forward biased and se the capacitor.

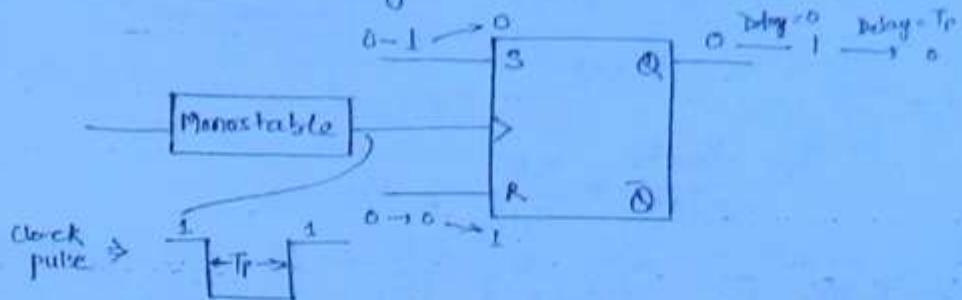
### Application :-

- It is used as Pulse stretcher circuit.

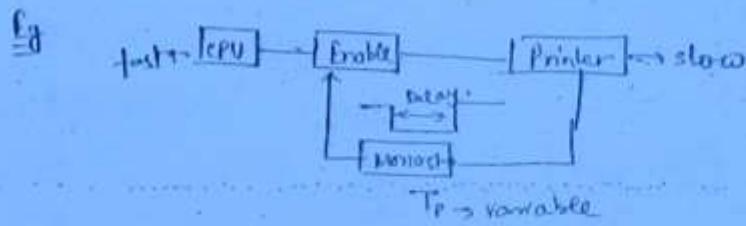


It is used as a delay element.

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↳ Delay (reqd. for synchronization fast & slow peripheral devices)



### Triangular Wave Generator :-

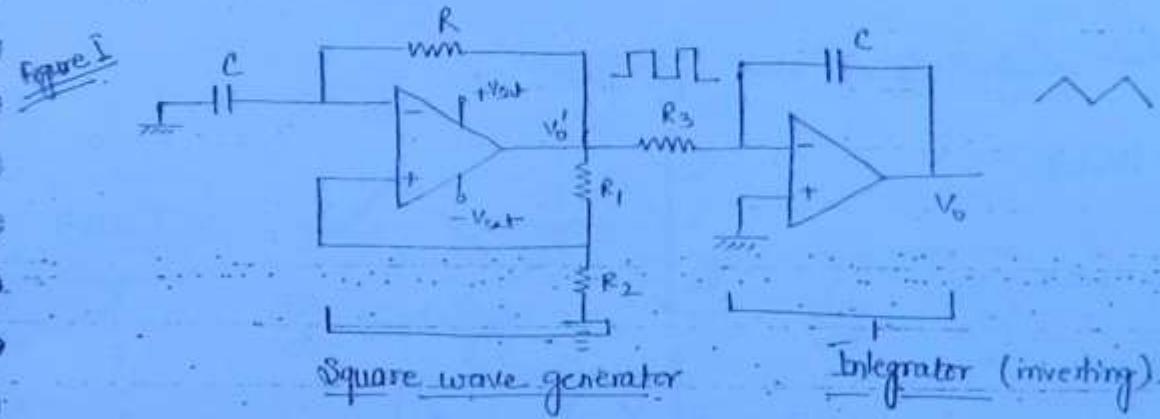
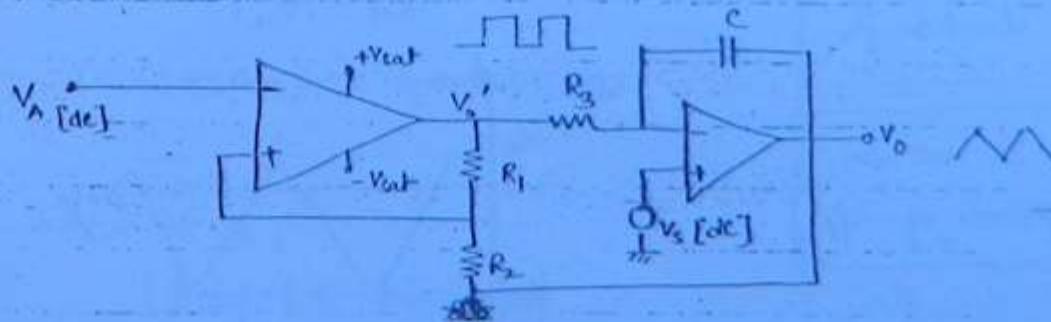
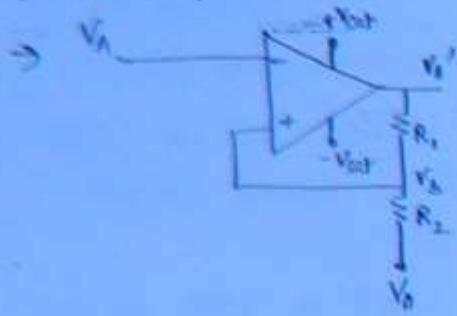


Figure II



→ Figure 2 is better than fig. 1 due to less no. of components.

Variation of  $V_A$  :-



$$V_b = \frac{V_o' R_2}{R_1 + R_2} + \frac{V_o R_1}{R_1 + R_2}$$

$$\textcircled{1} - V_o' = +V_{sat}$$

$$\Rightarrow V_o' = \frac{V_{sat} R_2}{R_1 + R_2} + \frac{V_o R_1}{R_1 + R_2} = V_A$$

$$\Rightarrow V_o = \frac{R_1 + R_2}{R_1} \left[ V_A - \frac{V_{sat} R_2}{R_1 + R_2} \right] = \text{lower amplitude of } \Delta \text{ wave.}$$

\textcircled{2} -  $V_o = -V_{sat}$

$$\Rightarrow V_{o2} = -\frac{V_{sat} R_2}{R_1 + R_2} + \frac{V_o R_1}{R_1 + R_2} = V_A$$

$$\Rightarrow V_o = \frac{R_1 + R_2}{R_1} \left[ V_A + \frac{V_{sat} R_2}{R_1 + R_2} \right] = \text{upper amplitude of } \Delta \text{ wave.}$$

Case I - when  $V_A = 0$ ,  $V_{o1} = \frac{R_2}{R_1} V_{sat}$ ,  $V_{oL} = -\frac{R_2}{R_1} V_{sat}$

$$|V_{o1}| = |V_{oL}|$$

Case II

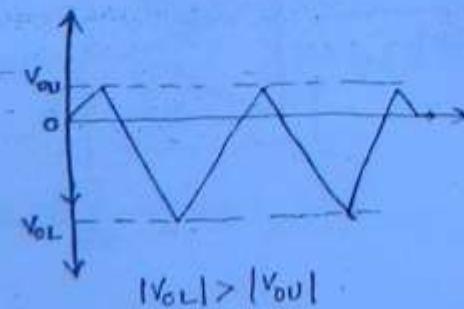
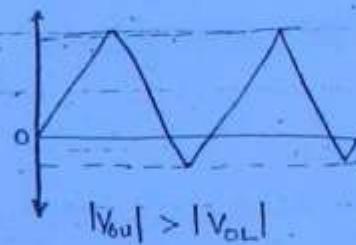
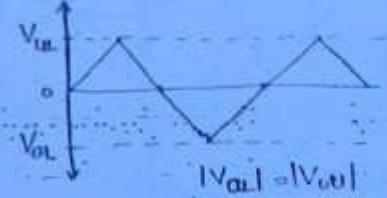
when  $V_A \uparrow$ , waveform will move

in upward direction.

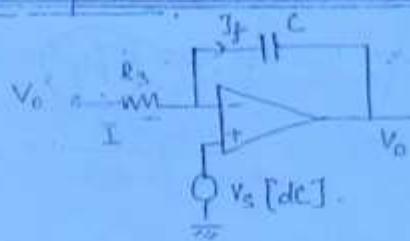
Case III when  $V_A \downarrow$ , waveform will move  
in downward direction -

Hence, by changing  $V_A$ , we can  
control the amplitude of o/p.

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### Variation of $V_o$



$$V_P = V_N = V_S.$$

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$$I_f = I.$$

$$C \frac{d}{dt} (V_S - V_o) = \frac{V_o' - V_o}{R_3}$$

$$\Rightarrow \text{Since } V_s = \text{dc} \Rightarrow \frac{dV_s}{dt} = 0 \Rightarrow -C \frac{dV_o}{dt} = \frac{V_o' - V_o}{R_3}$$

$$\Rightarrow \frac{dV_o}{dt} = -\frac{[V_o' - V_o]}{R_3 C} \quad \text{--- (1)}$$

$\rightarrow$  When  $V_o' = +V_{sat}$ ,

$$\frac{dV_o}{dt} = -\frac{[V_{sat} - V_o]}{R_3 C} = -\text{ve slope} \quad (\because V_{sat} > V_o) \\ = \text{constant.} \quad \{ \because V_{sat}, V_s, R_3, C = \text{constants}$$

$\Rightarrow V_o \downarrow \text{linearly}$

$\rightarrow$  When  $V_o' = -V_{sat}$ ,

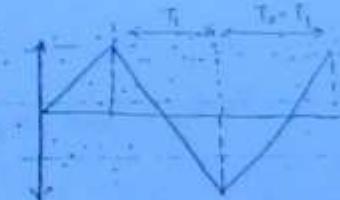
$$\frac{dV_o}{dt} = \frac{V_{sat} + V_o}{R_3 C} \quad \text{--- (2)} \Rightarrow V_o \uparrow \text{linearly}$$

Case I :-  $V_s = 0$ , in (2) & (3) -

$$\frac{dV_o}{dt} = -\frac{V_{sat}}{R_3 C}$$

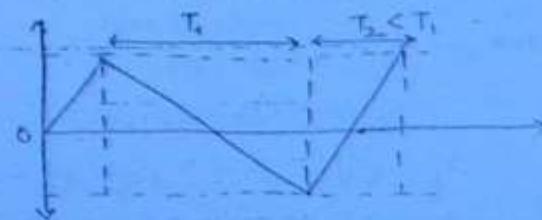
$$\frac{dV_o}{dt} = \frac{+V_{sat}}{R_3 C}$$

$$\Rightarrow |\downarrow \text{slope}| = |\uparrow \text{slope}|.$$



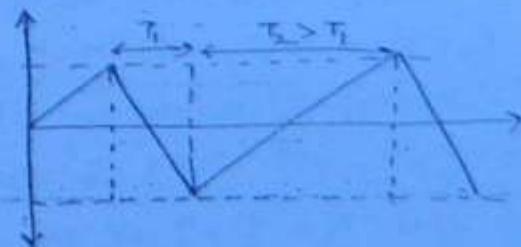
Case II :- When  $V_s > 0$ ,

$$|\uparrow \text{slope}| > |\downarrow \text{slope}|.$$



Case III When  $V_s < 0$ ;

$$|\uparrow \text{slope}| < |\downarrow \text{slope}|$$



Hence by varying  $V_s$ , we can change the slope of op.

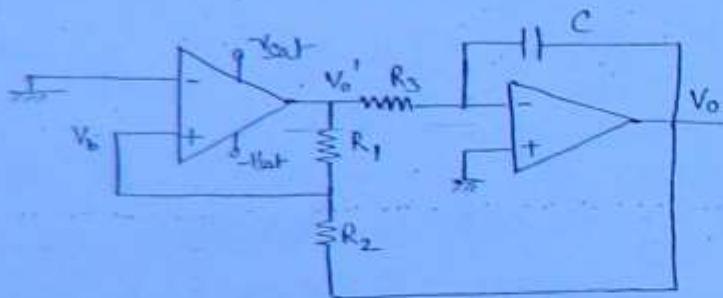
Eg for  $V_A = 2V$ ,  $V_S = -2V$ .

$\Rightarrow \because V_A = +ve \Rightarrow |V_{OU}| > |V_{OL}|$

$V_S = -ve \Rightarrow |\downarrow slope| > |\uparrow slope|$



Symmetrical Triangular Wave (with  $V_A = 0$  and  $V_S = 0$ ) :-



① If  $V_o' = +V_{sat}$ ,  $V_o$  will  $\downarrow$  with slope  $\frac{dV_o}{dt} = -\frac{V_{sat}}{R_3 C}$  upto  $V_{OL} = -\frac{R_2}{R_1} V_{sat}$ .

$$V_{OL} = -\frac{R_2}{R_1} V_{sat}.$$

② When  $V_o' = -V_{sat}$ ,  $V_o$  will  $\uparrow$  with slope  $\frac{dV_o}{dt} = \frac{+V_{sat}}{R_3 C}$  upto  $V_{OU} = \frac{R_2}{R_1} V_{sat}$ .

Eg : let  $R_1 = R_2$  and  $V_{sat} = 10V$ .

$$V_b = \frac{R_2}{R_1 + R_2} V_o' + \frac{R_1}{R_1 + R_2} V_o, = \frac{V_o'}{2} + \frac{V_o}{2}$$

due to noise.

let at  $t=0$ ,  $V_o' = +V_{sat} = 10V$

$V_o = 0$  due to C?

$\therefore V_b = 5V > V_a = 0 \Rightarrow V_o' = V_{sat}$

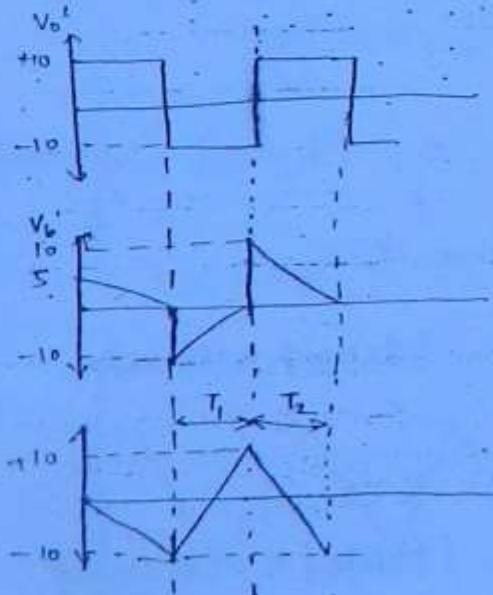
$\therefore V_o' = +V_{sat}$ ,  $V_o \downarrow$  and in turn  $V_b \downarrow$ .

when  $V_o = V_{OL} = -10V$ ,  $V_b = \frac{10 - 10}{2} = 0V$ .

Now,  $\because V_b \leq V_a$ ,  $V_o'$  switches from  $+V_{sat}$  to  $-V_{sat}$ .

$V_b = -\frac{10 - 10}{2} = -10V$  and  $\because V_o' = -V_{sat}$ ,  $\therefore V_o$  will  $\uparrow$  and  $V_b \uparrow$  and when  $V_o = V_{OU} = 10V$

then  $V_b = \frac{-10 + 10}{2} = 0V$  and  $\because V_b \geq V_a$ ;  $V_o'$  switches from  $-V_{sat}$  to  $+V_{sat}$  and cycle will be repeated.



Calculation of  $T_1$  and  $T_2$ :

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$$\text{Time} = \frac{\text{Change}}{\text{Rate of change}} = \frac{V_{\text{final}} - V_{\text{initial}}}{\text{slope}}$$

$$\Rightarrow T_1 = \frac{V_{\text{ou}} - V_{\text{OL}}}{dV_o/dt} = \frac{R_2/R_1 V_{\text{sat}} - \left(-\frac{R_2}{R_1}\right) V_{\text{sat}}}{V_{\text{sat}}/R_3 C} \Rightarrow T_1 = \frac{2R_2 R_3 C}{R_1}$$

$$T_2 = \frac{V_{\text{OL}} - V_{\text{ou}}}{dV_o/dt} = \frac{-\left(\frac{R_2}{R_1}\right)V_{\text{sat}} - \left(\frac{R_2}{R_1}\right)V_{\text{sat}}}{-V_{\text{sat}}/R_3 C} \Rightarrow T_2 = \frac{2R_2 R_3 C}{R_1}$$

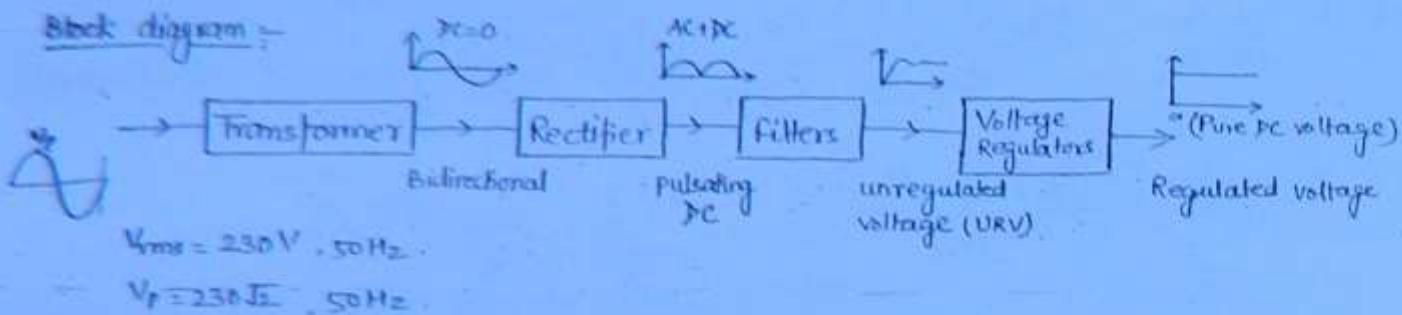
$\rightarrow \because T_2 = T_1 \quad \therefore V_o = \text{symmetrical triangular wave.}$   
 $V_o' = \text{symmetrical square wave.}$

$$\rightarrow \text{Time period} = T = T_1 + T_2 \Rightarrow \frac{4R_2 R_3 C}{R_1} = T \quad \text{or} \quad f = \frac{R_1}{4R_2 R_3 C}$$

## Diode Circuit :-

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### \* Rectifiers:-



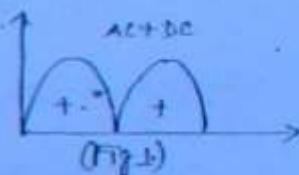
- Basic purpose of a rectifier is to convert a bidirectional voltage or current waveform into unidirectional voltage or current waveform.

### Important terms :-

- Average or DC Level,  $I_{dc} = \frac{1}{2\pi} \int_0^{2\pi} I(t) dt$ .

- RMS value,  $I_{rms} = \left[ \frac{1}{2\pi} \int_0^{2\pi} I^2(t) dt \right]^{1/2}$ .

### Ripple Voltage :-



$$V = V_{ac} + V_{dc}$$

$V_{dc}$  = dc value of o/p

$V_{rms}$  = RMS value of o/p.

$V_{acms}$  = RMS value of ac component

$$V_{rms} = \sqrt{V_{dc}^2 + V_{acms}^2}$$

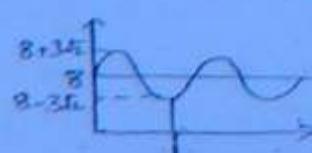
$$\Rightarrow V_{acms} = \sqrt{V_{rms}^2 - V_{dc}^2}$$

Let  $V_{dc} = 8V$  and  $V_{acms} = 3V \Rightarrow V_{acpm} 3\sqrt{2}$

$$\therefore V = 8 + 3\sqrt{2} \sin \omega t$$

↓  
Ripple

(Variation of o/p voltage from pure dc)



→ Mathematical representation,

(Ripple superimposed on dc)

fig. 1. is actual representation.

- It is the deviation of op voltage from its dc value. The waveform after rectification is not pure dc. It has an ac component called Ripple superimposed on dc.

→ Ripple factor :-  $\tau = \frac{\text{rms value of ac component}}{\text{dc value}}$

$$\Rightarrow \tau = \left| \frac{V_{\text{ac rms}}}{V_{\text{dc}}} \right|^{\frac{1}{2}} ; \text{ ideally } V_{\text{ac rms}} = 0 \text{ or } T = 0$$

$$\Rightarrow \tau = \sqrt{V_{\text{rms}}^2 - V_{\text{dc}}^2} \Rightarrow \tau = \sqrt{\left( \frac{V_{\text{rms}}}{V_{\text{dc}}} \right)^2 - 1}$$

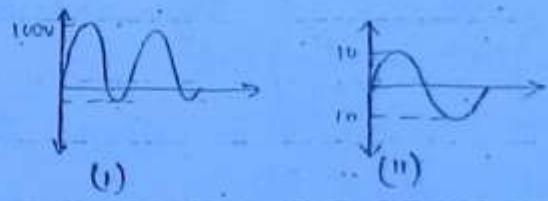
→ form factor :-

$$f = \frac{V_{\text{rms}}}{V_{\text{dc}}} \Rightarrow \tau = \sqrt{f^2 - 1}$$

$$[\text{ideally, } f = 1]^*$$

→ Crest factor :-  $C = \frac{\text{Peak value}}{\text{RMS value}}$

- It should be as low as possible



Ex.  $\Rightarrow$  RMS is same for both then (i) signal should be preferred since peak is ↓ and circuit elements will have to be designed accordingly.

→ Peak Inverse Voltage (PIV)

- It is the max voltage across the diode in reverse direction, i.e., when the diode is reverse biased.
- Diode is selected on the basis of PIV rating.
- PIV should be as low as possible.
- We can ↑ PIV of ckt by cascading two or more diodes in series.

Rectifier Efficiency :-  $\eta = \frac{\text{o/p dc power}}{\text{i/p ac power}} \times 100\%$

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Transformer Utilization factor (TUF) :-

- It indicates how much is the utilization of transformer in the circuit.
- It should be as  $\uparrow$  as possible.

Type of Rectifiers:

- I Half wave Rectifier
- II Full wave rectifier - (a) Center tapped transformer type  
(b) Bridge Rectifier.

Workbook

Chap. 10.

1)  $(Av)_{dB} = 20 \log Av = 80$   
 $\Rightarrow Av = 10^4$ .

6. BW =  $Av \times BW = 20 \times 10^4 = 200 \text{ kHz}$ .

2)  $\frac{10k}{1k} = \frac{10k}{1k} \Rightarrow CMRR = \infty \Rightarrow A_C = 0$

$\Rightarrow V_o = A_d (V_1 - V_2)$   
 $\Rightarrow V_o = 0$

Alternate  
 Find the point  
 where 20dB/dec.  
 is intersecting freq.  
 axis.

3)  $V_m = V_p = 2V$

$\therefore I_E = \frac{10^{-2}}{1k} = 8 \text{ mA}$   
 $I_B = \text{negligible}$

$\Rightarrow I_E = I_C = 8 \text{ mA}$

$\rightarrow 3(a)$ .

4)  $V_o = \left(1 + \frac{4.14}{10}\right) \cdot V_p = \sqrt{2} \cdot V_p ; V_p = \left(\frac{1/C_S}{R + 1/C_S}\right) \sin t = \left(\frac{1}{1+j}\right) \sin t$

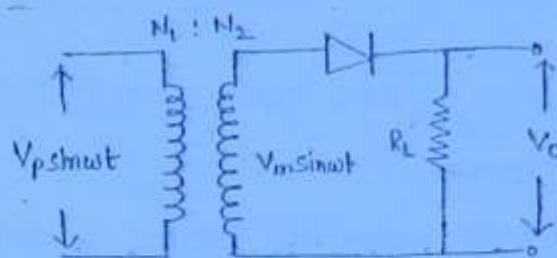
$\Rightarrow V_o = \frac{\sqrt{2} \sin(t)}{(1+j)} \Rightarrow V_o = \sin(t - \pi/4)$ .

5) (c). 6)  $V_o = \left(1 + \frac{2R}{R}\right) \left[ \frac{\sin(100t) + 2 - 2}{2} \right] = \frac{3}{2} \sin(100t)$

10)  $CMRR = \frac{A_d}{A_C}, 1. error = \frac{A_C V_C \times 100}{A_d V_d} = \frac{1}{1000} \times \frac{10 \times 100}{1} = 1\%$ .

23/08/2012

### Half-Wave Rectifier :-



(79)

$$\frac{V_p}{V_m} = \frac{N_1}{N_2}$$

Assuming ideal circuit,

- ①  $V_i \geq 0$ ,  $\Rightarrow$  FB  $\rightarrow$  short ckt.
- ②  $V_i < 0$ ,  $\Rightarrow$  RB  $\rightarrow$  Open ckt

When D  $\rightarrow$  ON -  $I_L = \frac{V_m \sin \omega t}{R_L}$

$\rightarrow I_{m'} = \frac{V_m}{R_L}$  = max. or peak current through  $R_L$

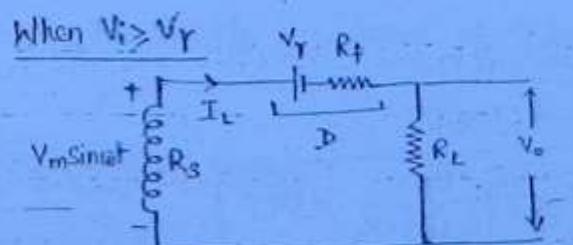
$\rightarrow V_o = I_L R_L = V_m \sin \omega t$  (ideal case).

$\rightarrow$  When D  $\rightarrow$  OFF,  $I_L = 0 \Rightarrow V_o = 0$

$\rightarrow$  The o/p frequency or ripple frequency =  $f_r = \text{supply frequency } f$

$\rightarrow$  Conduction angle  $\phi = \pi$  or  $180^\circ$ .

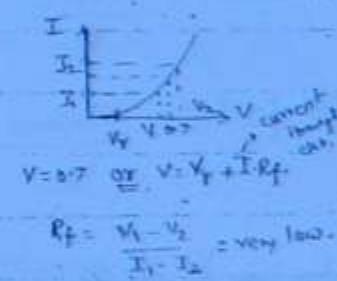
- Practical circuit -
- ①  $V_i \leq V_F$ , D  $\rightarrow$  OFF  $\rightarrow$  RB
  - ②  $V_i \geq V_F$ , D  $\rightarrow$  ON  $\rightarrow$  FB



$$I_L = \frac{V_m \sin \omega t - V_F}{R_s + R_f + R_L}$$

\*  $V_D = V_F + I R_f \approx 0.7V$  for Si

$R_s$  = Resistance of secondary coil



$$I_{m'} = \text{max. current} = \frac{V_m - V_F}{R_s + R_f + R_L} < I_m \text{ (ideal)}$$

When  $V_L < V_T$  —  $\Rightarrow \text{off} = I_L = 0$

(88)

When  $D = \text{ON}$ ,  $V_m^+ = I_m / R_L$  —

→ Ripple frequency will remain same as ideal case.

→ Conduction angle  $\left[ \phi = \pi - 2\theta \right] \left\{ < 180^\circ \right\}$

$$V_m \sin \theta = V_T \Rightarrow \theta = \sin^{-1} \left( \frac{V_T}{V_m} \right)$$

- Average or dc level — (for half wave)

$$I_{dc} = \frac{1}{2\pi} \int_0^\pi I_m \sin \omega t \, d\omega t \Rightarrow I_{dc} = \frac{I_m}{\pi} \quad \text{— for Ideal}$$

Similarly,

$$V_{dc} = \frac{V_m}{\pi} \quad \text{— for Ideal}$$

- RMS value — (for half wave).

$$I_{rms} = \frac{I_m}{2}$$

$$V_{rms} = \frac{V_m}{2}$$

- form factor =  $\frac{V_{rms}}{V_{dc}}$   $\Rightarrow F = 1.57$

- Ripple factor =  $\frac{V_{ac rms}}{V_{dc}} = \sqrt{F^2 - 1} \Rightarrow \tau = 1.21$

- Crest factor =  $\frac{V_{peak}}{V_{rms}}$   $\Rightarrow C = 2$

-  $PIV = +V_m$  (Drop across  $R_L < 0$ ,  $\therefore I_L = 0$ )

$$\eta = \frac{4}{\pi^2} \cdot \frac{R_L}{R_L + R_f + R_S} \times 100\%$$

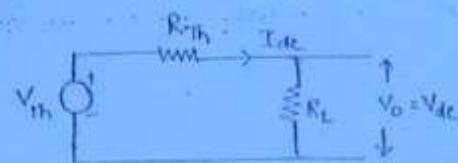
- Rectifier Efficiency,  $\eta = \frac{\text{eff de power}}{\text{ipp ac power}} \times 100\% \Rightarrow \eta = \frac{I_{dc}^2 \cdot R_L}{I_{rms}^2 (R_L + R_S + R_f)} \times 100\%$

$$\Rightarrow \eta = 0.406 \times \frac{1}{\frac{R_L + R_f + R_s}{R_L} + 1} \times 100\% \quad (81) \quad \left\{ \begin{array}{l} \eta = 40\%, \text{ means only } 40\% \\ \text{ac power is converted to dc} \end{array} \right.$$

If  $R_L \gg R_f + R_s$ , then  $\boxed{\eta_{max} = 40.6\%}$

If the efficiency is 40%, it means that 40% of ac power is converted into dc and remaining 60% (approx.) power is in form of ripple (ac component at off).

### Thevenin's equivalent of Half Wave Rectifier



$$I_{dc} = \frac{V_m}{R_{Th} + R_L} = 0$$

$$V_{dc} = I_{dc} \cdot R_L$$

$$I_{dc} = \frac{1}{2\pi} \int_0^{\pi/2} I_L \sin \omega t \, d\omega t; \theta = \sin^{-1}\left(\frac{V_r}{V_m}\right); I_L = \frac{V_m \sin \omega t - V_r}{R_s + R_f + R_L}$$

$$\text{Let } V_r = 0, \Rightarrow \theta = 0, I_L = \frac{V_m \sin \omega t}{R_s + R_f + R_L} = I_m' \sin \omega t.$$

$$\therefore I_{dc} = \frac{1}{2\pi} \int_0^{\pi/2} I_m' \sin \omega t \, d\omega t = \frac{I_m'}{\pi} = \frac{V_m}{\pi(R_s + R_f + R_L)} \quad (2)$$

Comparing (1) and (2) :-

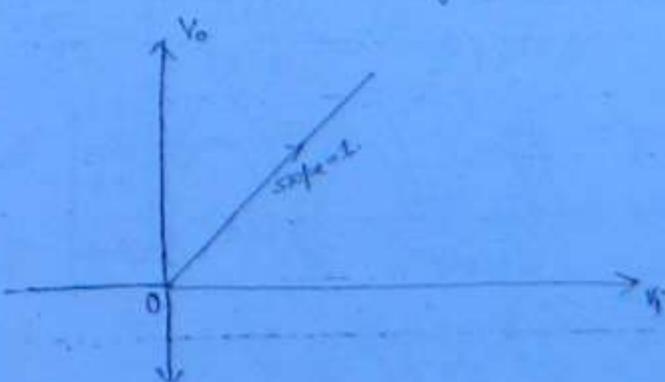
$$\boxed{V_{Th} = \frac{V_m}{\pi}, R_{Th} = R_s + R_f}$$

\*  $R_{Th}$  is the off resistance of ckt & it represents the losses occurring at off.

### Transfer Curve:-

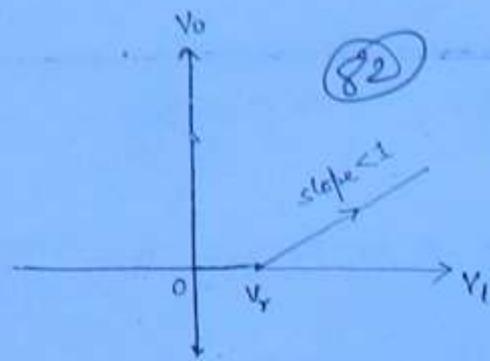
Ideal:

$V_i$	D	$V_o$
$V_i < 0$	off	0
$V_i > 0$	on	$V_i$



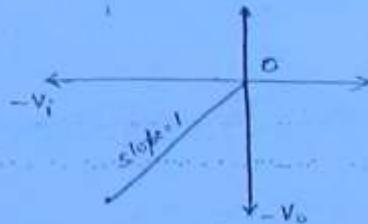
Practically

$V_i$	D	$V_o$
$V_i \leq V_f$	OFF	0
$V_i \geq V_f$	ON	$I_L R_L = \frac{(V_i - V_f)}{R_s + R_L + R_f} \times R_L$



$$\text{slope} = \frac{R_L}{R_s + R_L + R_f} < 1$$

\* If diode polarity is reversed, then charac. will come into III quadrant.



→ Transfer Utilization factor-

$$\boxed{\text{TUF} = 0.286} \rightarrow (\text{very low})$$

Ques: A HMR is supplied by a 230V, 50Hz supply with a step down ratio of 3:1 to a resistive load  $R_L = 10\text{ k}\Omega$ . If  $R_f = 75\Omega$  and  $R_s = 10\Omega$ , calculate—

- ① Max, average and rms value of current
- ② DC value of op. voltage
- ③ Efficiency.

$$\text{Soln} \quad V_m = \frac{230}{3} \text{ V} \Rightarrow V_m = \frac{230\sqrt{2}}{3} \text{ V.} = 108.4 \text{ V}$$

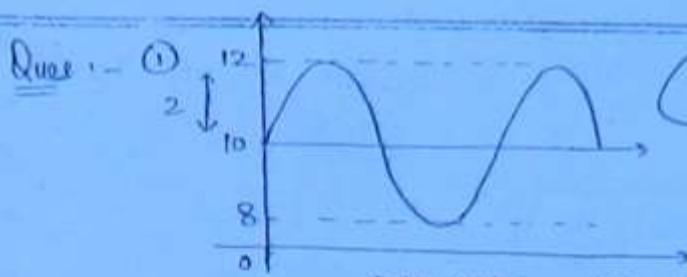
$\Rightarrow I_m = 0$  {as not mentioned}

$$I_m = \frac{V_m - V_f}{R_f + R_s + R_L} \approx \frac{230\sqrt{2}}{3(10\text{ k})} = \frac{23\sqrt{2}}{3} \text{ mA.} = 10.84 \text{ mA}$$

$$I_d = I_{avg} = \frac{I_m}{\pi} = \frac{23\sqrt{2}}{3\pi} \text{ mA} = 3.45 \text{ mA} \quad V_{dc} = I_{dc} \times R_L = \frac{23\sqrt{2} \times 10}{3\pi} \text{ V} = 34.5 \text{ V}$$

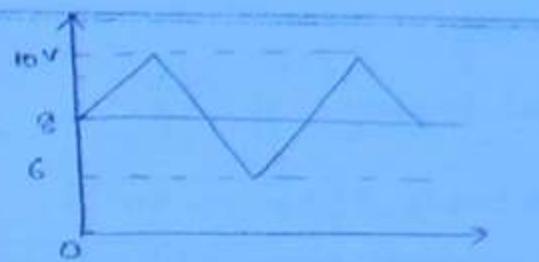
$$V_{rms} = \frac{I_m}{3 \times 10^3} \times \frac{23\sqrt{2}}{3} \text{ mA} = 11.66 \text{ mA} \quad \eta = \frac{0.406 \times 10}{10 + 9.5} \approx 40.6\%$$

$$I_{rms} = \frac{I_m}{2} = 5.42 \text{ mA.}$$



83

Calculate ripple factor  $\gamma$



$$\text{① } F = \frac{V_{\text{rms}}}{V_{\text{dc}}} = \frac{\sqrt{10^2 + (2/f_2)^2}}{10} = \frac{\sqrt{102}}{10}$$

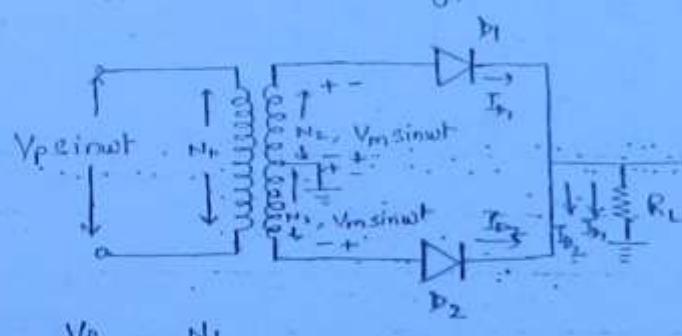
$$\therefore \gamma = \sqrt{F^2 - 1} = \frac{1}{5f_2}$$

$$\text{② } \gamma = \frac{V_{\text{ac rms}}}{V_{\text{dc}}} = \frac{(2/f_2)}{8} = \frac{1}{4f_2}$$

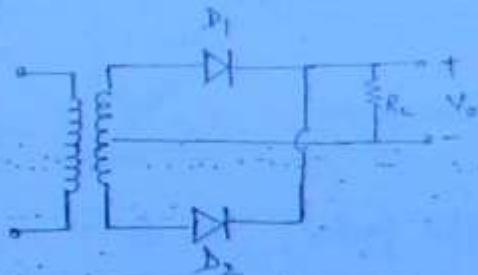
③  $\gamma = \frac{V_{\text{ac rms}}}{V_{\text{dc}}} = \frac{(2/f_2)}{8} = \frac{1}{4f_2}$

### Full Wave Rectifier :-

#### a) Center Tapped Transformer Type :-



$$\frac{V_p}{V_m} = \frac{N_1}{N_2}$$



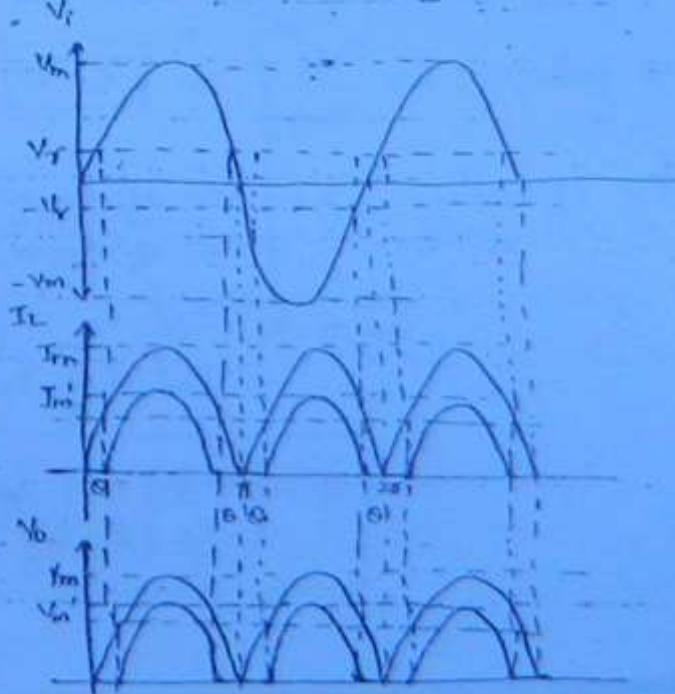
Ideally:  $I_L = \frac{V_m \sin \omega t}{R_L} = I_m \sin \omega t; I_m = \frac{V_m}{R_L}$

$$V_0 = I_L R_L = V_m \sin \omega t$$

→ Ripple frequency =  $f_r = 2f$  \*\*

→ conduction Angle =  $\phi = 2\pi$  \*\*

for individual diode,  $\phi = \pi$  \*\*



Practically:

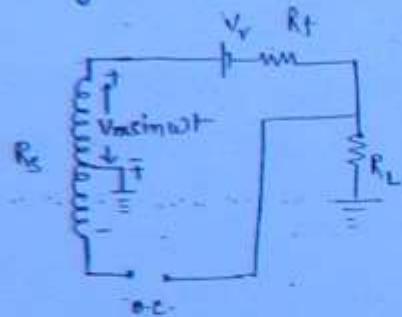
→ Ripple frequency,  $f_r = 2f$ .

→ for circuit,  $\phi = 2\pi - 4\theta$ ;  $\theta = \sin^{-1}\left(\frac{V_r}{V_m}\right)$

for individual diode,  $\phi = \pi - 2\theta$ .

(84)

→ During +ve cycle -



$$I_L = \frac{V_m \sin \omega t - V_Y}{R_f + R_L + \frac{R_s}{2}} \quad ; \quad I_{m'} = \frac{V_m - V_Y}{R_f + R_L + \frac{R_s}{2}} \quad (< I_m)$$

$$V_o = V_L \cdot I_L$$

$$\rightarrow I_{dc} = \frac{1}{\pi} \int_0^{\pi} I_m \sin \omega t \cdot d\omega t \Rightarrow I_{dc} = \frac{2I_m}{\pi}, \quad V_{dc} = \frac{2V_m}{\pi}$$

$$\rightarrow I_{m'my} = \frac{I_m}{\sqrt{2}}, \quad V_{m'my} = \frac{V_m}{\sqrt{2}}$$

$$\rightarrow \text{form factor, } f = \frac{V_m/\sqrt{2}}{2V_m/\pi} \Rightarrow F = 1.11$$

$$\rightarrow \text{Ripple factor, } \tau = \sqrt{F^2 - 1} \Rightarrow \tau = 0.48$$

$$\rightarrow \text{Grest factor, } C = \frac{V_m}{V_m/\sqrt{2}} \Rightarrow C = \sqrt{2}$$

$$\rightarrow \text{Rectifier Efficiency, } \eta = \frac{\text{dc op power}}{\text{ac up power}} \times 100\%$$

$$\Rightarrow \eta = \frac{I_{dc}^2 \cdot R_L}{I_{m'my}^2 \left( \frac{R_s}{2} + R_f + R_L \right)} \times 100\%$$

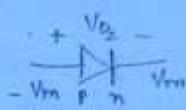
$$\Rightarrow \eta = \left( \frac{0.812 \times R_L}{\frac{R_s}{2} + R_f + R_L} \right) \times 100\%$$

$$\Rightarrow \eta = \left( 1 + \frac{R_f + \frac{R_s}{2}}{R_L} \right)^{-1} \times 100\%$$

$$\text{If } R_L \gg R_f + \frac{R_s}{2}$$

$$\Rightarrow \eta_{max} = 81.2\%$$

→ Peak Inverse voltage  $\Rightarrow$   $PIV = 2V_m$

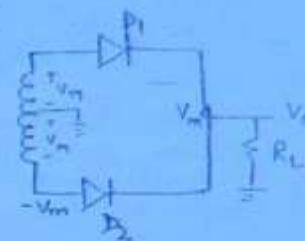


$$V_{D2} = V_p - V_n = -V_m - V_m$$

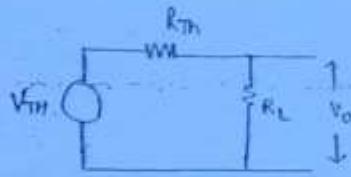
$$\Rightarrow V_{D2} = -2V_m$$

$$\Rightarrow PIV = 2V_m$$

{ When  $V_p > 0$   $D \rightarrow FB$   
 $V_p < 0$   $D \rightarrow RB$  }



Thevenin's Equivalent of FWR :-



$$R_{TH} = \frac{R_S + R_f}{2}$$

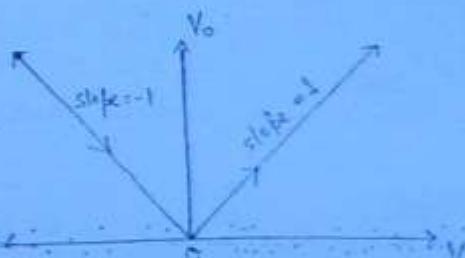
$$V_{TH} = \frac{2V_m}{\pi}$$

Transfer curve :-

Ideally :-

$V_i$	$D_1$	$D_2$	$V_o$
$V_i < 0$	off	on	$-V_i$
$V_i > 0$	on	off	$V_i$

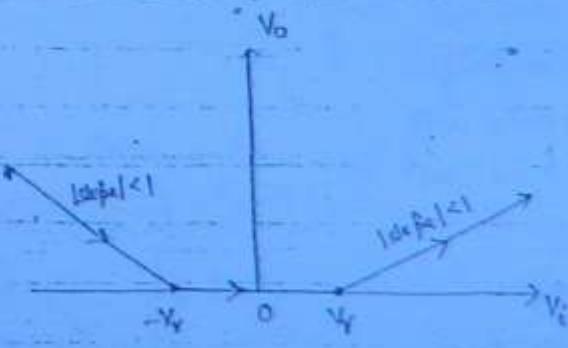
$$\therefore V_o = -V_i$$



Practically :-

$V_i$	$D_1$	$D_2$	$V_o$
$V_i \leq -V_r$	off	on	$V_o'$
$-V_r \leq V_i \leq V_r$	off	off	0
$V_i > V_r$	on	off	$V_o'$

$$V_o' = I_L R_L = \frac{V_i - V_r}{\frac{R_s + R_f}{2} + R_L} \times R_L$$



$$\text{slope} = \frac{R_L}{\frac{R_s + R_f}{2} + R_L} (< 1) \Rightarrow |\text{slope}| < 1$$

→ If the polarity of diodes is reversed, the transfer curve will be present in III and IV quadrant.

(86)

→ In practical condition, it is not possible to rectify very small signals using centre tapped Transformer.

e.g.  $V_i = 5 \sin \omega t \text{ mV}$   $\Rightarrow V_m = 5 \text{ mV} = 0.005 \text{ V} \ll V_T$ , hence op  
will be 0.

→ TUF ~  $\boxed{TUF = 0.693}$

→ Workbook

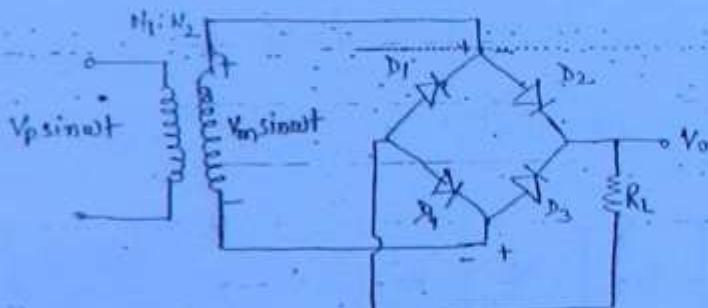
Pg 57 (Chap. 16).

(n)  $\Sigma$

24<sup>th</sup> August, 2012

### Bridge Rectifier

$$\frac{V_p}{V_m} = \frac{N_1}{N_2}$$



for positive half-

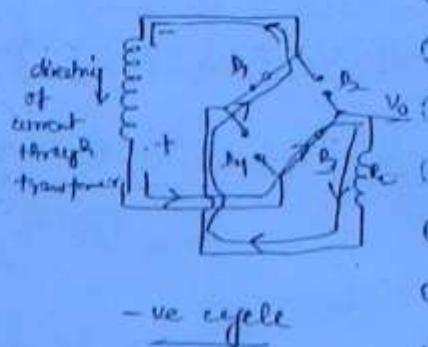
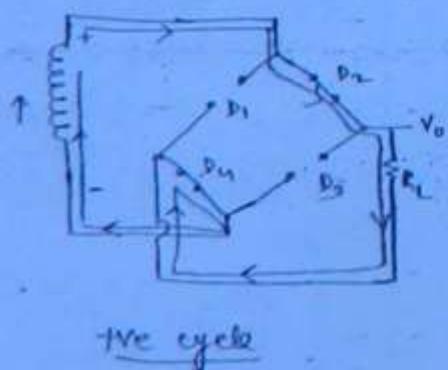
$D_1$  &  $D_3$  off.

$D_2$  &  $D_4$  on

for -ve half-

$D_1$  &  $D_3$  on

$D_2$  &  $D_4$  off.



- The current through transformer coil is bidirectional, hence avg. dc component is zero, which in turn results in minimum loss in  $\chi^{\text{core}}$ . (87)
- TUF is maximum for Bridge rectifier due to above mentioned reason.
- zero dc prevents the Eddy current, hysteresis losses and saturation of  $\chi^{\text{core}}$ .

### \* Ideally

$$\rightarrow V_{dc} = \frac{2V_m}{\pi}; I_{dc} = \frac{2I_m}{\pi}$$

$$\rightarrow I_{rms} = \frac{I_m}{\sqrt{2}}; V_{rms} = \frac{V_m}{\sqrt{2}}$$

$$\rightarrow r = 0.48$$

$$\rightarrow F = 1.11$$

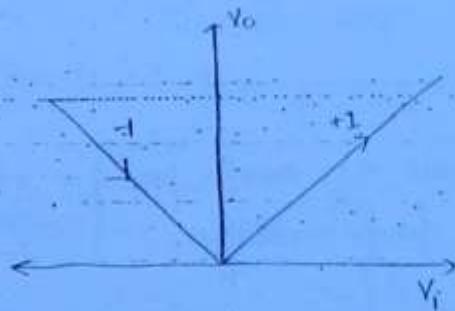
$$\rightarrow C = \sqrt{2}$$

$$\rightarrow \phi = 2\pi$$

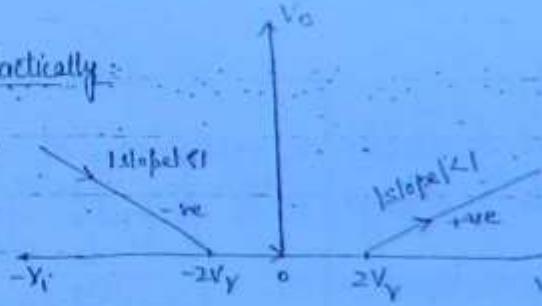
$\rightarrow$  Individual diode,  $\phi = \pi$

### Transfer Curve

#### Ideally:

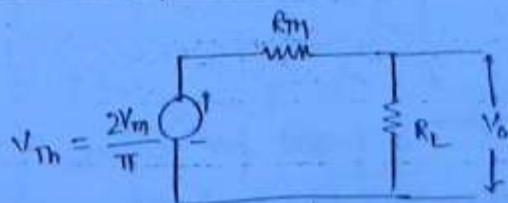


#### Practically:

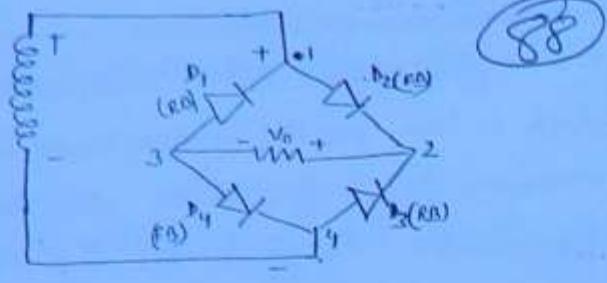
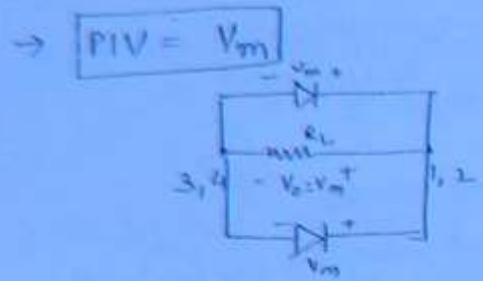


$$\text{Slope: } \frac{R_L}{R_S + 2R_F + R_L} (< 1)$$

### Thevenin's Equivalent:



$$Q_m = R_S + 2R_F$$



→  $\boxed{TUF = 0.812}$

### Advantages of Bridge Rectifier

- TUF is highest.
- Transformer can be replaced by ac source if step up/down of voltage is not required.
- PIV is smaller as compared to half-wave.
- Voltage required to deliver same power is smaller w.r.t half wave rectifier, hence no. of turns is more in HWR, hence the size of transformer used in Bridge rectifier is smallest.

### Disadvantage

- It cannot be used for rectification of small signals as cutoff voltage for response is  $2V_0$ , though it is preferable for high power ratings.

$V_m$	$2V_0$	loss
2	1	50%
10V	1	10%
20V	1	5%

### By Sir :- Disadvantages

- (i) The current in both primary & secondary of  $X^{mer}$  is present for entire cycle and hence for a given power o/p, power  $X^{mer}$  of a small size and less cost may be used.

→ No centre-tap is required in "x<sup>mer</sup>" secondary, hence whenever possible, ac voltage can directly be applied to bridge.

(89)

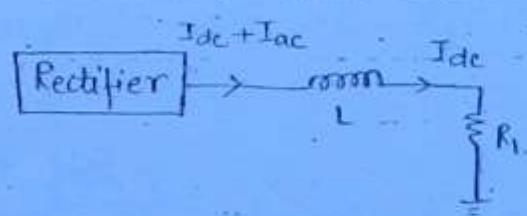
- The current in secondary of x<sup>mer</sup> is in opposite direction in two half cycles and hence net dc component through x<sup>mer</sup> coil is zero. Which reduces the losses and reduces the danger of saturation of x<sup>mer</sup>.
- As two diodes conduct in series, in each half cycle, inverse voltage appearing across the diode get shared hence the circuit can be used for high voltage applications. (since PIV is less)

### Filter Circuits :-

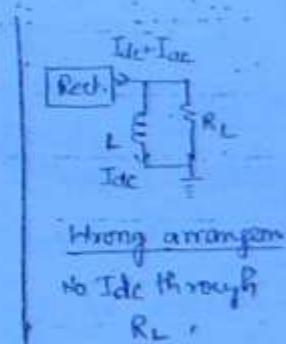
- To minimise ripple (ac component) at the o/p, filter circuits are used. We are using inductor & capacitor in filter circuits.

► Inductor :-  $|Z_L| = \omega L = 2\pi f L$

for dc,  $f=0 \Rightarrow Z_L = 0 \Rightarrow L$  acts as SC for dc.



$|Z_L|$  should be very high so that it blocks ac  
 $L \rightarrow$  very high



→  $L \uparrow$ , and/or  $f \uparrow \Rightarrow |Z_L| \uparrow \Rightarrow$  ac at o/p  $\downarrow \Rightarrow$  Ripple  $\downarrow \Rightarrow \tau \downarrow$

$$\tau \propto \frac{1}{fL} \quad \text{--- (1)}$$

→  $\tau \uparrow \Rightarrow \frac{L}{R_L} \uparrow \Rightarrow L \uparrow$  and  $R_L \downarrow \Rightarrow$  variation in current I.

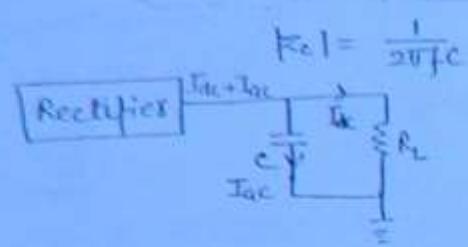
$$\Rightarrow \tau \downarrow$$

$$\therefore \tau \propto \frac{1}{C} \quad \text{--- (2)}$$

From (1) & (2) -

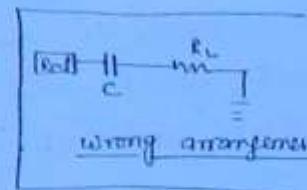
$$\tau \propto \frac{R_L}{fL}$$

2) Capacitance :-



(Q) - for dc,  $f=0 \Rightarrow Z_C = 0$ .

$\Rightarrow C$  acts as o.c. for dc.



-  $F_{cl}$  should be very high low for ac to bypass it

$$\Rightarrow [C \rightarrow \text{very high}]$$

-  $C \uparrow$  and/or  $f \uparrow \Rightarrow F_{cl} \downarrow \Rightarrow$  ac through  $R_L$   $\downarrow \Rightarrow$  ripple &  $r \downarrow$

$$\Rightarrow r \propto \frac{1}{C \cdot f}$$

$\Rightarrow \tau = R_L C \Rightarrow$  should be very  $\uparrow \Rightarrow$  variation in  $V \downarrow \Rightarrow$  ripple &  $r \downarrow$ .

$$r \propto \frac{1}{C} \Rightarrow \tau = R_L C \Rightarrow [C \uparrow \text{ and } R_L \uparrow] \text{ for } r \downarrow$$

$$\Rightarrow [r \propto \frac{1}{f C R_L}]$$

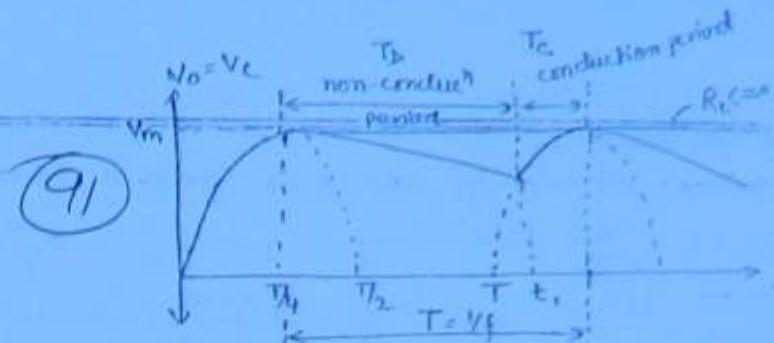
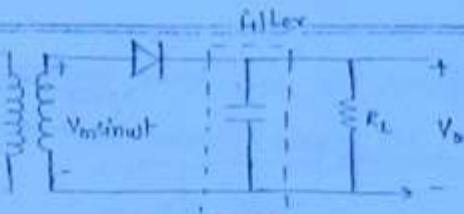
∴ Hence, for low load resistances, inductor is preferred and for high load resistances, capacitor is preferred.

Alternatively, for low load ( $R_L$  high) capacitor is preferred and for high load ( $R_L$  low), inductor is preferred.

Types of filter :-

- 1) Capacitor Filter
- 2) Choke or Inductor filter
- 3) L-section or L-C filter
- 4) T or CLC filter
- 5) T or CRC filter for compact circuit.

→ Capacitor filter :- HWR with capacitor filter



-  $V_C(0) = V_C(0^+) = 0V$ . Initially  $C$  acts as S.C and  $V_o = V_C = 0V$ .

- For the first half of  $V_i$ ,  $D \rightarrow FB \rightarrow ON$ .

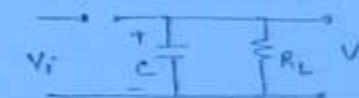
$$\begin{aligned} \tau &= (R_f \parallel R_L) \cdot C \\ &= R_f \cdot C \quad (\because R_f \ll R_L) \end{aligned}$$

-  $\tau$  should be such that  $\tau \ll T \Rightarrow$  rate of charging of  $C$  should be very high.

- At  $T = T/4$ ,  $V_i = V_m$  and  $V_C = V_o = V_m$ .

$$V_i = V_m \rightarrow V_C = V_o = V_m$$

- For  $T > T/4$ ,  $V_i \downarrow$  & when  $V_i < V_o$ ,  $\Rightarrow D \rightarrow RB \rightarrow OFF$ .



$\rightarrow$   $\tau \leq R_L C$  should be  $\gg T$ , so that rate of discharging  $\tau \leq R_L C$  of  $C$  is very slow.  $\Rightarrow V_C \downarrow$  exponentially.

$\rightarrow$   $V_i$  will  $\downarrow$  and then  $\uparrow$  and when  $V_i \geq V_C \Rightarrow D \rightarrow FB \rightarrow ON$  and it will again charge  $C$  with  $\tau = R_f C$  upto  $V_m$ . Thus, the cycle repeats.

$\rightarrow$  from plot  $\rightarrow$

$$T_D + T_C = T = \text{time period of signal}$$

$\rightarrow$  When we  $\uparrow R_L C$ , then  $T_D \uparrow$ ,  $T_C \downarrow$ , variation  $\downarrow$ ,  $T \downarrow$ .

for best filter,  $T_D \gg T_C$

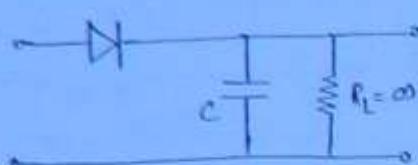
$$\text{or } T_D \approx T = V_f \quad \text{--- (1)}$$

Ideal condition:

$\rightarrow$  If  $R_L C = \infty$ ,  $V_o = V_m \rightarrow$  pure dc

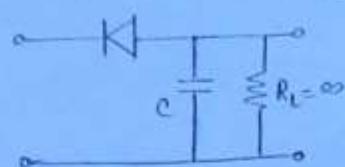
$\Rightarrow \alpha = 0$ ,  $F = 1$ ;  $C = 1 \dots$

## Peak Detector :-



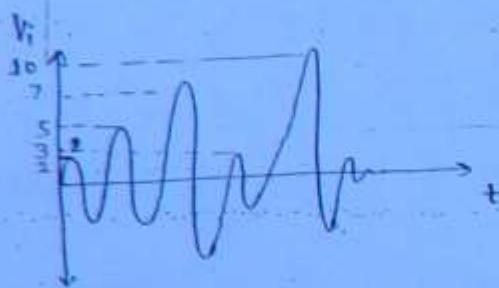
+ve peak detector

(92)



-ve peak detector

Ex:



$V_0$  = will charge upto

(i) 2V and hold  
Hence 8V and hold

(ii) 5V .. "

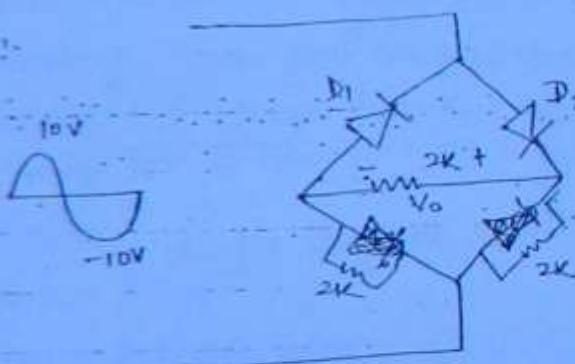
(iii) 7V .. "

(iv) 3V .. → do not change  $\rightarrow 7V$   
(diode will be RB).

(v) 10V → charge upto 10V.

Hence, the o/p will always hold the max. value of i/p.

Ques:



Assume ideal diodes

① - Draw the o/p waveform.

② - find o/p dc level

③ - find PIV.

$$\text{① o/p dc level} = \frac{2 \times V_m}{\pi} = \frac{10}{\pi}$$

$$\text{③ PIV} = \text{from fig ①} \\ PIV = V_1 - V_3 \\ = 10 - 5 = 5V$$

Sol<sup>n</sup> for +ve half-

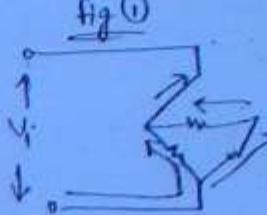
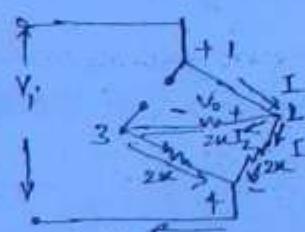
$$V_{24} = V_{234} = 10V$$

$$\therefore V_0 = \frac{10}{2} = 5V.$$

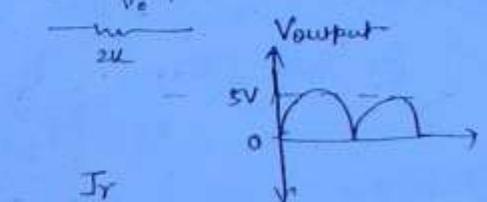
During -ve half-

$$\text{again } V_0 = \frac{10}{2} = 5V$$

in some direction.



$$I_f = \frac{V_0}{R}$$



$$I_f = \frac{V_0}{R}$$

\* The circuit of given que is comparable to HVR since the op for the same ip would have been same as  $\frac{10V_m}{\pi}$ .

(93)

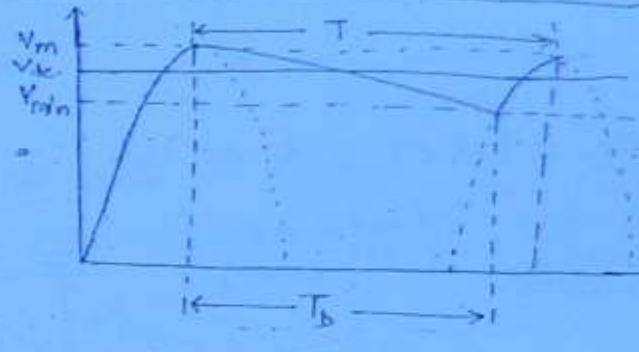
Approximate solution

-  $T_D \approx T = \frac{1}{f} \quad \text{--- (1)}$

-  $R_L C = \text{very high}$

- During  $T_D$ , C will discharge

$$V_o = V_c = V_m e^{-t/R_L C}$$



$$V_c \approx V_m \left[ 1 - \frac{t}{R_L C} \right] \quad \left\{ \because R_L C \text{ very high} \right\}$$

-  $V_{dc} = \frac{V_m + V_{min}}{2} \quad \text{--- (2)}$

-  $V_r = \text{peak to peak value of ripple voltage.}$

-  $\therefore V_{dc} = V_m - \frac{V_r}{2} \quad \text{--- (3)}$

-  $V_r = V_{min} - V_{max} = \text{change in } V_c \text{ during time } T_D.$

-  $V_f = \frac{\text{(discharge)}}{C}$

-  $I_m = \frac{V_m}{R_L}, \quad I_{min} = \frac{V_{min}}{R_L}$

$$\frac{I_{dc}}{T} + \frac{1}{T} \int_{V_c}^{V_c+I_{dc}/T} \frac{1}{R_L} dt$$

$\therefore I_{dc} = \frac{1}{2} \left( \frac{V_m + V_{min}}{R_L} \right) = \frac{V_{dc}}{R_L}$

$$\therefore \text{(discharge)} = \frac{V_{dc}}{R_L} \times T_D = I_{dc} \times T \quad \left\{ \because T = T_D \right\}$$

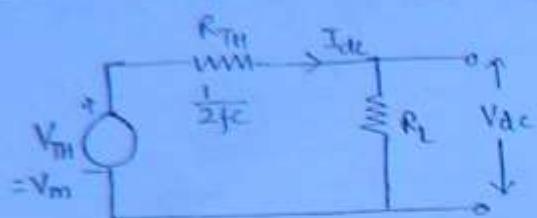
$$\therefore V_r = \frac{I_{dc} \cdot T}{C} \Rightarrow V_r = \frac{I_{dc}}{C \cdot f} \quad \text{or} \quad V_r = \frac{V_{dc}}{R_L \cdot C \cdot f}$$

from (3) -

$$\boxed{V_{dc} = V_m - \frac{I_{dc}}{2 \cdot f \cdot C}}$$

$$\text{or } \left\{ V_{dc} = \frac{I_{dc}}{R_L} \right\}$$

94

Thevenin's Equivalent :-

$$V_{dc} = V_m - I_{dc}(R_m)$$

Comparing with last eqn-

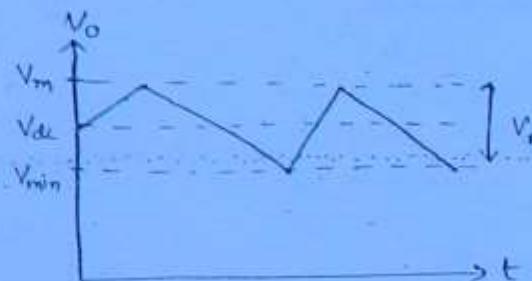
$$V_m = V_m ; R_m = \frac{1}{2fc}$$

\* When  $f \uparrow$  &  $C \uparrow$  or  $R_L \rightarrow \infty$  then  $V_{dc} \approx V_m$ . (Ideal case  $\rightarrow$  pure dc in op)

Ripple factor :-

$$\begin{aligned} V_{ac rms} &= \frac{V_p}{\sqrt{3}} = \frac{V_r}{2\sqrt{3}} \\ &= \frac{I_{dc}}{2\sqrt{3} f C} = \frac{V_{dc}}{2\sqrt{3} f C R_L} \end{aligned}$$

$$\left. \begin{aligned} \therefore V_r &= V_{p-p} \text{ and } \frac{V_{p-p}}{2} = V_p \end{aligned} \right\} \Rightarrow \tau = \frac{V_{ac rms}}{V_{dc}} \Rightarrow \boxed{\tau = \frac{1}{2\sqrt{3} f C R_L}}$$



## HWR with C.

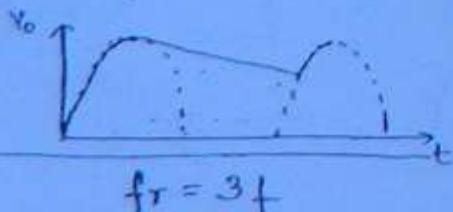
f

$$I_{dc}/fC$$

$$V_{dc} = V_m - \frac{I_{dc}}{2fc}$$

$$V_{TH} = V_m, R_m = \frac{1}{2fc}$$

$$\tau = \frac{1}{2\sqrt{3} f C R_L}$$



→ 3φ rectifier

## FWR with C (Bridge/center tapped)

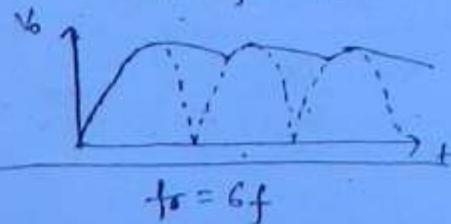
2f

$$I_{dc}/2fc$$

$$V_{dc} = V_m - \frac{I_{dc}}{4fc}$$

$$V_{TH} = V_m, R_m = \frac{1}{4fc}$$

$$\tau = \frac{1}{4\sqrt{3} f C R_L}$$



fr = 6f

\* Peak Inverse Voltage with C :-

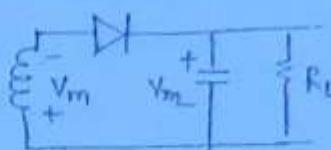
HWR

$C \rightarrow$  max charged

$$\Rightarrow V_C = V_m.$$

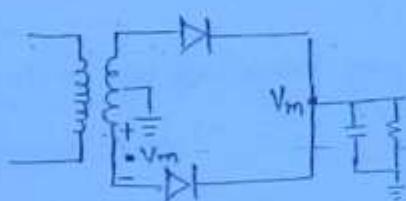
$$\therefore \text{PIV} = 2V_m$$

(95)



$$V_D = V_m - (-V_m) \\ = 2V_m.$$

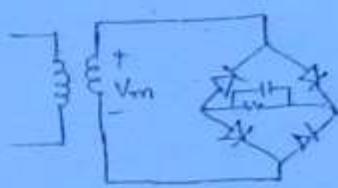
FWR



Center Tapped

$$\text{PIV} = 2V_m$$

{ same as before ?  
(w/o C) }



$$\text{PIV} = V_m$$

{ same as before, i.e., w/o C filter }

Bridge

Surge Current or Peak Diode Current :-

During  $T_D \rightarrow C$  discharge

$$Q(\text{discharge}) = I_{dc} \cdot T_D$$

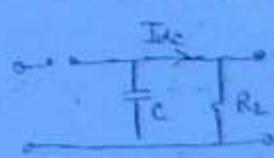
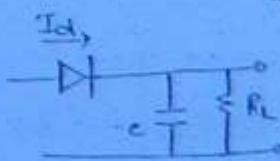
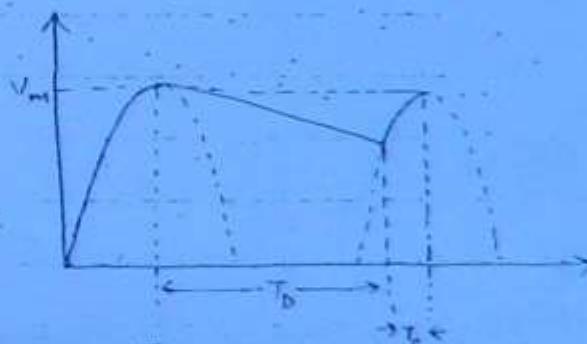
During  $T_C \rightarrow$  Diode  $\rightarrow$  ON  $\rightarrow$  C will charge

$$Q(\text{charge}) = I_d \cdot T_C$$

According to law of conservation of Q-

$$I_D \cdot T_C = I_{dc} \cdot T_D$$

$$\Rightarrow I_D = \frac{I_{dc} \cdot T_D}{T_C}$$



27/09/2012

(96)

\* If  $R_L \downarrow$ , then  $T_D \uparrow$ ,  $T_C \downarrow$ ,  $T \downarrow$  but  $I_D \uparrow$   
 ↓ (adv)                          ↓ (backward)

by for best filter,

$$T_D \gg T_C$$

$$\Rightarrow V_{dc} \approx V_m, I_{dc} \approx I_m = \frac{V_m}{R_L} \quad \text{For } V_m = 10 \text{ V, } R_L = 10 \text{ k}\Omega \rightarrow I_m = 1 \text{ mA}$$

$$\text{for } f=50 \text{ Hz, } T=1/f = 20 \text{ msec, } T_D = 19.98 \text{ mA, } T_C = 0.02 \text{ msec (say)}$$

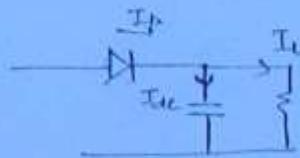
$$I_s = \text{surge current} = \frac{I_m \times 19.98}{0.02} \approx 1000 \text{ mA. (very large) } \rightarrow \text{High power diss.} \\ \rightarrow \text{diode damage.}$$

> Conduction Angle =

$$\phi = \omega T_C = \sqrt{\frac{2V_T}{V_m}}$$

$$, \omega = \frac{d\phi}{dt} \text{ rad/sec.}$$

$$\rightarrow V_T = \frac{V_{dc}}{fR_L C}$$



$$* I_{Dmax} = I_L \left[ 1 + 2\pi \sqrt{\frac{2V_m}{V_T}} \right]$$

$$\rightarrow V_{dc} = V_m - \frac{V_T}{2} ; \quad V_{dc} \approx V_m ; \quad I_L \approx I_{dc} \approx I_m .$$

$$\Rightarrow I_m = \frac{V_m}{R_L} ; \quad I_{dc} = \frac{V_{dc}}{R_L}$$

for FWR :

$$\phi = \omega T_C = \sqrt{\frac{2V_T}{V_m}}$$

$$\rightarrow V_T = \frac{I_{dc}}{2fC}$$

$$\rightarrow I_{Dmax} = I_L \left[ 1 + 2\pi \sqrt{\frac{V_m}{2V_T}} \right]$$

$$\rightarrow V_m \approx V_{dc} ; \quad I_L \approx I_m = I_{dc}$$



Q.3b (Workbook)

(1) (conventional) -

Given -  $V_{dc} = 30V$ ,  $\gamma \leq 0.61$ ,  $R_L = 500\Omega$ ,  $f = 50Hz$ .

$$I_{max} = ? , \epsilon = ?$$

$$\therefore \gamma = \frac{1}{2\pi f C R_L} \leq 0.61 \Rightarrow C \geq 11.54 \mu F$$

$$I_{max} = I_L \left[ 1 + 2\pi \sqrt{\frac{2V_m}{V_r}} \right]$$

$$V_r = \frac{V_{dc}}{f C R_L} = \frac{30}{50 \times 11.54 \times 10^{-6} \times 500} = 1.02V$$

$$I_L \approx I_{dc} \approx \frac{V_{dc}}{R_L}$$

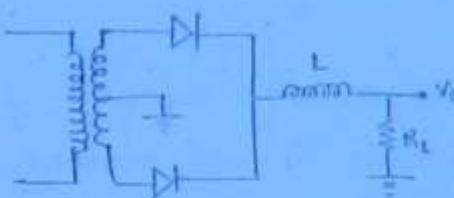
$$V_m = V_{dc} + \frac{V_r}{2} = 30.51V$$

Substitute,  $I_L$ ,  $V_r$  and  $V_m$  & calc.  $I_{max}$ .

Inductor Filter (or) Choke filter :-

$$\rightarrow \gamma = \frac{2}{3fL} \cdot \frac{1}{\sqrt{1 + (X_L/R_L)^2}}$$

$$\begin{aligned} X_L &= \omega L \text{ for IWR} \\ Y_L &= 2\omega L \text{ for FWR.} \end{aligned}$$

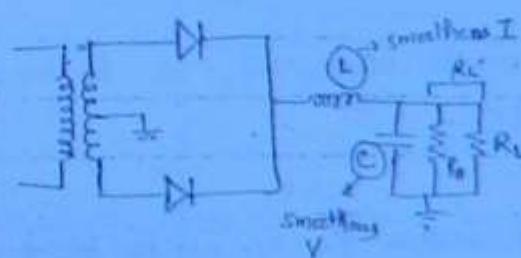


$$\rightarrow \text{If } \left( \frac{X_L}{R_L} \right)^2 \gg 1 \Rightarrow \gamma \propto \frac{R_L}{X_L} \Rightarrow \boxed{\gamma \propto \frac{R_L}{f \cdot \omega L}} = \boxed{\gamma \propto \frac{1}{f \cdot L}}$$

L-section or LC filter :-

$$\rightarrow \gamma = \frac{L}{3} \cdot \left( \frac{X_C}{X_L} \right) ; X_C = \frac{1}{\omega C} ; X_L = \omega L \text{ for IWR.}$$

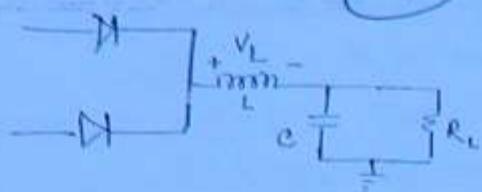
$$Y_C = \frac{1}{2\omega C} ; X_L = 2\omega L \text{ for FWR.}$$



$$\rightarrow \boxed{\gamma \propto \frac{1}{f^2}} \quad \because \gamma \text{ is very small.}$$

$$\rightarrow \boxed{\gamma \text{ is independent of } R_L}$$

$$\begin{aligned} R_B &= \text{Bleeder Resistance.} \\ R_L' &= R_B // R_L \geq R_L \left[ \because R_B \gg R_L \right] \end{aligned}$$



- \* When there is sudden change in current - then  $\frac{di}{dt}$  = large.

$$\therefore V_L = \frac{L \cdot di}{dt} = \text{very large.} \rightarrow \text{Back emf.}$$

This  $V_L$  will act as reverse bias for both diodes,

- ⇒ This sudden change occurs when circuit is ON w/o  $R_L$ .

$$\therefore \tau = \frac{L}{R_L} = 0 \quad (\because R_{L\text{eff}} = \infty)$$

$$\therefore \tau = 0, \text{ then } \frac{di}{dt} = \text{large} \Rightarrow V_L = \text{large.}$$

Hence,  $R_B$  is attached in the off. so that even if  $R_L = \infty$ . (i.e.) effective resistance  $R'_L = R_B || \infty = R_B$  and hence  $\tau = \frac{L}{R'_L}$  is never equal to 0.

Therefore, no sudden change of current.

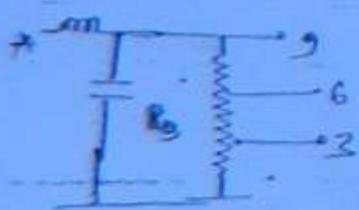
- ⇒ When  $R_B$  is attached across a capacitor, it helps C to discharge through it. when supply and  $R_L$  are removed.

By Sir:

- \* The basic req. of this filter is the current through choke must be continuous. An interrupted current through choke may develop large back emf which may be in excess of PIV rating of diode and/or max rating of capacitor.

- To eliminate back emf, a bleed resistance  $R_B$  is connected across off terminal.

- Another reason for  $R_B$  is to bleed off voltage stored in filter capacitor when supply is turned off.

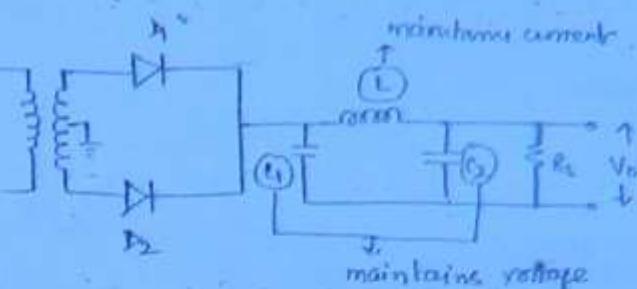


Potential Divider  $\rightarrow$  Different off's from diff. points from  $R_B$ .

(99)

II or CRC filter :-

$$\rightarrow \tau = \frac{\sqrt{2} \cdot X_C \cdot X_L}{R_L \cdot X_L} = \frac{\sqrt{2} \cdot (X_C)^2}{R_L \cdot X_L} \quad \left\{ C_1 = C_2 \right\}$$



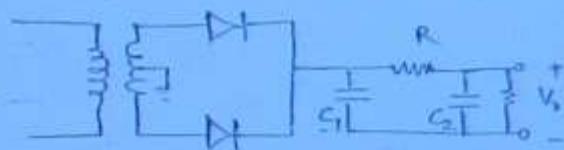
$$\rightarrow X_C = \frac{1}{\omega C} ; X_L = \omega L \quad \text{for HWR}$$

$$\rightarrow X_C = \frac{1}{2\omega C} ; X_L = 2\omega L \quad \text{for FWR}$$

$$\rightarrow \tau \propto \frac{1}{\omega^3 \cdot C_1 C_2 L R_L} \Rightarrow \boxed{\tau \propto \frac{1}{f^3}} \quad \therefore \tau = \text{very small.}$$

II or CRC filter :-

$$\tau = \frac{\sqrt{2} \cdot X_C \cdot X_C}{R_L \cdot R} = \frac{\sqrt{2} (X_C)^2}{R_L \cdot R} \quad (\text{for } C_1 = C_2).$$



$$\rightarrow X_C = \frac{1}{\omega C} \quad \text{for HWR}$$

$$\rightarrow X_C = \frac{1}{2\omega C} \quad \text{for FWR}$$

$$\rightarrow \tau \propto \frac{1}{\omega^2 C_1 C_2 R_L R} \Rightarrow \boxed{\tau \propto \frac{1}{f^2}} \quad \tau = \text{small} \quad (\text{relatively more than CLC filter}).$$

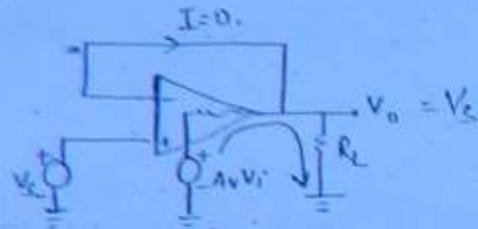
<u>*</u>	C	L	LC	CLC
$\rightarrow \tau \propto \frac{1}{f}$		$\tau \propto \frac{1}{f}$	$\tau \propto \frac{1}{f^2}$	$\tau \propto \frac{1}{f^3}$
$\rightarrow \tau \propto \frac{1}{f^2}$		$\tau \propto \frac{1}{f^2}$	$\tau \propto \frac{1}{f_1 f_2}$	$\tau \propto \frac{1}{f_1 f_2 f_3}$
$\rightarrow \tau = R_L C$		$\tau = \frac{1}{R_L}$	$\tau = \frac{L}{R_L} \cdot R_L C$ $= LC$	$\tau = \frac{1}{R_L C_1 C_2 L R_L}$ $= C_1 C_2 L R_L$
$\rightarrow \tau \propto \frac{1}{R_L C}$		$\tau \propto \frac{1}{R_L}$	$\therefore \tau \propto \frac{1}{f_C}$	$\tau \propto \frac{1}{C_1 C_2 L R_L}$

## Precision Rectifiers :-

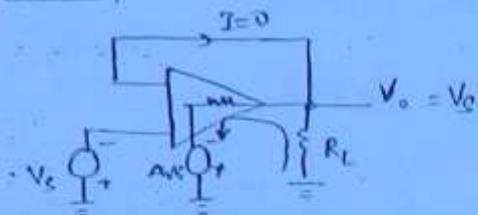
(10)

### - Voltage follower -

$V_s > 0$



$V_s < 0$



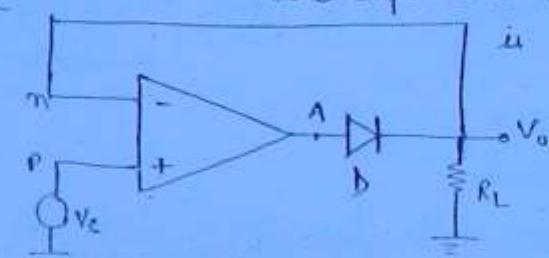
Hence, to maintain  $V_o$  at a level of input  $V_s$ , op-amp should provide a current as indicated above. If this current = 0, then  $V_o = 0$ .

### - Precision HWR :-

(This circuit is also called superdiode since cut-in voltage is very small,  $\approx \mu V$ )

→ Assuming Ideal op-amp & practical diode.

$V_s$	$V_A$	D	$V_o$
$V_s > 0$ (very small) $\approx \mu V$	$\uparrow$ towards $+V_{sat}$ Reaches till $V_A \approx 0.7V$	ON	$V_s$
$V_s < 0$	$V_A < (-V_{sat})$ Reaches to final value $= -V_{sat}$	off	0 (since there is no current due to D in $R_B$ )



→ Till the time D is about to 'ON', the op-amp will act as Open loop. When D is on, the due to -ve hence, gain is very high. applying virtual ground, (see next page  
 $V_o$  vs  $V_i$  plot)  
expn

$$V_p = V_n = V_s = V_o$$

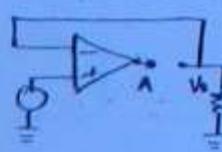
$$\therefore V_A = 0.7V + V_o$$

$$\Rightarrow V_A = 0.7V + V_s$$

$$\Rightarrow V_A \approx 0.7V$$

Hence, the diode D will avoid op-amp to go into positive saturation.

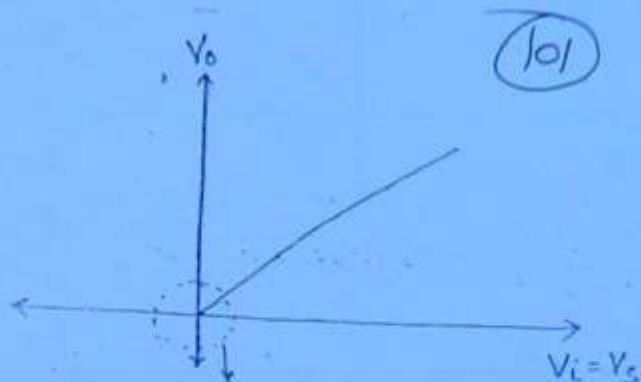
When  $V_s < 0$  —



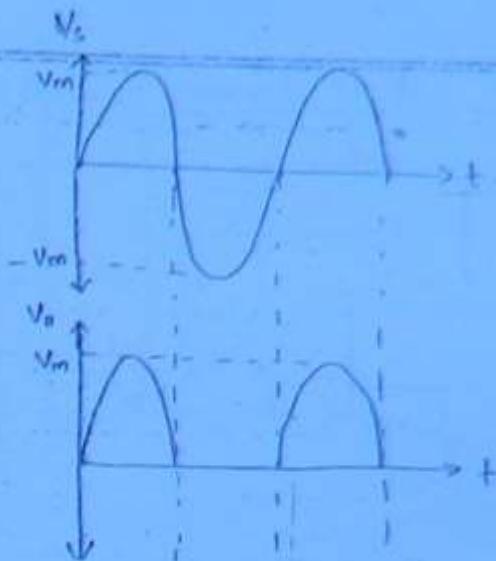
Op-amp will behave as open loop and  $A_v = 10^6$

∴ for small  $V_s$ ,  
 $V_A = -V_{sat}$ .

→ PIV for the diode D is  $-V_{sat}$ .

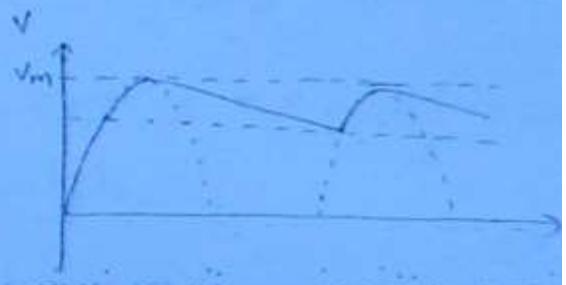
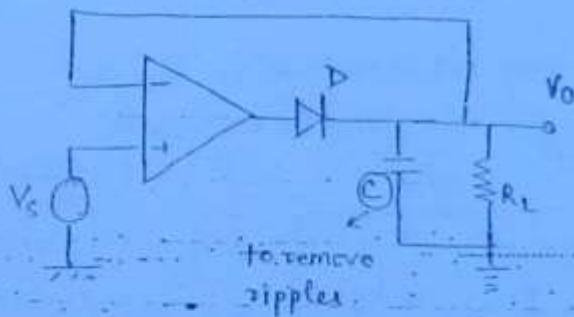


Cutoff voltage is very small,  $\approx 0.7 \mu V$

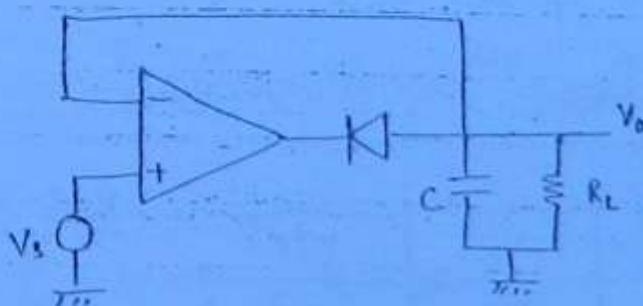


$$\left. \begin{aligned} \text{When } V_i = 0.7 \mu V, \quad V_A = A_v \cdot V_i = 10^6 \times 0.7 \mu V = 0.7 V \\ = V_Y \end{aligned} \right\}$$

Drawback: PIV is very high.



\* If  $R_L C = \infty$ , then above circuit will act as +ve peak detector

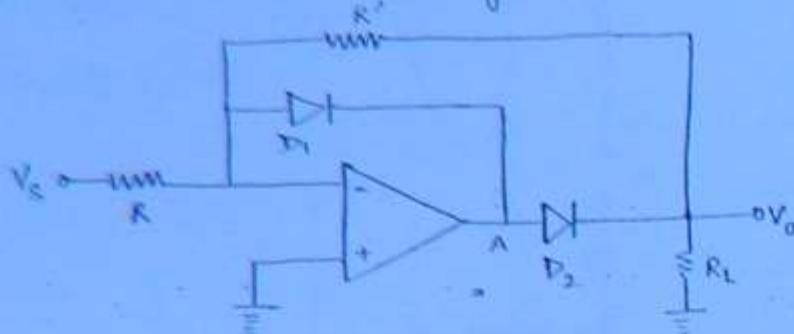


→ This circuit will avoid -ve saturation for  $V_s < 0$ .

→ If  $R_L C = \infty$ , then it will act as -ve peak detector.

Precision HWR (i/p at Inverting Terminal).

(102)



When  $V_c > 0$  -

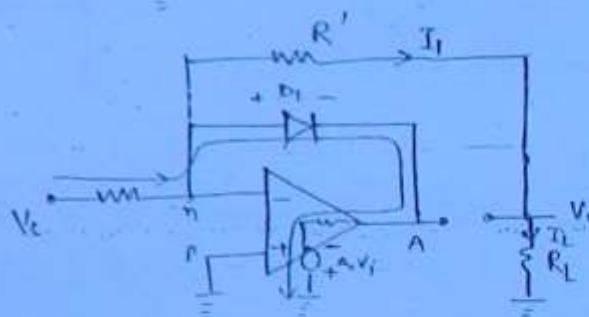
$$D_1 = FB, D_2 = RB$$

$$V_m = V_p = 0$$

$$V_m = 0.7V \therefore V_A = -0.7V$$

$\therefore D_1$  will avoid negative saturation when i/p is +ve.

$$I_1 = I_2 = 0 \Rightarrow V_o = 0$$

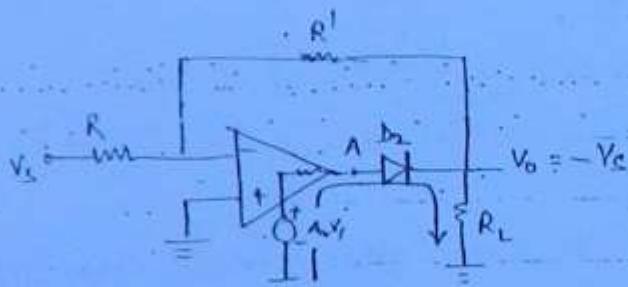


$\rightarrow$   $\rightarrow PIV = 0.7V$ . { very less as compared to  $V_{sat}$  as we were getting in last case }

When  $V_c < 0$  -

$$D_1 = RB, D_2 = FB$$

$$\rightarrow V_o = -\frac{R'}{R} V_s = -V_s \quad \{ \text{if } R = R' \}$$



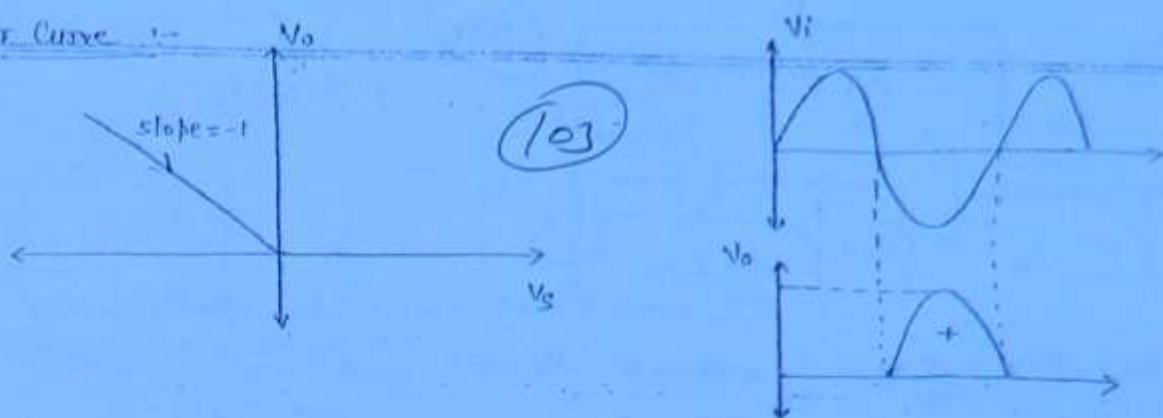
$$\rightarrow V_A = 0.7 - V_s \approx 0.7V$$

$\therefore D_2$  will avoid positive saturation when i/p is -ve

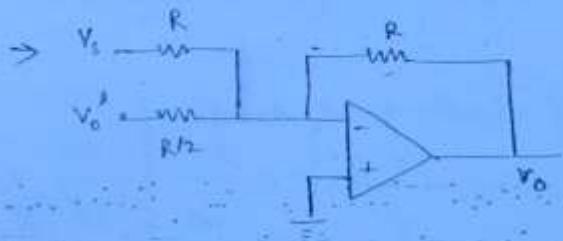
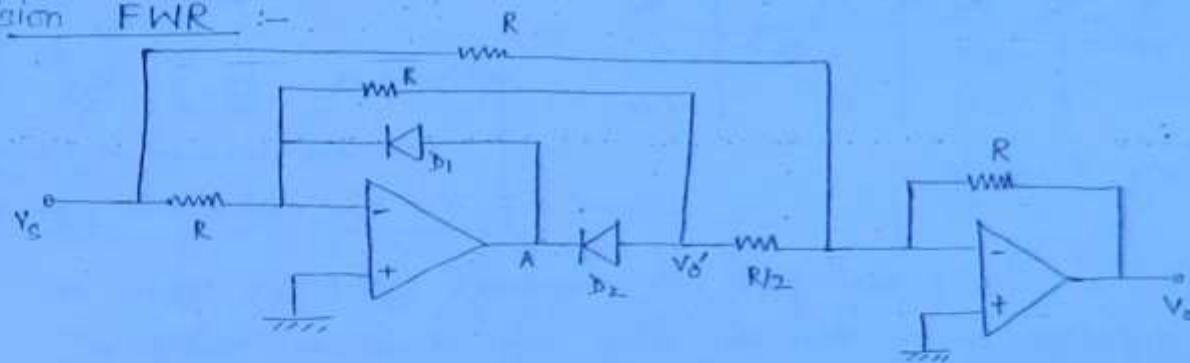
$\rightarrow$   $\rightarrow PIV = 0.7V$ .

$V_s$	$V_A$	$D_1$	$D_2$	$V_c$	$PIV$
$\rightarrow V_s > 0$	$\downarrow \text{towards } -V_{sat} (-0.7V)$	ON	OFF	0	0.7V for $D_2$
$\rightarrow V_s < 0$	$\uparrow \text{towards } +V_{sat} (0.7V)$	OFF	ON	$-V_s$	0.7V for $D_1$

Transfer Curve :-



Precision FWR :-



$$V_o = \frac{-R}{R/2} V_o' = \frac{R}{R/2} V_s$$

$$\Rightarrow V_o = -2V_o' - V_s \quad (1)$$

$V_s$	$V_A$	$D_1$	$D_2$	$V_o'$
$V_s > 0$	$\downarrow -V_{sat}$ $(-0.7V)$	OFF	ON	$-V_s$
$V_s < 0$	$\uparrow +V_{sat}$ $(0.7V)$	ON	OFF	0

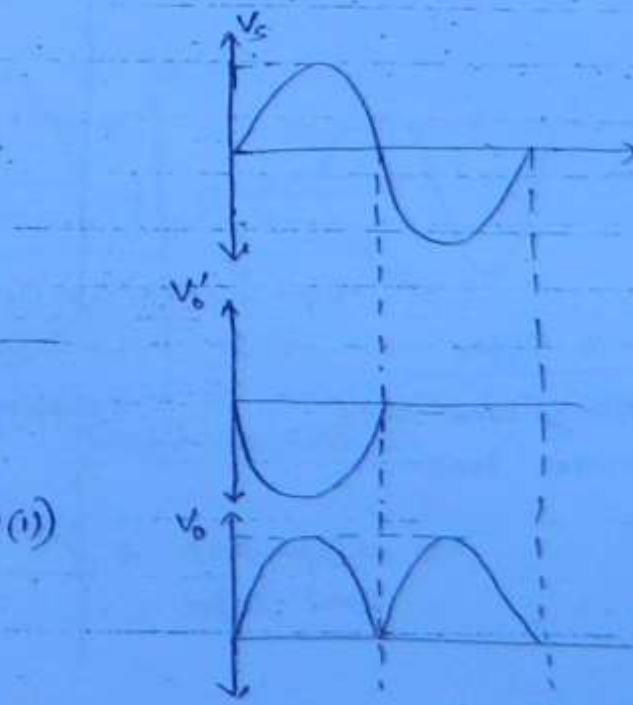
for +ve half -

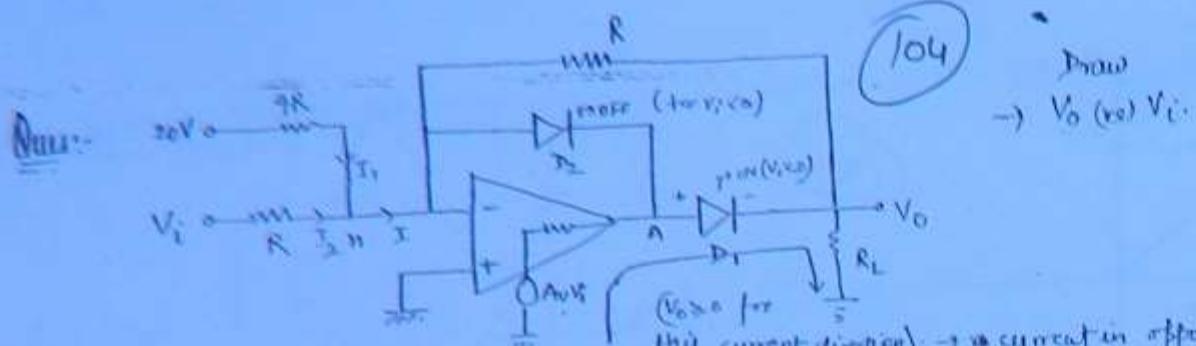
$$V_s > 0 = 5 \text{ (sat)} \therefore V_o' = -5V$$

$$\therefore V_o = -2(-5) - 5 = 5V \quad (\text{from eqn (1)})$$

for -ve half -

$$V_s < 0, \quad V_o' = 0 \Rightarrow V_o = 5V$$





Soln :-

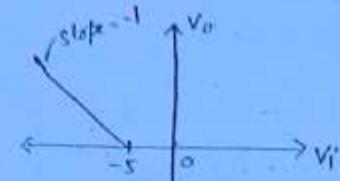
for  $V_i < 0$ ,  
( $D_1 = \text{ON}, T_1 = \text{OFF}$ )

$$V_o = -\frac{R}{R} V_i - \frac{R}{4R} \times 20 = -V_i - 5$$

$V_o > 0$  (i.e. this current direction)  $\rightarrow$  a current in opposite direction  
not possible as  $T_1$  will be RB.

$$\text{but, } V_o > 0 \Rightarrow -V_i - 5 > 0 \Rightarrow V_i \leq -5$$

$V_i$	$b_1$	$b_2$	$V_o$	$V_m$
$V_i \leq -5$	OFF	ON	$-V_i - 5$	$\geq 0$
$V_i > -5$	ON	OFF	0	$< 0$



$$I = I_1 + I_2 = \frac{20}{4R} + \frac{V_i}{R} = \frac{V_i + 5}{R}$$

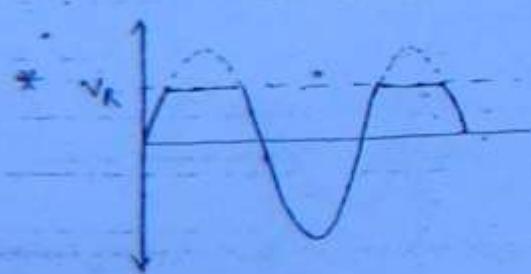
$I > 0 \Rightarrow V_o = \text{effectively } +ve \Rightarrow V_o = +ve$   
 $I < 0 \Rightarrow V_i = \text{effectively } -ve \Rightarrow V_o = -ve$

### Clippers / Limiting Circuits :-

- These are used to select that part of waveform which lies above or below some reference level. These are also referred to as voltage or current limiters, amplitude selectors or slicers.

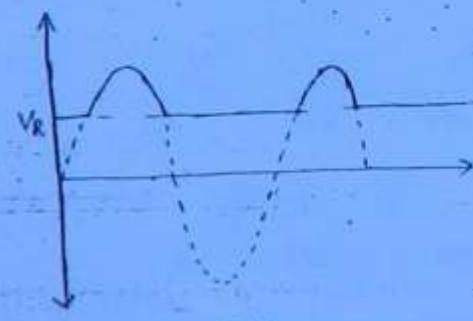
- There are of two types (according to the position of diode w.r.t. load) -

- (a) Series clipper
- (b) Shunt clipper



+ve clipper

$\rightarrow$  clipping above some reference level.



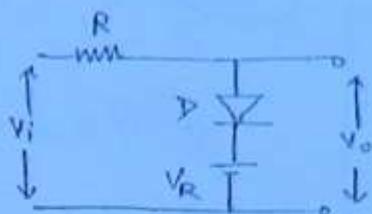
-ve clipper

$\rightarrow$  clipping below some reference level.

Two independent level clipper

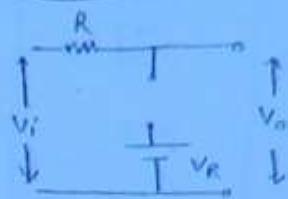


28<sup>th</sup> August, 2012



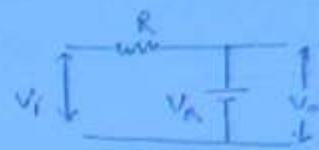
Shunt Clipper (Positive)

105



D  $\rightarrow$  RB

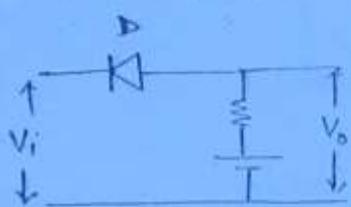
$(V_R > V_i)$



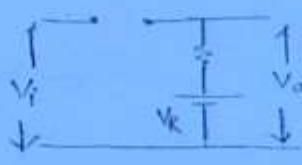
D  $\rightarrow$  FB

$(V_R < V_i)$

$V_i$	D	$V_o$
$V_i \leq V_R$	OFF	$V_i$
$V_i \geq V_R$	ON	$V_R$

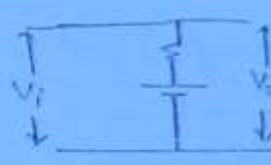


Series Clipper (positive)



D  $\rightarrow$  RB

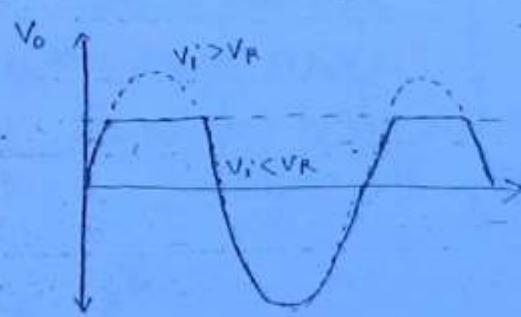
$(V_i > V_R)$



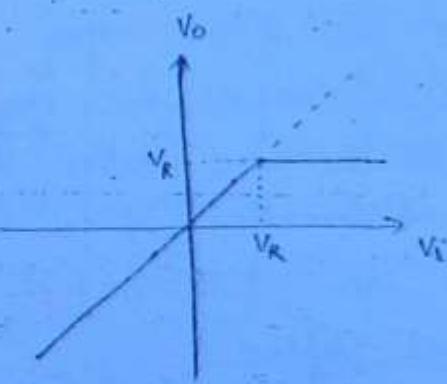
D  $\rightarrow$  FB

$(V_i < V_R)$

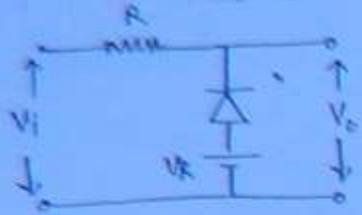
$V_i$	D	$V_o$
$V_i \leq V_R$	ON	$V_i$
$V_i \geq V_R$	OFF	$V_R$



O/P



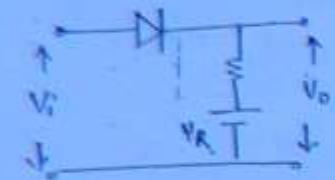
Transfer Characteristic



$V_i$	D	$V_o$
$V_i \geq V_R$	OFF	$V_i$
$V_i \leq V_R$	ON	$V_R$

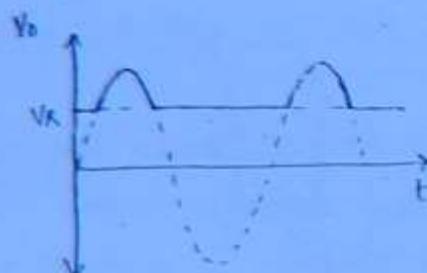
(106)

Shunt clipper (negative)

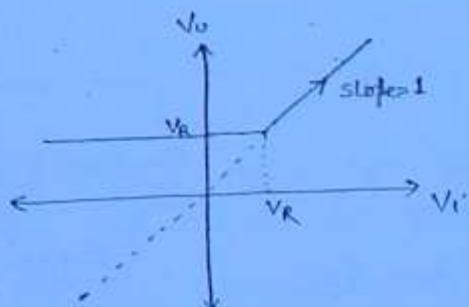


$V_i$	D	$V_o$
$V_i > V_R$	ON	$V_i$
$V_i \leq V_R$	OFF	$V_R$

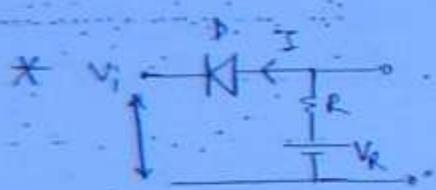
Series Negative Clipper



O/P curve



Transfer Characteristic



$$I = \frac{V_i - V_R}{R}$$

$I \geq 0$  then  $D \rightarrow \text{ON}$

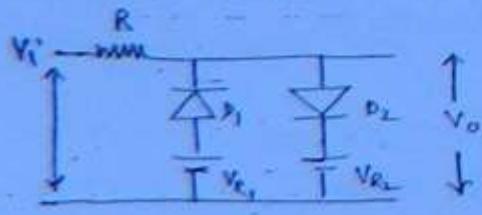
$\Rightarrow V_i \geq V_R$  then  $D \rightarrow \text{ON}$

$I \leq 0$  then  $D \rightarrow \text{OFF}$

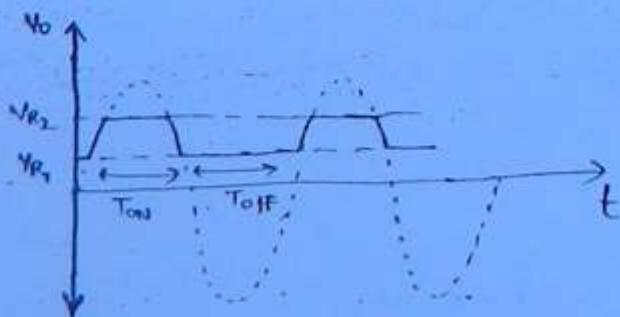
$\Rightarrow V_i \leq V_R$  then  $D \rightarrow \text{OFF}$

It is easiest way to determine whether diode is on or off. Calculate the current in forward direction of diode and apply the condition.

Two Independent level clipper :-



$$(V_{R2} > V_{R1})$$



$\rightarrow$  Range of  $V_i$

$$V_i \leq V_{R_1}$$

$$V_{R_1} \leq V_i \leq V_{R_2}$$

$$V_i \geq V_{R_2}$$

$D_1$

ON

OFF

OFF

$D_2$

OFF

OFF

ON

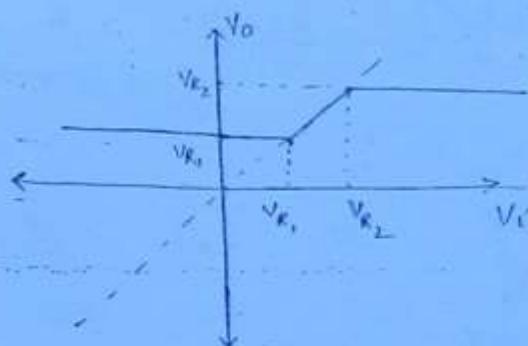
$V_o$

$$V_{R_1}$$

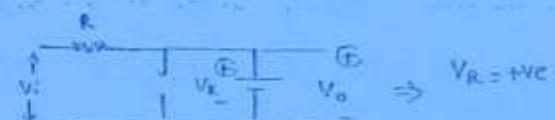
$$V_i$$

$$V_{R_2}$$

(107)



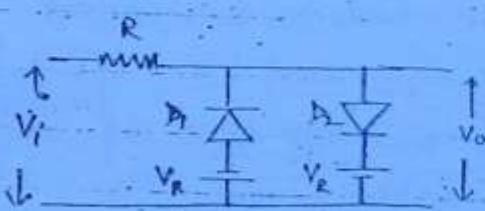
$\rightarrow$  To conclude that  $V_{R_1}$  &  $V_{R_2}$  are true, check polarity at  $V_o$  whenever o/p is  $V_R$ , if same polarity then  $V_R = +ve$ , else  $-ve$ .



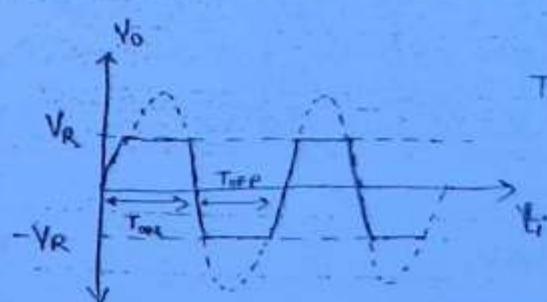
$\rightarrow$  from o/p curve,  $T_{off} > T_{on} \Rightarrow D < 50\%$ . Output is an asymmetrical square wave.

$\rightarrow$  This circuit is used as a means of converting a sinusoidal waveform into a square wave.

$\rightarrow$  To generate a symmetrical square wave,  $V_{R_1}$  and  $V_{R_2}$  are adjusted to be numerically equal but are of opposite sign.

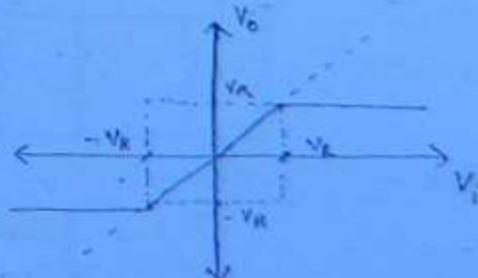


$V_i$	$D_1$	$D_2$	$V_o$
$V_i \leq V_R$	ON	OFF	$-V_R$
$-V_R \leq V_i \leq V_R$	OFF	OFF	$V_i$
$V_i \geq V_R$	OFF	ON	$V_R$



$$T_{on} = T_{off} \Rightarrow D = 50\%$$

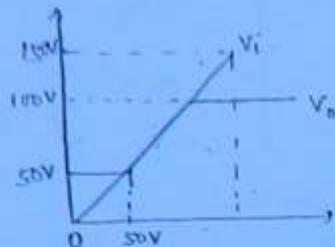
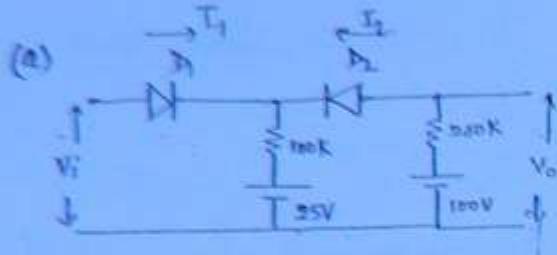
$\rightarrow$  Symmetrical Sq. wave / Symmetrical Clipping



Transfer characteristic

Ques: (i) The i/p voltage  $V_i$  to the two level clapper shown in fig varies linearly from 0 to 150V. Sketch the o/p voltage  $V_o$ , to the same time scale as the i/p voltage. Assume ideal diodes.

(108)



Ans

Range of $V_i$	$D_1$	$D_2$	$V_o$
$0 \leq V_i \leq 50$	OFF	ON	50V
$50 \leq V_i \leq 100$	ON	ON	$V_i$
$V_i \geq 100$	ON	OFF	100V

$$\rightarrow I_1 = \frac{V_i - 25}{100k} + \frac{V_i - 100}{200k}$$

$$\text{For } D_1 = \text{ON}, \quad I_1 \geq 0 \Rightarrow \frac{V_i - 25}{100k} + \frac{V_i - 100}{200k} \geq 0$$

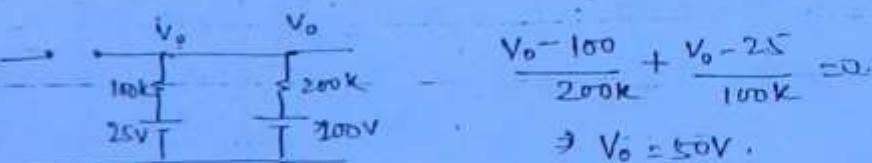
$$\Rightarrow V_i \geq 50$$

$$\rightarrow I_2 = -\frac{V_i + 100}{200k}, \text{ for } D_2 = \text{ON} -$$

$$I_2 \geq 0$$

$$\Rightarrow V_i \leq 100$$

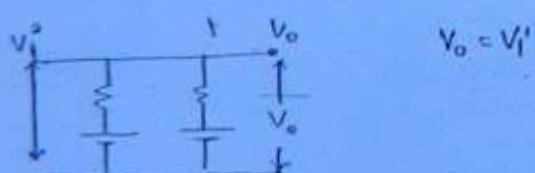
$\rightarrow$  When  $0 \leq V_i \leq 50$ ,



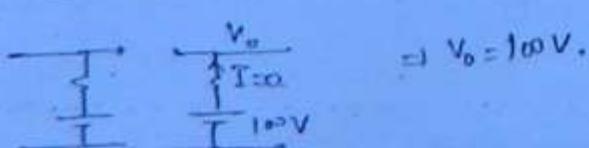
$$\frac{V_o - 100}{200k} + \frac{V_o - 25}{100k} = 0$$

$$\Rightarrow V_o = 50V$$

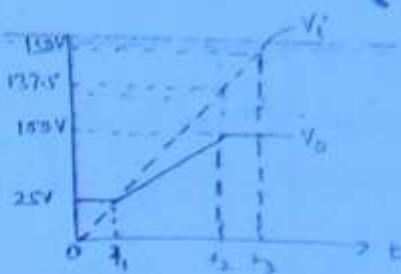
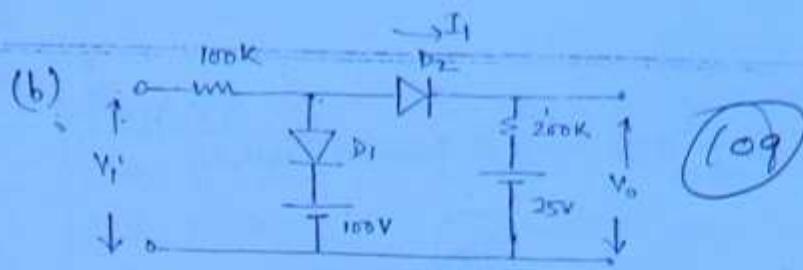
$\rightarrow$  When  $50 \leq V_i \leq 100$



$\rightarrow$  When  $V_i \geq 100$  -



$$\Rightarrow V_o = 100V$$



Soln: Since voltage across  $D_1$  is very high (100V), then  $D_2$  will ON before  $D_3$ .

$$\therefore I_1 = \frac{V_i - 25}{300k} \quad , \text{ for } D_2 = \text{ON}$$

(Assuming  $D_2$  OFF)

$$I_1 > 0$$

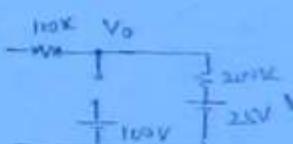
$$\Rightarrow V_i > 25 \rightarrow (\text{Breakpoint.})$$

$V_i$	$D_1$	$D_2$	$V_o$
$0 < V_i \leq 25$	OFF	OFF	25V
$25 \leq V_i \leq 137.5$	OFF	ON	$\frac{2V_i + 25}{3}$
$V_i \geq 137.5$	ON	ON	100V

for  $V_i > 25$ ,  $V_o$  will be -

$$\frac{V_o - 25}{200} + \frac{V_o - V_i}{100} = 0$$

$$\Rightarrow V_o = \frac{2V_i + 25}{3}$$



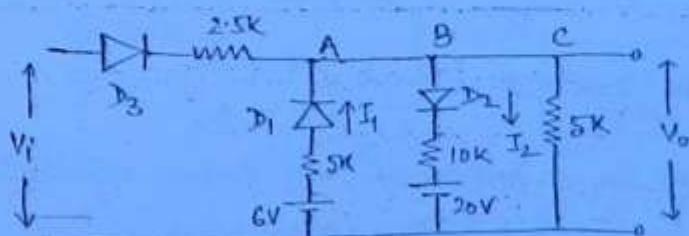
Now for  $D_2 = \text{ON}$  -

$$V_o \geq 100V$$

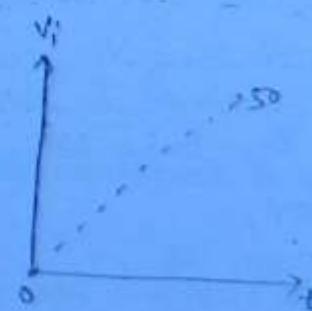
$$\Rightarrow \frac{2V_i + 25}{3} \geq 100 \Rightarrow V_i \geq 137.5V$$

L (Breakpoint)

Ques:- Assume that the diodes are ideal, make a plot of  $V_o$  vs  $V_i$  for the range of  $V_i$  from 0 to 50V. Indicate all slopes and voltage levels. Indicate for each region, which diodes are conducting.



Soln



Ques: When  $V_i = 0$ ,

voltage across  $D_2 = \infty$  is large ( $\approx 20V$ ) and  $A$  is forward biased.  
Due to  $D_1$ , current voltage at  $A$ ,

(110)

$$V_A = 3V.$$

Now, this  $V_A$  is making diode  $D_3$  RB.



Now, when  $V_i > 3V$  then  $D_1 = ON$ .  
↳ Break point.

for  $0 \leq V_i \leq 3$ ,  $V_o = V_A = 3V$

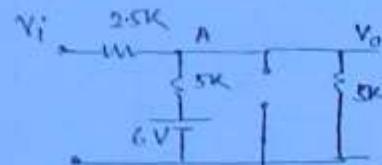
for  $V_i > 3$ ,

$D_1 = ON$ ,  $D_2 = OFF$ ,  $D_3 = ON$ .

$$\frac{V_A - V_i}{2.5K} + \frac{V_A - 6}{5} + \frac{V_A}{5} = 0$$

$$\Rightarrow \frac{4V_A}{5} = \frac{2V_i}{5} + \frac{6}{5}$$

$$\Rightarrow V_A = \frac{V_i + 3}{2}$$



Now diode  $D_1$  will remain in FB till

$$V_A < 6 \quad \left\{ \begin{array}{l} I_1 = \frac{V_A - 6}{5} > 0 \text{ for } D_1 = ON \\ D_1 \Rightarrow V_A \geq 6 \end{array} \right.$$

$$\Rightarrow \frac{V_i + 3}{2} \leq 6$$

$$\Rightarrow V_i \leq 9V \rightarrow \text{Break point.}$$

for  $3 \leq V_i \leq 9$ ;  $V_o = V_A = \frac{V_i + 3}{2}$

for  $V_i > 9V$ ,

$D_1 = OFF$ ,  $D_2 = OFF$ ,  $D_3 = ON$ .

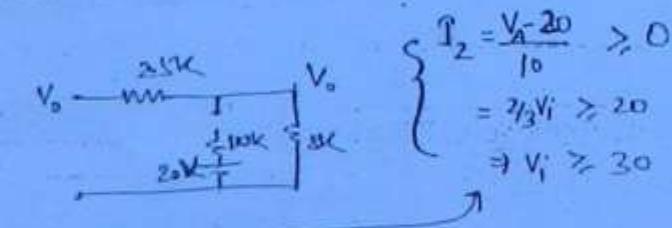
$$V_o = \frac{5}{7.5} V_i = \frac{2}{3} V_i$$

Now for  $D_2$  to be ON,

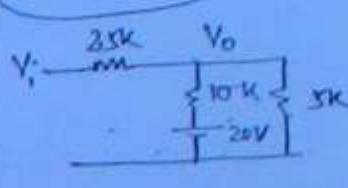
for  $V_i \geq 30V$ ,

on applying KCL,

$$V_o = \frac{4V_i + 20}{7}$$



$$\left( \frac{2}{3} V_i \geq 20V \right) \Rightarrow V_i \geq 30V \rightarrow \text{Break point.}$$



Range of  $V_i$

$0 \leq V_i \leq 3$

$3 \leq V_i \leq 9$

$9 \leq V_i \leq 30$

$30 \leq V_i \leq 50$

$D_1$

ON

OFF

OFF

ON

ON

OFF

OFF

ON

ON

OFF

ON

ON

ON

$D_2$

OFF

ON

ON

OFF

OFF

ON

ON

OFF

ON

ON

ON

$D_3$

OFF

ON

ON

OFF

OFF

ON

ON

OFF

ON

ON

ON

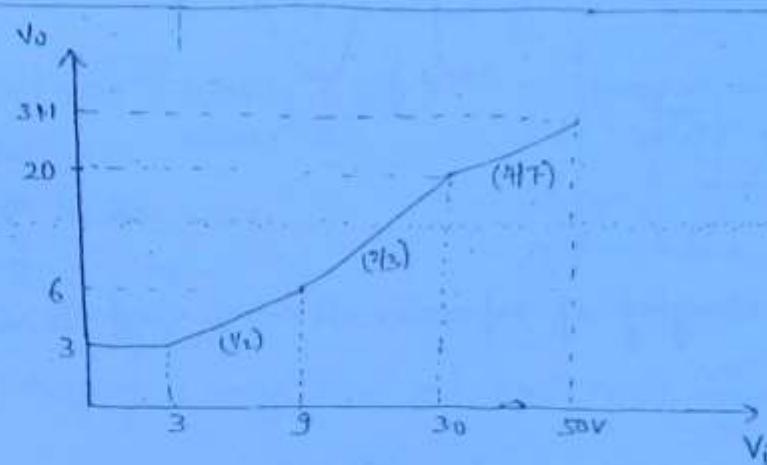
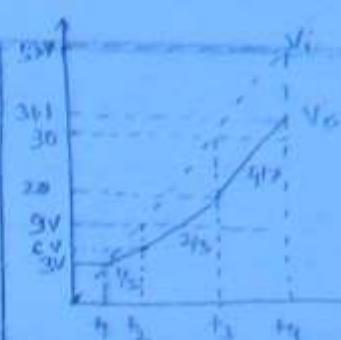
$V_o$

3V

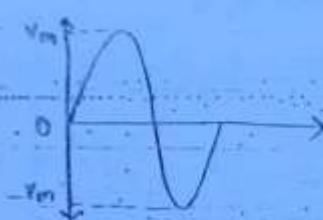
$\frac{V_i+3}{2}$

$\frac{2V_i}{3}$

$\frac{4V_i+20}{7}$



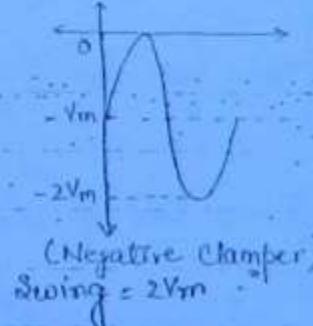
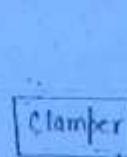
Clamper Circuit :-



Swing =  $2V_m$

f = 50 Hz

dc level = 0

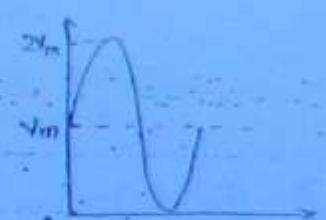


(Negative Clamper)

Swing =  $2V_m$

f = 50 Hz

dc level =  $-V_m$



(Positive Clamper)

Swing =  $2V_m$

freq = 50Hz

dc level =  $V_m$

→ Clamper circuits are also called as dc translator, dc restorer, dc inserter.

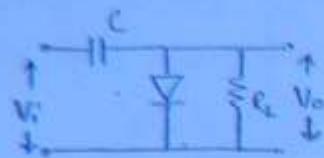
→ The circuit which are used to add a dc level as per the requirements to ac o/p signal are called clamper circuit.

→ These are of two types → Negative Clamper → adds  $-ve$  level to ac o/p signal  
Positive Clamper → adds  $+ve$  level.

25<sup>th</sup> August, 2012

### Negative Clamper

(1) 2



$$\text{Initially, } V_o(0^-) = V_o(0^+) = 0.$$

hence, C will act as S.C.

During 1<sup>st</sup> half-wave,

D  $\rightarrow$  FB.



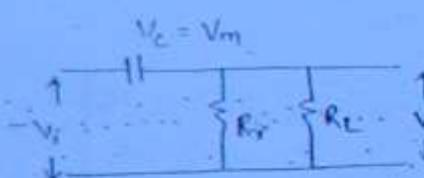
$$\tau = (R_F \parallel R_L) C \approx R_F C \quad (\because R_F \ll R_L)$$

$\because R_F C \ll T$ . Hence, rate of charging of capacitor is very high. It will charge till maximum value  $V_m$ .

$\rightarrow$  At  $t = T/4$ ,  $V_i = V_m$  &  $V_c = V_m$ .

$\therefore$  D  $\rightarrow$  ON,  $R_F \approx 0$  and hence,  $V_D \approx 0$ .  $\therefore V_D = V_D = 0$ .

$\rightarrow$  for  $t > T/4$ ,



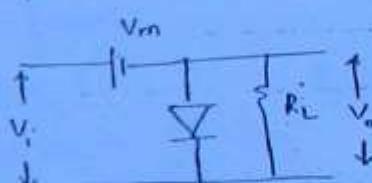
$$V_D = V_i - V_m \quad \text{and} \quad V_i \downarrow$$

and when  $V_i \leq V_m$

$\therefore D \rightarrow RB$ .

$\tau = (R_F \parallel R_L) C \approx R_L C \gg T \Rightarrow$  Rate of discharging is very small  $\times 0$ .

$V_i$	$V_o = V_i - V_m$
$V_m$	0
0	$-V_m$
$-V_m$	$-2V_m$



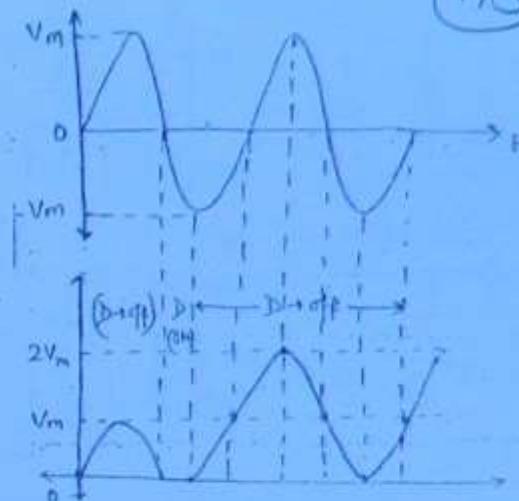
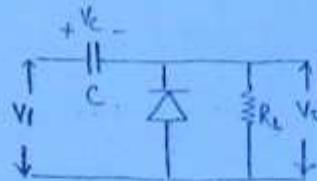
$\rightarrow$  Diode will remain off. after this instant.

\* We cannot use a dc battery instead of capacitor as the value of battery will vary w.r.t the peak value of signal.

→ Once the capacitor is charged till  $V_m$ , it will act as battery of value  $V_m$

Positive Clamper: It adds positive dc level to ac o/p.

(1/3)



→  $V_C(0^-) = V_C(0^+) = 0V \Rightarrow$  initially C will act as S.C.

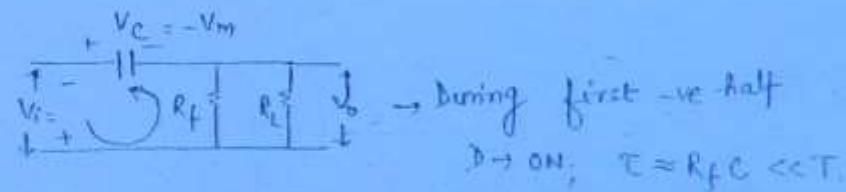
During 1<sup>st</sup> +ve half -

→ Diode D will be R.B.



∴ Rate of charging is very low and C will remain uncharged till  $t=T/2$ .

→ for  $t > T/2$  -



Now, the rate of charging is very high and will charge till  $t = 3T/4$ .

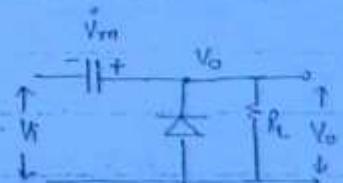
→ At  $t = 3T/4$ ,  $V_i = -V_m$ , and  $V_c = -V_m$ .

→ for  $t \geq 3T/4$  -

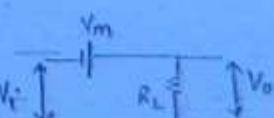
$V_i$	$V_o = V_i + V_m$
$-V_m$	$0V$
$0$	$V_m$
$V_m$	$2V_m$

$$V_o = V_i + V_m$$

∴ Voltage across D  
is always -ve, hence D → off.  
( $V_o = +ve$ )



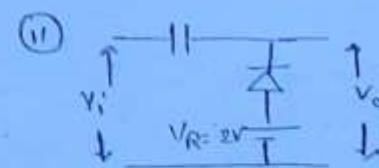
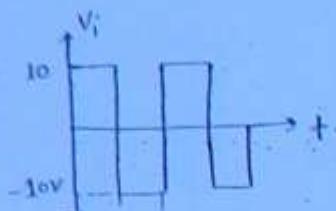
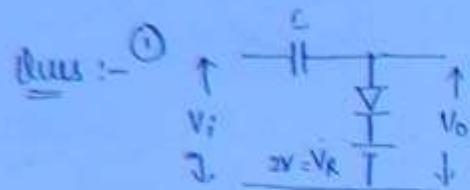
and ∵ D is off, C will never discharge.



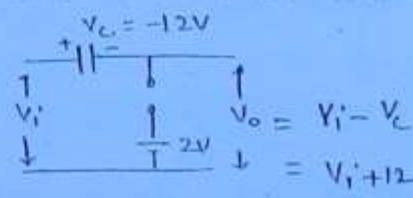
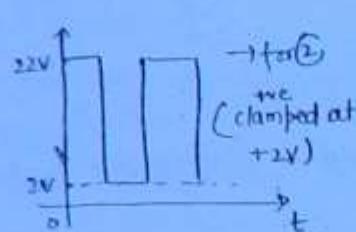
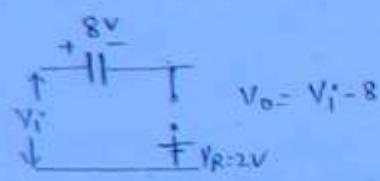
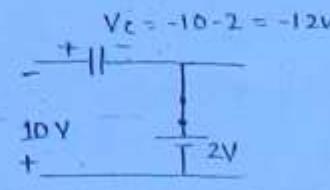
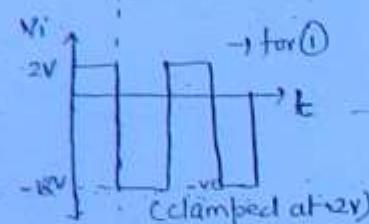
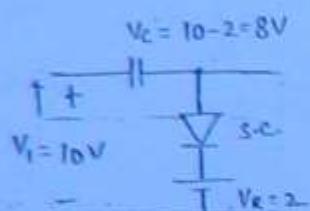
→  $R_{f\min} = \sqrt{R_f R_L}$  - for proper functioning of clamper

→ During first negative half, capacitor gets charged upto  $-V_m$  through FB diode D. The capacitor once charged to  $-V_m$ , will act as a battery of  $-V_m$  and therefore  $V_o = V_i + V_m$ .

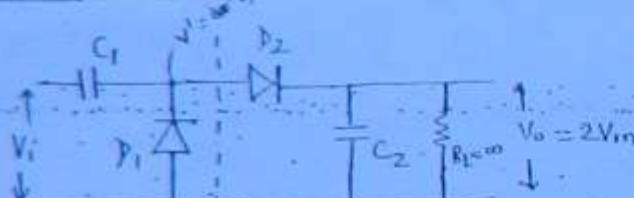
(14)



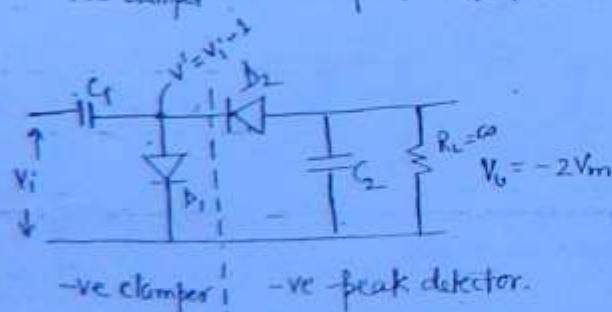
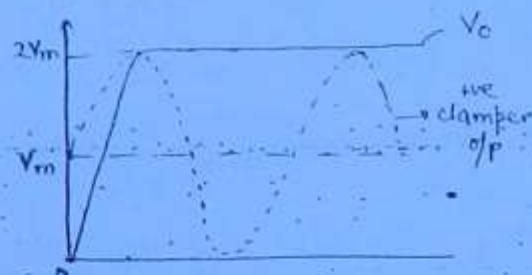
Soln



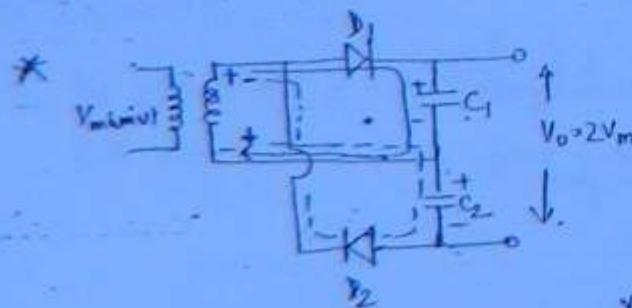
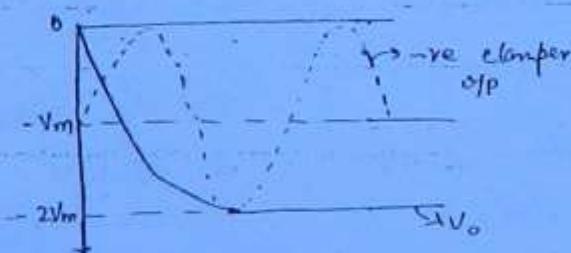
### Voltage Doubler :-



+ve clamer      +ve peak detector



-ve clamer      -ve peak detector



$C_1$  = will charge till  $+V_m$ . during +ve half

$C_2$  = will charge till  $+V_m$  during -ve half.

$$V_p \rightarrow V_n \quad V_p = V_p - V_n = V_p - V_m \geq 0 \text{ for FB}$$

Not possible  $\Rightarrow V_p > V_m$  for FB

$V_p = V_p - V_n$  hence  $D_1, D_2$  will remain off.

$= -V_m - V_n \geq 0 \Rightarrow V_n \leq -V_m$  for FB

Workbook

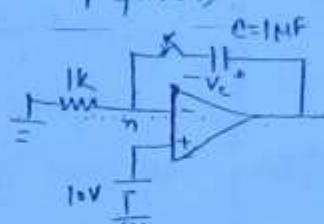
Pg. 27

①	$V_i$	$D_1$	$D_2$	$V_o$	② d
	$V_i < 0$	OFF	OFF	0	⑩ b
	$0 \leq V_i \leq 20$	ON	OFF	$V_i/2$	⑪ a
	$V_i > 20$	ON	ON	10V	⑫ d

11B

Chapter 10 Pg. 56

Q 7 c, a (c preferred)



$$V_n = 10V$$

$$V_o = 10 + V_c$$

: Ans 0V  $\rightarrow$  ⑨.

for  $V_c$  = values in option -

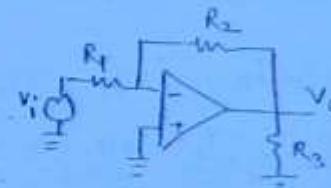
$$\therefore V_o = 10V + 0 = 10V$$

$$\left. \begin{array}{l} V_o = 10.3V \\ V_o = 19.5V \\ V_o = 20V \end{array} \right\} \times \text{cannot exceed } 15V$$

Q 18 C 22 C 23 L

Q 25 (a)

$$\frac{0 - V_i}{R_1} + \frac{0 - V_o}{R_2} = 0 \Rightarrow V_o = -\frac{R_2}{R_1} V_i$$



Conventional

② When switch is ON, gain = -1; S  $\rightarrow$  OFF, gain = -2.

③  $R_1 = 3k\Omega$ ,  $R_2 = 2k\Omega$

R = dc resistance seen by inverting terminal;  $R = R_1 || R_2 || 6k = 1k$

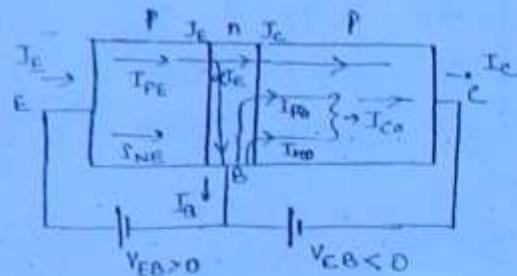
30<sup>th</sup> August, 2012

## Bipolar Junction Transistor :-

(116)

p-n-p transistor in active mode :-

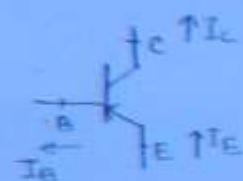
$$I_E = I_{PE} + I_{NE} \approx I_{PE} \quad \left\{ \begin{array}{l} \text{Minority carrier injection} \\ \text{(Diffusion)} \end{array} \right.$$



$$I_{CO} = I_{PCO} + I_{NCO} \quad \left\{ \begin{array}{l} \text{Majority carrier inj.} \\ \text{(Drift)} \end{array} \right.$$

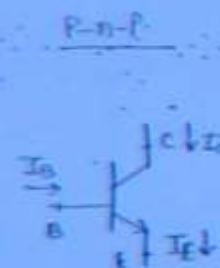
$I_C = \alpha I_E + I_{CO}$  ;  $\alpha$  = large signal current gain or  $\alpha_{dc}$ .  
(valid only for active current region)

$$\alpha \text{ or } \alpha_{dc} = \frac{I_C - I_{CO}}{I_E} \text{ or } \Rightarrow \alpha \approx \frac{I_C}{I_E} \quad \text{for CB configuration.}$$



$$I_E = I_C + I_B ; I_C = \alpha I_E .$$

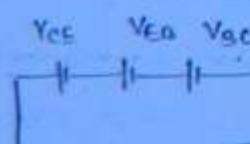
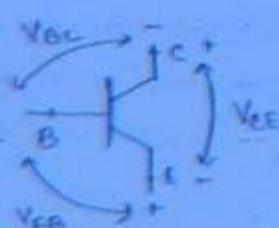
( $I_E, I_B, I_C$  = all +ve with this direction)



$$I_E = I_C + I_B ; I_C = \alpha I_E$$

( $I_E, I_B, I_C$  = all +ve with this direction).

n-p-n



$$V_{CE} + V_{EB} + V_{BC} = 0. \quad \left\{ \text{CEB} \right\}$$

( $V_{BE} = -V_{EB}$ ;  $V_{EC} = -V_{CE}$ ;  $V_{CB} = -V_{BC}$ ).

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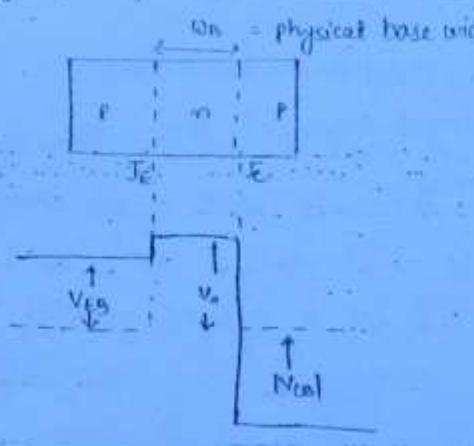
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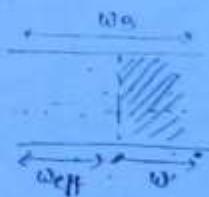
Note:- Guys Be Cool Dude I am here for help You ☺

	Common Emitter	(Emitter follower) Common Collector	Common Base
1) Input Terminal	B	B	E
2) Output Terminal	C	E	C
3) Common Terminal	E	C	B
4) Current Gain, $A_I$	high (moderate)	very high.	very low ( $<1$ )
5) Voltage Gain, $A_V$	high (moderate)	very low ( $<1$ )	very high
6) Input Resistance, $R_I$	high (moderate)	very high	very low
7) Output Resistance, $R_O$	high (moderate)	very low	very high
8) Power Gain $(A_P = A_V \cdot A_I)$	Highest	Moderate	Moderate
9) Phase Shift	$180^\circ$	$0^\circ$	$0^\circ$
10) Normally used as	Amplifier in multistage	Buffer (voltage).	High freq. application Buffer (current)

### Early effect / Base width Modulation :-



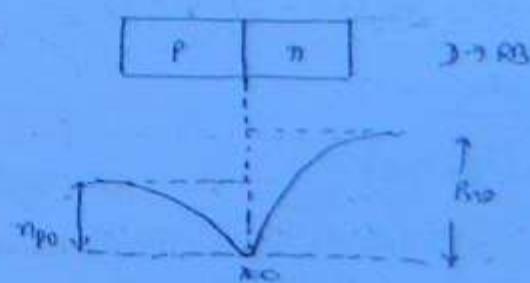
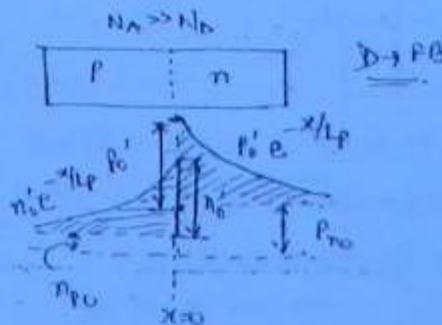
When collector junction is RB -



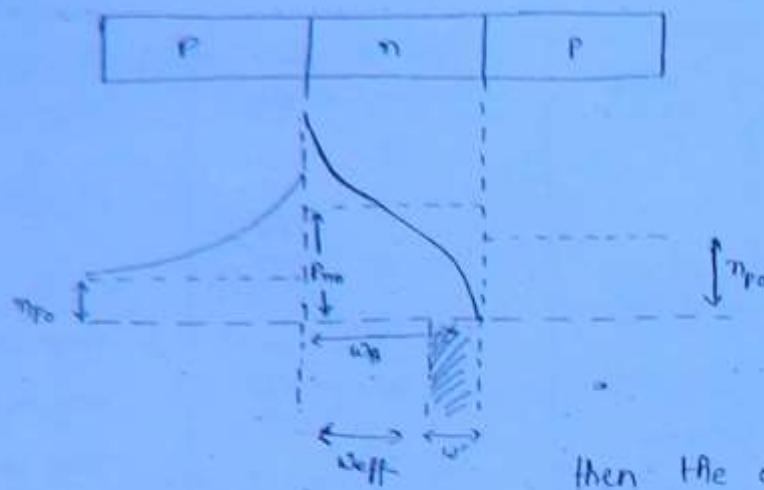
$$w_{eff} = \text{effective base width} = w_B - w'$$

$w'$  = depletion width.

As  $|V_{CBL}| \uparrow$ ;  $I_e$  becomes more RB and  $w_{eff} \downarrow$ , therefore recombination and hence  $I_D \downarrow$  and  $\propto \uparrow$ .



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 $I_E = \text{diff. current}$ 

$$I_E = q D_p \frac{dp}{dx}$$

$$I_E \propto \frac{dp}{dx} \propto \frac{dp}{w_{\text{eff}}}$$

Now, due to early effect,  $w_{\text{eff}} \downarrow$  and  $I_E \uparrow$ . When  $w_{\text{eff}}=0$ , then the condition is called reach through or punch through &  $I_E$  will be very large.

- The variation of effective base width with  $|V_{CB}|$  is called Base width modulation or early effect. This results in following-
  - i) There is less chance of recombination in Base region as effective base width reduced. Therefore,  $\alpha \uparrow$  causing an  $\uparrow$  in collector current  $I_C$ .
  - ii) conc gradient of injected holes (minority carriers in base region) also  $\uparrow$  due to reduced base width. Since, diffusion current is directly proportional to conc gradient,  $I_E$  also  $\uparrow$ .
  - iii) for large value of  $V_{CB}$ , effective base width may be reduced to 0 causing extremely large  $I_E$ . This result in breakdown of transistor and is called punch through or reach through.

### Input and Output Characteristics :-

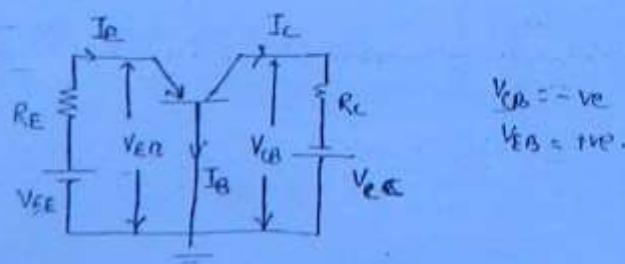
#### Common Base configuration :-

##### If characteristic -

$$V_{EB} = f_1(I_E, V_{CB})$$

##### of characteristic -

$$\nabla I_C = f_2(I_E, V_{CB})$$



## Output characteristic

$$I_C = \alpha I_E + I_{CO}$$

(119)

### 1) Cutoff Region -

$$I_E = 0; I_C = I_{CO}$$

$$\& I_C + I_B = I_E \Rightarrow I_B = -I_{CO}$$

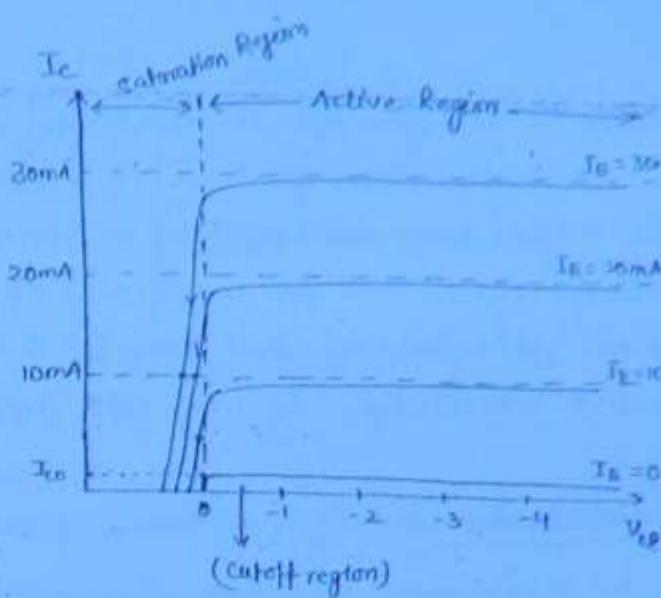
### 2) Active Region :-

$$I_C \approx \alpha I_E \quad \& \quad \alpha < 1 \Rightarrow I_C < I_E$$

Now, as  $V_{CE} \uparrow$ , junction is more RB.

&  $\alpha \uparrow$  due to early effect. (but the effect is very small).

→ In this region,  $I_C \propto I_E$  and is almost constant of  $V_{CE}$  variation, hence transistor in this config can be used as CCES.



### 3) Saturation Region :-

| 8 | 7 | 1 |

$$\frac{dI_E}{I_{CO}[e^{V_{CE}/V_T} - 1]}$$

$V_{CE} = +ve$  - since collector junction is RB.

①  $\alpha I_E \approx$  almost constant ; Hence, as  $V_{CE} \uparrow$  (+ve value),  $I'$  will  $\uparrow$  and hence  $I_C$  will decrease as  $V_{CE} \uparrow$  towards +ve value for constant off current.

② When we are from circuit,  $V_{EB} = -V_{CE} + I_C R_C$ .

If we  $\uparrow I_E$ , then  $I_C \uparrow \Rightarrow I_C R_C \uparrow \Rightarrow V_{CE} \uparrow$  towards +ve.

When  $|I_C R_C| > |V_{CE}|$ , then  $V_{CE} = +ve$ . → junction  $T_c \in FB$ .

After this,  $(xI_E) \uparrow$

- when  $I_E \uparrow$ ,  $\rightarrow I_C \uparrow \Rightarrow V_{CE} \uparrow \Rightarrow I' \uparrow$  and  $I_C = \alpha I_E - I' \approx$  constant hence off current will not change after this and the transistor will move into saturation.

## Important Points :-

- Cutoff → Region below  $I_E = 0$ .

- Active →  $I_C$  is <sup>almost</sup> independent of off voltage  $V_{CE}$ .

- off characteristic of CE is called constant current characteristic.

- It is a CCES.

- Saturation  $\rightarrow$  The region left to  $V_{CB} = 0$  and above  $T_C = 0$ .

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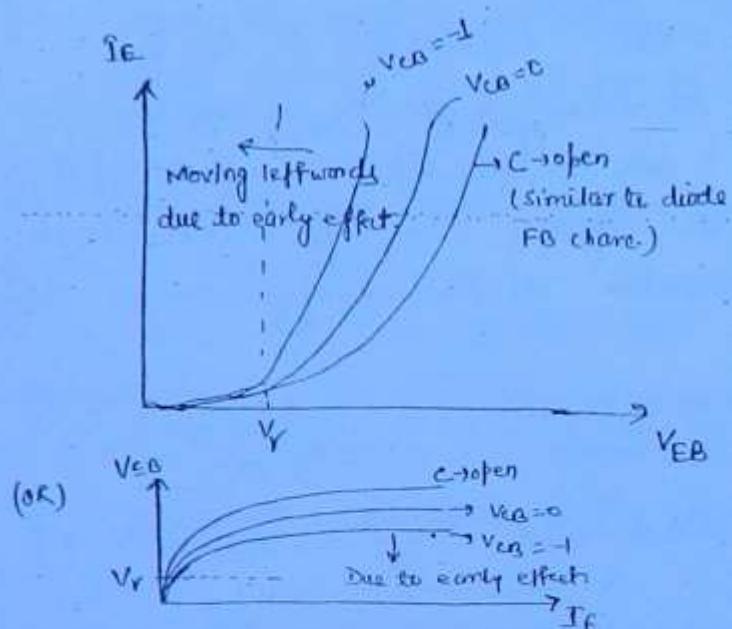
$\rightarrow$  As the collector junction is pB, the holes flow from p-type collector towards n-type base and constitute a current  $I'$  in a direction opposite to direction of  $+I_C$ . Even for small value of  $+V_{CB}$ , large change in  $I_C$  take place and characteristics fall towards 0 as  $V_{CB}$  is made more & more +ve. Since  $I' \uparrow$  exponentially,  $I_C$  may even become -ve.

Input Characteristics :-

$$V_{EB} = f_1(I_E, V_{CB}).$$

When  $V_{CB} = \infty$ , i.e., C-B  $\Rightarrow$  O.C. then charc will be similar to diode.

When  $V_{CB} = 0$ ,  $I_C =$  slightly R.B.  
Due to early effect,  $I_E \uparrow$  more rapidly.  
Now, as  $V_{CB} \uparrow$ , more early effect



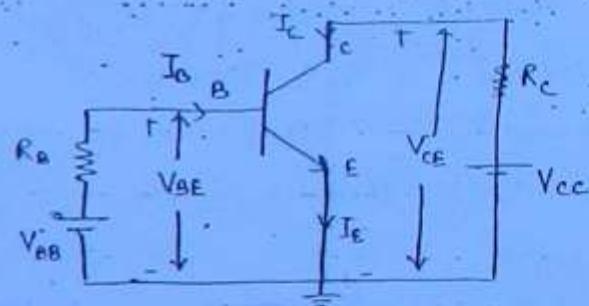
### III) COMMON Emitter Configuration:-

i/p charc -

$$V_{BE} = f_1(I_B, V_{CE}).$$

o/p charc -

$$I_C = f_2(I_B, V_{CE}).$$



$$I_C = \alpha I_E + I_{CO}$$

$$\therefore I_C = \alpha (I_C + I_B) + I_{CO}$$

$$\therefore I_C = \left( \frac{\alpha}{1-\alpha} \right) I_B + \frac{I_{CO}}{1-\alpha}$$

or

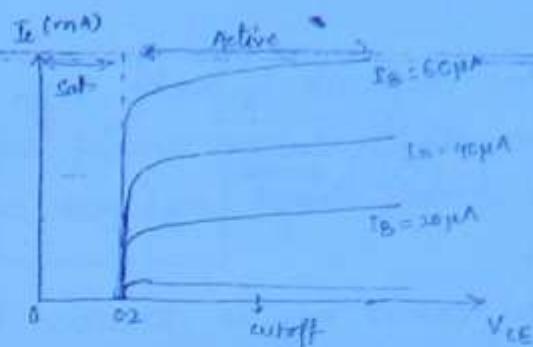
$$I_C = \beta I_B + (\beta + 1) I_{CO}$$

$$\beta = \frac{\alpha}{1-\alpha}$$

$\beta \gg \alpha$

## Output Characteristic

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### Active Region

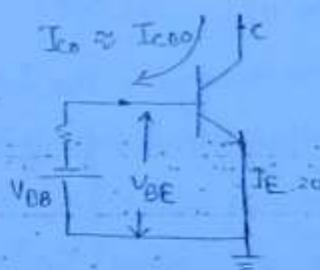
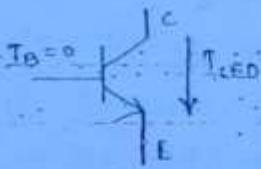
- $I_c = \beta I_B + (\beta+1) I_{C0} \approx \beta I_B$ .
- $V_{CE} = V_C - V_E = V_C$ .
- As  $V_{CE} \uparrow \Rightarrow V_C \uparrow \rightarrow I_c$  more RB  $\Rightarrow$  width  $\downarrow$  &  $\alpha \uparrow$   
Now,  $\alpha \rightarrow 0.98 \rightarrow 0.985 \rightarrow 0.99 \uparrow$   
 $\beta \rightarrow 49 \rightarrow 63 \rightarrow 34.1 \uparrow \rightarrow$  can't be neglected.
- $\rightarrow$  current gain is high, i.e., small change in  $I_B$  results in large change in  $I_c$

### Cutoff Region

- When  $I_B = 0$ ,  $I_c = (\beta+1) I_{C0}$   $\rightarrow I_c \neq I_{C0}$  and transistor is not in cut-off.
- $I_{CEO} = (\beta+1) I_{C0} = \frac{I_{C0}}{\alpha - \alpha}$   $\boxed{I_{CEO} \gg I_{C0}}, \because \beta \gg 1$ .

- Now, for  $I_c = I_{C0}$ ,  $I_E$  should be 0

- When  $I_E = 0$



$$I_{CEO} \gg I_{C0} > I_{C0}$$

- The  $I_c$  in a physical (real) non-idealized device when  $I_E = 0$  is designated by symbol  $I_{C00}$ .

- Cut-off is defined as a condition where  $I_c = I_{C0}$  and  $I_E = 0$ . In order to cut-off transistor, it is not enough to reduce  $I_B$  to 0, instead it is necessary to reverse bias the emitter junction slightly, i.e.,  $V_{BE} = -V_C$ .

$$V_{BE} = -0.1 \text{ for Ge; } 0.0 \text{ for Si.}$$

- The actual  $I_c$  with collector junction RB & base open is designated by symbol  $I_{CEO}$ .

- $I_{CEO}$  = reverse collector saturation current.

- Two factors co-operate to make  $I_{CBO}$  larger than  $I_{CO}$ .
  - a) There exist a leakage current which flows not through junction, but around it and through surfaces and it is proportional to voltage across the junction.
  - b) New carrier may be generated by collision in  $T_c$  transition region leading to avalanche multiplication of current.
- $I_{CBO} = \mu A$  for Ge  
 $\approx 1A$  for Si.
- $I_{CBO}$  approximately doubles for every  $10^\circ$  rise in temp for both Ge & Si and Si can be used upto about  $200^\circ C$  and Ge upto about  $150^\circ C$ .

31<sup>st</sup> August, 2012 :

### Saturation

$\rightarrow T_E \rightarrow PB$ ,  $T_C \rightarrow PR$ .

$$\rightarrow I_E = \beta I_B \quad ; \quad V_{CE} = V_{CE(sat)} - T_c R_E \quad (\text{from eqn})$$

When we  $\uparrow V_{BE}$ , then  $T_E \uparrow$  (due to barrier lowering), then  $I_E \uparrow$  and  $V_{CE} \downarrow$ .

When  $V_{BE} = 0.8V$ ,  $V_{CE} = 0.2V$  and is constant for further  $\uparrow$  in  $V_{BE}$ .

$$\therefore V_{CE(sat)} = 0.2V$$

$$I_{CE(sat)} = \frac{V_{CE} - V_{CE(sat)}}{R_E}$$

Now,  $V_{CE} + V_{EB} + V_{CB} = 0 \rightarrow V_{CE} - V_{BE} + V_{CB} = 0$   
 $\Rightarrow V_{EB} = 0.2 - 0.8 = -0.6V$ .

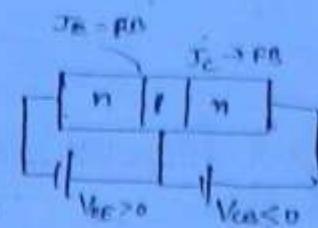
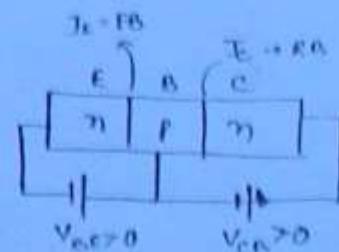
Now,  $T_E = PB$ , and a reverse current will start flowing which will oppose  $T_E$  and the diode transistor will go into saturation

$$\{ I_E = I_{CE} [e^{\frac{V_{CE}}{V_{Tsat}}} - 1] \}$$

If  $V_{BE}$  is kept constant and  $V_{CE}$  is changed -

When  $V_{CE} \uparrow \rightarrow V_{EB} \downarrow$

$V_{CE} = V_{CE} - V_{BE} \rightarrow V_{CB} \downarrow$  and when it is  $-ve$  then  $T_E \rightarrow PR$ .  
 i.e., Transistor will be in saturation.



### Checking Transistor for saturation

\* Let Q sit in saturation.

Then  $V_{CE} = V_{CEsat}$  &  $V_{BE} = V_{BEsat}$

$$I_{Csat} = \frac{V_{CC} - V_{CEsat}}{R_C}$$

$$\text{and } \because I_C = \beta I_B \Rightarrow I_{Csat} = \beta I_{Bmin} \Rightarrow \boxed{I_{Bmin} = \frac{I_{Csat}}{\beta}}$$

Now, if  $I_B \geq I_{Bmin}$ , then transistor is in saturation.

$$I_B = \frac{V_{BE} - V_{BEsat}}{R_B}$$

To bring transistor in saturation—

- 1) Increase  $I_B$  by  $\Delta V_{BE}$  so that  $I_B = I_{Bmin}$ .
- 2) If  $I_B$  constant, then  $\Delta I_{Bmin}$  so that  $I_{Bmin} = I_B$  by  $\Delta V_{CE}$  and/or  $\Delta R_C$ .
- 3) If  $I_B$  = constant &  $I_{Csat}$  = constant, then  $\Delta \beta$  so that  $I_{Bmin} = I_B$ .

At  $\beta = \beta_{max}$ ,

$$I_{Bmin} = \frac{I_{Csat}}{\beta_{max}} = I_B \Rightarrow \boxed{\beta_{max} = \frac{I_{Csat}}{I_B}}$$

Important points for saturation—

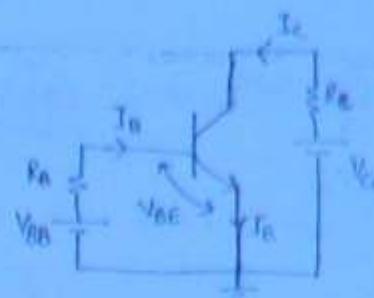
- $I_{Csat}$ ,  $I_B$  &  $I_C$  are  $R_B$  by cutin voltage  $V_T$ .
- If a transistor has to be operated in saturation region, we should design the ckt, so that  $I_B > I_{Bmin}$  by a factor of 2 to 10.
- The ratio of  $I_{Csat}$  &  $I_B$  to ensure saturation is called forced  $\beta$ .

Input Characteristic :-

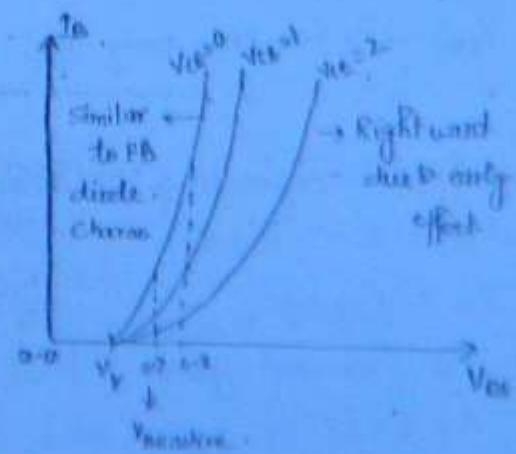
$$V_{BE} = f(I_B, V_{CE})$$

When  $V_{CE} = 0$ , op char similar to diode.

When  $V_{CE} \uparrow \Rightarrow V_C \uparrow \& V_E = 0$  due to early effect ( $V_{BE}$  more  $R_B$ ),  $I_B \downarrow$ .



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### III Common Collector Configuration

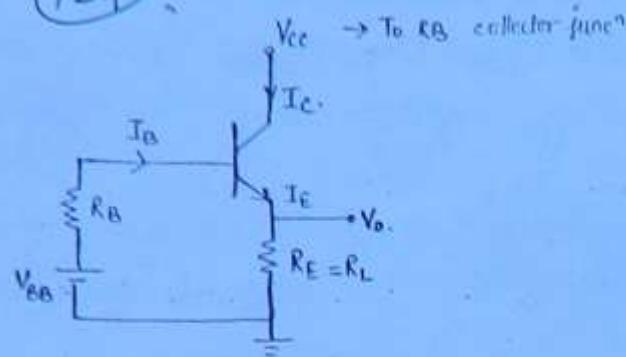
(124)

If char.: -

$$V_{CE} = f(I_C, V_{CE})$$

Op char.: -

$$I_E = f(I_B, V_{CE})$$



Output characteristic:

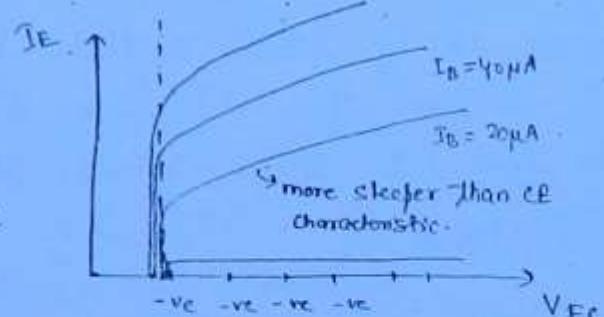
$$I_C = \beta I_B + (1+\beta) I_{CO}$$

$$\Rightarrow I_E - I_B = \beta I_B + (1+\beta) I_{CO} \Rightarrow I_E = (1+\beta) I_B + (1+\beta) I_{CO} \approx (1+\beta) I_B$$

- When  $V_{CE} \downarrow (-ve)$ ,  $V_{CE} \uparrow$  &  $I_C$  is more RB.

& due to early effect  $\propto \uparrow$  and  $\beta \uparrow$ .

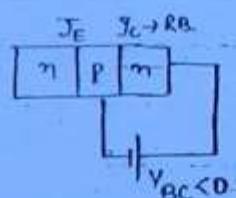
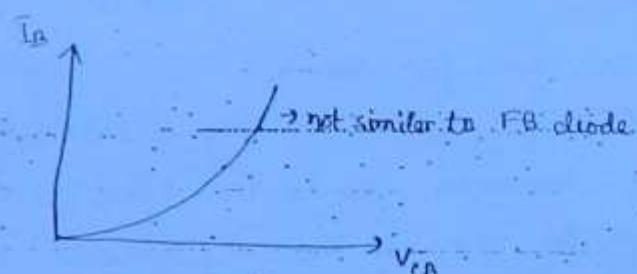
- Curve is more steep than CE config., since  $(\beta+1)$  variation is  $> \beta$  variation.



Input characteristic:

→ If characteristic is not similar to forward biased diode charac. since it is taken across RB junction.

→  $V_{BC} \uparrow \Rightarrow V_{CB} \downarrow \Rightarrow I_E \downarrow$  i.e.  $R_B$ , early effect  $\downarrow \& I_B \uparrow$ .



Important points regarding CC config.:-

- Highest  $f$  (50 kHz - 500 kHz)

- Lowest  $R_o$  ( $< 100 \Omega$ )

- Highest  $A_I$  (current gain);  $|A_I| = \frac{I_E}{I_B} = (1+\beta)$  } lowest for  $CB = \alpha$  }

- lowest  $A_V (< 1)$ ; typical value = 0.98. Max.  $A_V = 1$  (ideal condn), hence it

-  $IC$  is also called emitter follower.

- It is basically CC vs.

- Emitter follower is analogous to voltage follower in op-amp and source follower in FET.

Voltage follower & source follower are VCVS.

(125)

lowest Power gain ; typical value = 48.

Phase shift =  $0^\circ$ .

Application -

- i) Highest i/p resistance device.
- ii) As a buffer amplifier, i.e, an impedance matching device b/w high resistance & low resistance device.
- iii) As an audio freq. power amplifier.

Important Points for CB configuration:

lowest  $R_i$  ( $< 100\Omega$ )

Highest  $R_o$  ( $> 1M\Omega$ )

lowest  $A_I$  ( $= 1$ )

Highest  $A_V$

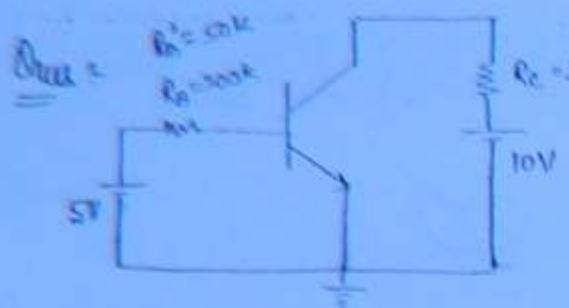
Moderate  $A_P$ , typical value = 68.

Phase shift =  $0^\circ$ .

CB-amplifier will offer largest bandwidth & hence more suitable for high freq. applications.

Application -

- i) As a constant current source
- ii) As an non-inverting voltage amplifier
- iii) As a high frequency amplifier
- iv) As an impedance matching device b/w low resistance & high resistance.



find transistor currents in ckt.

$\beta = 100, I_{C0} = 20\text{mA}, \text{ Si transistor}$

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\* Typical "Junction" voltages for npn transistor at 25°C -

	Si	Ge
$V_{BEsat}$	0.2	0.1
$V_{BEsat} = V_F$	0.8	0.3
$V_{BEactive}$	0.7	0.2
$V_{BEactive} = V_F$	0.5	0.1
$V_{CEsat}$	0.0	-0.1
$V_{CEactive} (V_{CEactive})$	> 0.2	> 0.1

\* For p-n-p transistor, sign of all the entries should be reversed.

$$\text{Sol}^n = \textcircled{1} R_B = 200\text{k}\Omega$$

$$\therefore V_{BE} = +ve = 5V \Rightarrow V_{BE} = +ve \Rightarrow I_E = \beta I_B$$

at B is in active region -

$$I_C = \beta I_B + (1+\beta) I_{C0}$$

$$I_B = \frac{V_{BE} - V_{BEactive}}{R_B} = \frac{5 - 0.7}{200\text{k}} = 0.0215 \text{ mA}$$

$$I_C \approx \beta I_B = 2.15 \text{ mA}$$

$$V_{CE} = 10 - 3k \cdot I_C \Rightarrow V_{CE} = 10 - 3 \times 2.15 = 3.55 \text{ V} \gg 0.2 \text{ V hence, transistor is in active region.}$$

$$\text{Alternatively, } V_{CB} = V_{CE} - V_{BE} \\ = 3.55 - 0.7 \\ = 2.85 \text{ V} \rightarrow \text{junction is definitely } R_B$$

$$\textcircled{2} R_E = 50\text{k}$$

$$I_B = \frac{5 - 0.7}{50} = 0.086 \text{ mA}$$

$$\therefore I_C = 8.6 \text{ mA} \Rightarrow V_{CE} = 10 - 3k(8.6) = -15.8 < 0.2 \Rightarrow \text{Tr} \rightarrow \text{saturation.}$$

our assumption was wrong

$$\therefore V_{BE} = V_{BEsat} = 0.8$$

$$V_{CE} = V_{CEsat} = 0.2$$

$$\therefore I_{Csat} = \frac{10 - 0.2}{3k} = \frac{3.27 \text{ mA}}{\text{say}}$$

$$I_B = \frac{5 - 0.8}{50\text{k}} = \frac{0.084 \text{ mA}}{\text{Ans}}$$

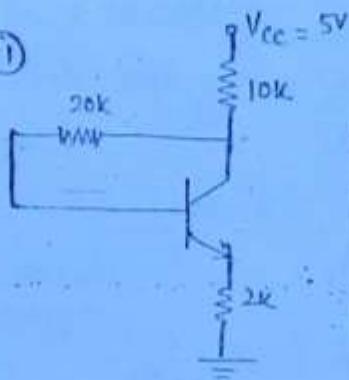
$$\times \text{Overdrive factor} = \frac{I_B}{I_{B\min}}$$

$$\text{Eq } I_{B\min} = \frac{3.27 \text{ mA}}{100} = 0.0327 \text{ mA}$$

(127)

$$\text{Overdrive factor} = \frac{0.081}{0.0327} = 2.5 \text{ times} \rightarrow \text{Hence, transistor is well in saturation}$$

Ques:-

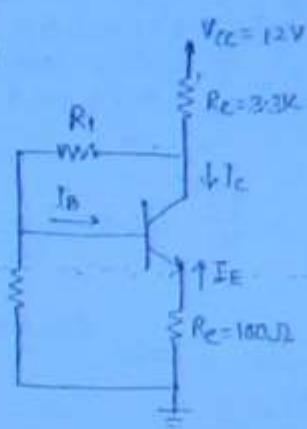


$\beta = 75$ , if done find  $V_C$ ?

If  $\alpha = 0.98$  and  $V_{BE} = 0.7 \text{ V}$   
find  $R_1$  in circuit for an  
emitter current  $I_E = 2 \text{ mA}$   
Neglect reverse sat. current.

$$\text{Ans: } I_B = 4.61 \mu\text{A}$$

$$V_C \approx 1.49 \text{ V}$$



Soln:

## Early Voltage:

(28)

Ques In the given ckt, determine the value of  $R_1$ ,  $R_2$  and  $R_L$  so that collector current through the transistor is 1mA.  $V_A = 3V$ ,  $V_{CE} = 6V$ . Take  $V_{BE} = 0.7V$  and let  $\beta$  of transistors are very high.

$$\text{Soln. } V_1 = V_{BE} + I_c R_E$$

$$V_1 = 0.7 + 0.2 \times 1 = 0.9V$$

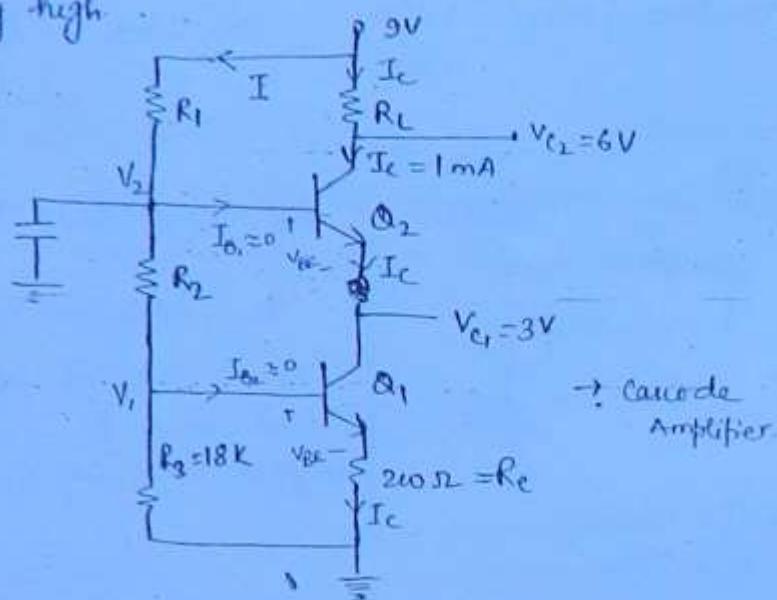
$$V_2 = V_{BE} + V_{CE1} = 0.7 + 3 = 3.7V$$

$$I_B = \frac{V_1}{R_3} = \frac{0.9}{18} = 0.05 \text{ mA}$$

$$\therefore R_1 = \frac{9 - V_1}{I} = 10K$$

$$R_2 = \frac{V_2 - V_1}{I} = 56K$$

$$- R_L = \frac{9 - 6}{1 \text{ mA}} = 3K$$



$$\left. \begin{array}{l} \because \beta = \text{very high} \Rightarrow \alpha \approx 1 \\ \Rightarrow I_C \approx I_E \text{ & } I_B \approx 0 \end{array} \right\}$$

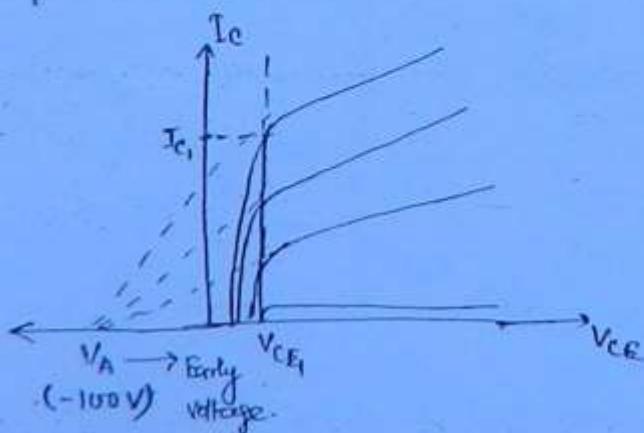
## Early Voltage:

- It helps in finding opf resistance of transistor.

$$\text{Slope } \frac{I_C - 0}{V_{CE1} - (-V_A)} = \frac{I_C}{V_{CE1} + V_A} \approx \frac{I_C}{V_A} = \frac{1}{r_0}$$

→  $|V_A| \gg |V_{CE1}|$

$$\rightarrow r_0 = \frac{V_A}{I_C} = \text{opf resistance of ckt}$$



- $V_A$  = very high for CB.  $\Rightarrow r_o$  = very high  $\approx \text{M}\Omega$ .
- $V_A$  for CC is slightly less than  $V_A$  of CE.  $\Rightarrow r_{OCC} < r_{OCE}$ .
- $V_{ACB} \gg V_{ACE} > V_{ACC}$  or  $r_{OCE} \gg r_{OCC} > r_{OCC}$

03<sup>rd</sup> September, 2012.

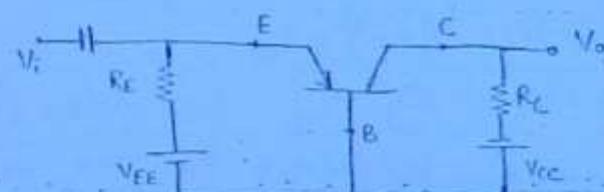
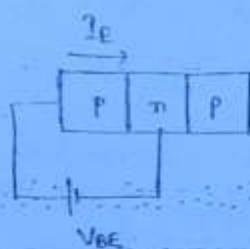
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Ques: In a CE-transistor, at  $V_{CE}=1V$ ,  $V_{EE}$  is adjusted to give a collector current of 1mA. Keeping  $V_{EE}$  constant,  $V_{CE}$  is  $\uparrow$  to 11V. Find new value of  $I_C$  if  $V_A = 150V$ .

$$\text{Soln: } \frac{0 - 1\text{mA}}{-100 - 1} = \frac{11 - 1}{11 - 1}$$

$$\Rightarrow \frac{+10}{101} = \alpha - 1 \Rightarrow \alpha = \frac{11}{101} = 1.09 \text{ mA.}$$

Transistor as an Amplifier :-



$$r_E = \frac{\eta V_T}{I_E} = \frac{V_T}{I_E} \quad [ \text{dynamic resistance} ]$$

or incremental resistance

$\Rightarrow$  Transistor is in active region

$$J_C \rightarrow FB, J_E \rightarrow PB$$

$$I_C = \alpha I_E + I_{CO} \approx \alpha I_E$$

AC analysis -

On applying signal at  $V_i$ . If  $V_i$   $\uparrow$  by  $\Delta V_i$ , then  $I_E$   $\uparrow$  by  $\Delta I_E$  &  $I_C$  also increases.

$$I_C + \Delta I_C = \alpha [I_E + \Delta I_E]$$

$$\Rightarrow [\Delta I_C = \alpha \Delta I_E]$$

Now,  $V_o$  will also  $\uparrow$ ,  $\Rightarrow \Delta V_o = \Delta I_C \cdot R_L \Rightarrow \Delta V_o = \alpha \Delta I_E \cdot R_L$ .

Change in i/p,  $\Delta V_i = \Delta I_E \cdot r_E$ .

In Voltage gain,  $A_v = \frac{\Delta V_o}{\Delta V_i} \Rightarrow \boxed{A_v = \frac{\alpha R_L}{r_E} \approx \frac{R_L}{r_E}}$  } max {

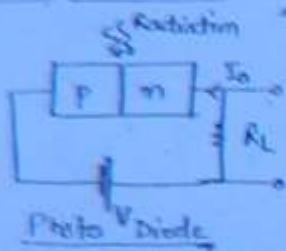
$\rightarrow$  lower path will also be  $\uparrow$ .

$\Rightarrow A_v \gg 1$  Hence, amplifier

→ Transistor provides power gain as well as voltage or current amplification. Current in low resistance if ckt is transferred to high resistance off ckt. The word transistor which originated as a contraction of transfer resistor is based upon above physical picture of device.

(130)

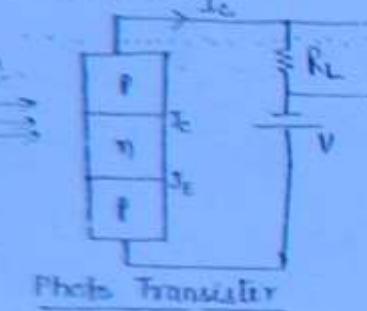
### Photo Transistor :- (Photo-diode)



$I_o$  = Reverse saturation current

Due to radiation  $T \uparrow$ ,  $I_o$  increases by  $\Delta I_o$ .

$V_o$  also increases by  $\Delta V_o$   $\therefore \Delta V_o = \Delta I_o \cdot R_L$ .



$J_E \rightarrow F_B$ ,  $J_C \rightarrow R_B$   $\Rightarrow$  Photo transistor is in active region

$I_c = \beta I_B + (1+\beta) I_{co}$  but  $I_B = 0$ , since base is open.

$$\therefore I_c = (1+\beta) I_{co}$$

Now, due to radiation,  $T \uparrow$ ,  $I_{co} \uparrow$ ;  $I_c \uparrow$  by  $\Delta I_c$ .

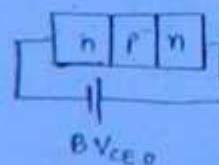
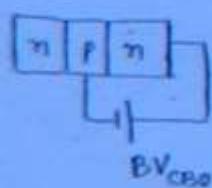
$$\Delta I_c = (1+\beta) \Delta I_{co}$$

$V_o$  increases by  $\Delta V_o = \Delta I_c \cdot R_L \Rightarrow \Delta V_o = (1+\beta) \Delta I_{co} \cdot R_L$

Therefore, Photo Transistor is more sensitive than photo diode by a factor  $(1+\beta)$ .

### Maximum Voltage rating of Transistor :-

#### Avalanche Multiplication :-



$$BV_{CEO} > BV_{CEO}$$

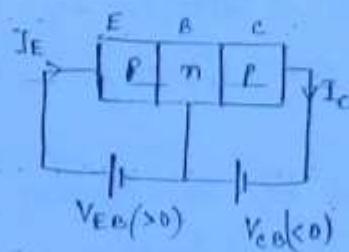
$BV_{CEO} \rightarrow$  maximum reverse biasing voltage which may be applied before breakdown b/w c & e of transistor, keeping E open., i.e.,  $I_E = 0$ .

•  $-BV_{CEO} \rightarrow$  for CE configuration, collector to emitter breakdown voltage with open circuit base.

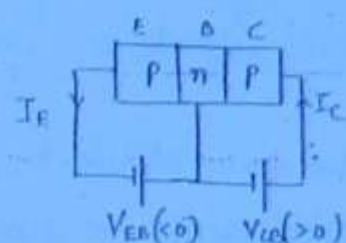
(31)

- Note
  - In a particular transistor, voltage limit is determined by punch-through or breakdown (due to avalanche multiplication) whichever occurs at the lower voltage.

### Ebers Moll Model



(Forward or Normal Active mode)  
( $\alpha_F$  or  $\alpha_H$ )



(Reverse or Inverse Active Mode)  
( $\alpha_I$ )

$$I_E \rightarrow FB, \quad I_C \rightarrow RB$$

$$I_C = \alpha_N I_E + I_{CO} \quad (1)$$

$$I_E \rightarrow RB, \quad I_C \rightarrow FB$$

$$I_E = \alpha_I I_C + I_{EO} \quad (2)$$

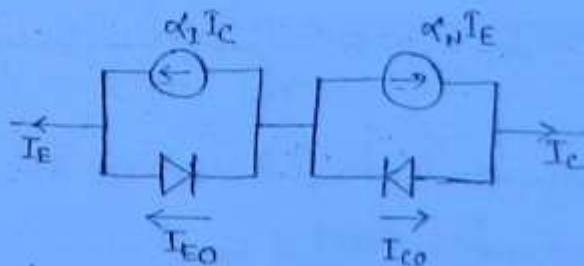
→ parameters  $\alpha_N, \alpha_I, I_{CO}, I_{EO}$  are not independent (experimentally).

They depend on each other as  $\alpha_N \cdot I_{EO} = \alpha_I \cdot I_{CO}$

→  $I_{EO} = 0.5 I_{CO}$  to  $I_{CO}$ . { i.e.,  $I_{EO} < I_{CO}$  as conc. of E > conc. of C }

⇒ conc. of minority in E < conc. of minority in C  
 $\Rightarrow I_{EO} \leq I_{CO}$

$$\frac{\alpha_N}{\alpha_I} = \frac{I_{CO}}{I_{EO}} \Rightarrow \boxed{\alpha_N \geq \alpha_I}$$



Now, if  $\alpha_I = \alpha_N = 0$  ;  
then



### Ebers Moll Model

- Model involves two ideal diodes placed back to back with reverse saturation current  $I_{co}$  &  $I_{eo}$  and two dependent current sources shunting ideal diodes. (132)
- Observe from the figure that, dependent current source can be eliminated from this figure provided  $\alpha_L = \alpha_N = 0$ . For e.g., by making base width much larger than diffusion length of minority carrier in base, then all minority carriers will recombine in base and none will survive to reach collector. for this case current gain  $\alpha$  will be 0. Under this condition transistor action ceases and we simply have two diodes placed back to back.
- This discussion shows why it is impossible to construct a transistor by simply connecting two separate or isolated diode in series opposing.
- A cascade of two p-n diode exhibits transistor properties like amplification only if carrier injected across one junction diffuse across 2nd junction.

### Cutoff Mode :

$$I_E \& I_C \rightarrow R_B \Rightarrow I_E = 0.$$

from eqn ① -  $I_C = I_{co}$

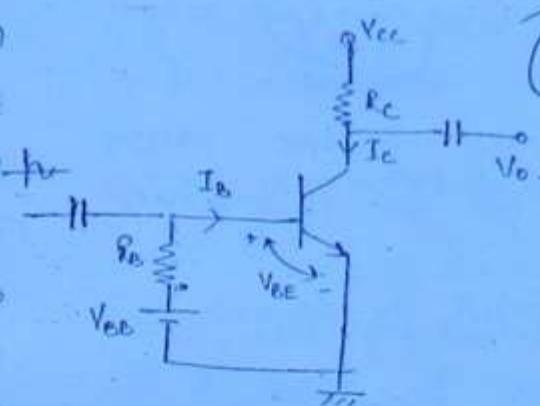
### Saturation

$$I_E \& I_C \rightarrow F_B.$$

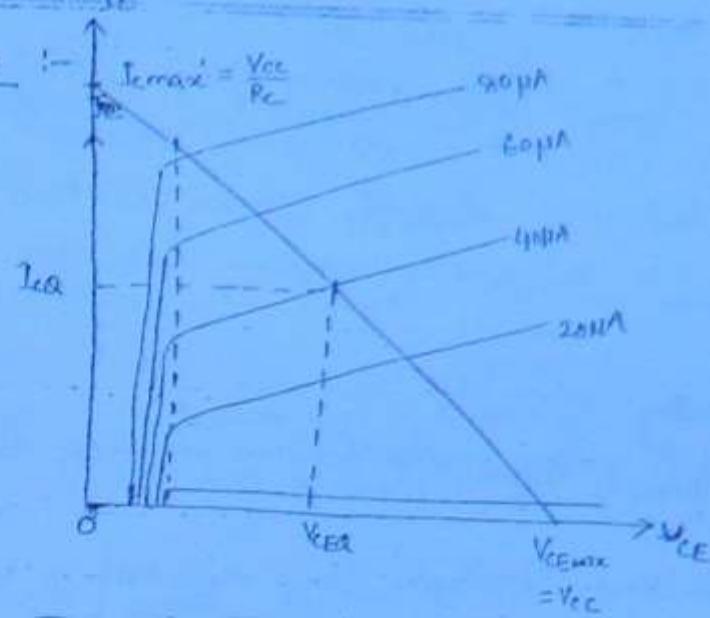
From eqn ① -

$$I_C = \alpha_N I_E - I_{co} [e^{\frac{V_{ce}}{V_T}} - 1]$$

## Transistor Biasing and Stabilization :-



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-  $J_E \rightarrow FB, J_C \rightarrow RB.$

$$I_B = \frac{V_{BE} - V_{BE}}{R_B} \quad \text{--- (1)}$$

$$- I_C = \beta I_B + (1 + \beta) I_{Co}$$

$$- V_{CE} = V_{CC} - I_C R_C \Rightarrow I_C R_C = V_{CC} - V_{CE} \Rightarrow I_C = -\frac{V_{CE}}{R_C} + \frac{V_{CC}}{R_C} \quad \text{--- (2)}$$

↳ DC load line.

Eqn (3) is similar to  $y = mx + c$ ; slope  $= -1/R_C$ .

$$\text{When, } V_{CE} = 0, I_{Co} = \frac{V_{CC}}{R_C}$$

$$\text{When, } I_C = 0, V_{CEmax} = V_{CC}$$

from eqn (1); set  $I_B = 40\mu A$ , then

$$\beta = \frac{I_{Co}}{I_B}$$

→ Q = Quiescent Point / operating point.  
→ Q = f (I\_B, I\_C, V\_{CE}).

On application of input -

$$I_b = I_B + i_b \Rightarrow I'_c = \beta I_b = \beta (I_B + i_b) = I_c + i_c$$

Eq (1)

Now, let  $I_B = 40\mu A$  &  $i_b = 20 \sin \omega t$ , then  $I_b = 40 + 20 \sin \omega t$

$$\therefore I_{bmax} = 60\mu A, I_{bmin} = 20\mu A$$

Hence, Q point lies well within the active region. Therefore, no distortion in the output.

Eg. 1  $I_B = 40\mu A$ ,  $i_b = 40 \sin \omega t$   
 $\therefore I_{b\max} = 80\mu A$ ,  $I_{b\min} = 0$

Hence, transistor is just in active region.

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Eg. 2  $I_B = 40\mu A$ ,  $i_b = 50 \sin \omega t$

$I_{b\max} = 90\mu A$ ,  $I_{b\min} = -10$ , Now, there will be distortion in the opp.

Eg. 3 Adjusting  $I_B = 20\mu A$ ,  $i_b = 20 \sin \omega t$   
 $I_{b\max} = 40\mu A$ ,  $I_{b\min} = 0 A$

Eg. 4  $I_B = 20\mu A$ ,  $i_b = 40 \sin \omega t$   
 $I_{b\max} = 60$ ,  $I_{b\min} = -20$   
 $\downarrow$  cutoff

Eg. 5  $I_B = 60\mu A$ ,  $i_b = 30 \sin \omega t \Rightarrow I_{b\max} = 90$ ,  $I_{b\min} = 30$   
 $\downarrow$  saturation

### Important Points:

- The collector characteristics or opp charc of transistor is divided into saturation, cutoff and active regions.
- Transistor can work as a switch when operated in saturation and cutoff region, ie, extreme ends of the characteristics.

### Procedure to plot dc load line & Q point -

- 1) Identify the value of  $V_{ce}$  &  $I_{c\max}$  of the circuit & locate this point on given charac.
- 2) Draw a straight line joining  $I_{c\max}$  &  $V_{ce}$  & this straight line is called dc load line.
- 3) find the operating values  $I_B$ ,  $I_c$  &  $V_{ce}$  for the given ckt & locate these values on given charac.
- 4) Project these operating values on dc load line & the intercepting point is called Q-point.

- The transistor is said to be under quiescent cond<sup>n</sup> when zero i/p signal is applied.

→ Transistor can work as an amplifier if Q point is within active region but Q point is temp. sensitive, i.e., as  $T \uparrow$ ,  $I_{CQ} \uparrow$  and  $V_{CEQ} \downarrow$ , so that Q point will be moving towards saturation region and if entered into saturation region, transistor will stop working as an amp.

(135)

- Trans. will provide more power gain or amplification, when Q point is in middle of dc load line.
- for a given trans., Q point is plotted to get faithful reproduction of op signal.
- If shape of op signal differs from shape of ip signal, it is said to be distorted.
- for a stable circuit, the variation in Q-point due to temp. must be small.

### Bias Stability:-

- Stability is effected due to -

#### 1) Temp. Instability

(a)  $I_{CO}$  → It doubles for every  $10^\circ$  rise in temp.  
 $T \uparrow \Rightarrow I_{CO} \uparrow \Rightarrow I_C \uparrow \Rightarrow$  Q point shift towards saturation.

#### (b) $V_{BE}$ →



$$\frac{dV_B}{dT} = -2.5 \text{ mV}/^\circ\text{C}, \text{ similarly } \frac{dV_{BE}}{dT} = -2.3 \text{ mV}/^\circ\text{C}$$

$$I_B = \frac{V_{BE} - V_{BE}}{R_B}$$

As  $T \uparrow$ ,  $V_{BE} \downarrow$ ,  $I_B \uparrow \Rightarrow I_C \uparrow$  &  $V_{CE} \downarrow$

→ Q → saturation.

Note -  $\beta \uparrow$  with temp but change is negligible.

2) Replacement of Transistor →  $\beta$  is highly affected due to replacement of transistor.

( $\because$  Since small change in  $\alpha$  results in large change in  $\beta$ )

# Stabilisation Techniques

## ↓ Biasing Techniques

- (i) C-B Biasing.
- (ii) Self-Biased

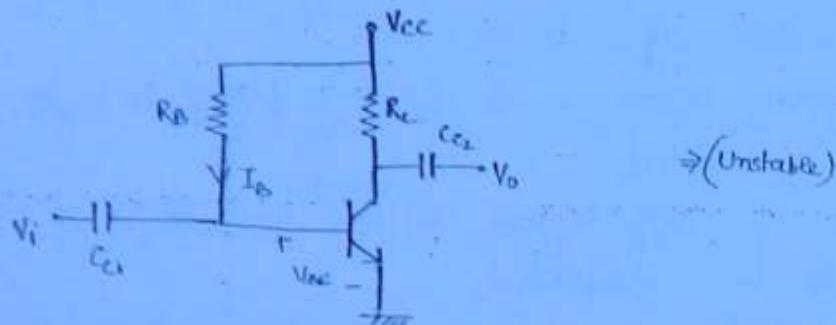
## ↓ Compensation Techniques

- (i) Diode compensation
- (ii) Zener & Thermistor compensation.
- (iii) Transistor compensation

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## Fixed Biased circuit :-

$$\rightarrow I_B = \frac{V_{CC} - V_{BE}}{R_B} = \text{constant}$$



⇒ (Unstable)

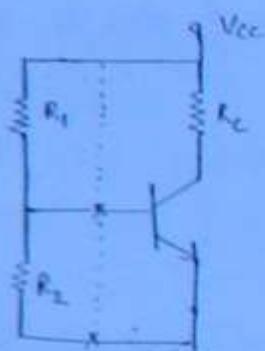
$$\rightarrow V_{Th} = \frac{R_2}{R_1 + R_2} \cdot V_{CC}$$

$$R_{Th} = R_1 \parallel R_2$$

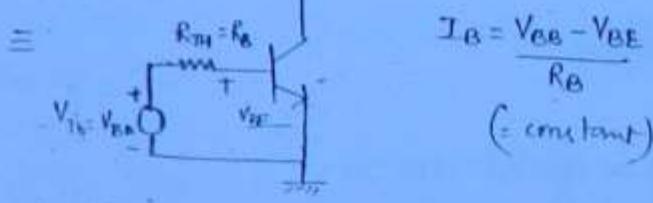
Normally,

$$R_1 \approx 10R_2$$

$$\text{i.e., } R_1 \gg R_2$$



⇒ (Unstable)



$$I_B = \frac{V_{CC} - V_{BE}}{R_B}$$

(- constant)

## Stability factors :-

$$\rightarrow S = \left[ \frac{dI_C}{dI_{Co}} \right]_{V_{BE} \& \beta = \text{constant}}$$

$$\rightarrow S' = S_\beta = \left[ \frac{dI_C}{d\beta} \right]_{I_{Co} \& V_{BE} = \text{constant}}$$

$$\rightarrow S'' = \left[ \frac{dI_C}{dV_{BE}} \right]_{I_{Co} \& \beta = \text{constant}} = S_\alpha$$

\* As  $T \uparrow$ ,  $I_{Co} \uparrow$ ,  $I_C \uparrow$   $\Rightarrow S = +ve$

As  $T \uparrow$ ,  $V_{BE} \downarrow$ ,  $I_C \uparrow$   $\Rightarrow S' = S_\alpha = -ve$

As  ~~$T \uparrow$~~ ,  $\beta \uparrow$ ,  $I_C \uparrow$   $\Rightarrow S'' = +ve$

(because of recombination)  
(not owing temp.)

## Stability Factor, $S$

$$S = \left| \frac{dI_c}{dI_{c_0}} \right| \quad |v_{BE} \text{ and } \beta = \text{constant.}$$

In active region,

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$$I_c = \beta I_B + (1+\beta) I_{c_0}$$

$$\Rightarrow \frac{dI_c}{dI_{c_0}} = \beta \cdot \frac{dI_B}{dI_{c_0}} + (1+\beta)$$

$$\Rightarrow \frac{dI_c}{dI_{c_0}} = \beta \cdot \frac{dI_B}{dI_c} \cdot \frac{dI_c}{dI_{c_0}} + (1+\beta)$$

$$\Rightarrow \frac{dI_c}{dI_{c_0}} \left[ 1 - \beta \cdot \frac{dI_B}{dI_c} \right] = (1+\beta)$$

$$\Rightarrow S = \boxed{\frac{1+\beta}{1-\beta \cdot \frac{dI_B}{dI_c}}}$$

→ If  $I_B = \text{constant}$ ,  $\frac{dI_B}{dI_c} = 0 \Rightarrow [S = (1+\beta)] \rightarrow \text{ckt is unstable} \rightarrow \text{fixed Bias}$

→ If  $I_c \uparrow$  then  $I_B$  should  $\downarrow$ , i.e.,  $dI_B/dI_c < 0$ . In ideal case,  
 $\frac{dI_B}{dI_c} = -1 \Rightarrow [S = 1] \rightarrow \text{highly stable.}$

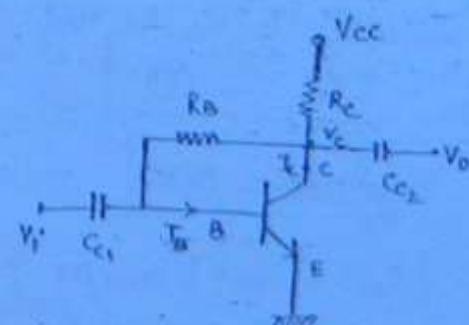
For  $[S < 1+\beta]$ , circuit is stable.

Range of  $S \Rightarrow [1 < S < (1+\beta)]$

## Techniques :-

### 1) Collector-Base Bias -

→ During DC analysis,  $C_C 1$  &  $C_C 2$  will act as open circuit.



When transistor is in active region-

$$I_c = \beta I_B + (1+\beta) I_{CO} \quad \text{--- (1)}$$

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On manipulating (1) -

$$S = \frac{dI_c}{dI_{CO}} = \frac{1+\beta}{1-\beta \cdot \frac{dI_B}{dI_c}} \quad \text{--- (2)} \quad (\text{keeping } V_{BE} \text{ & } \beta \text{ constant})$$

$$\text{Applying KVL at } i/p - V_{CC} = (I_c + I_B) R_C + I_B R_B + V_{BE}$$

$$\text{Differentiating w.r.t } I_c - 0 = (R_C + R_B) \frac{dI_B}{dI_c} + R_C + 0$$

$$\Rightarrow \frac{dI_B}{dI_c} = \frac{-R_C}{R_C + R_B} \quad \text{--- (3)}$$

Substituting (3) in (2) -

$$S = \frac{1+\beta}{1 + \frac{\beta R_C}{R_C + R_B}} < (1+\beta) \quad \text{Hence circuit is stable.}$$

∴

$$S = (1+\beta) \cdot \frac{R_C + R_B}{R_B + R_C (1+\beta)}$$

→ If  $R_C (1+\beta) \gg R_B$ , then

$$S = 1 + \frac{R_B}{R_C} \quad \text{GM}$$

→ If  $R_C \uparrow$  &  $R_B \downarrow$  then  $S \downarrow$ ; hence  $S$  depends on load resistance.

→ It is voltage shunt feedback, hence  $R_i \downarrow$  and  $R_o \downarrow$ .

→ There is unnecessary -ve feedback, circuit is not preferable.

Theoretical Analysis -

From circuit,  $V_C = V_{CC} - (I_c + I_B) R_C$  and  $I_c = \beta I_B + (1+\beta) I_{CO} \approx \beta I_B$

$$\therefore V_C \approx V_{CC} - I_c R_C \quad \text{--- (1)} \quad \because \frac{I_c}{\beta} = I_B \Rightarrow I_c \gg I_B$$

$$I_B = \frac{V_C - V_{BE}}{R_B} \quad \text{--- (2)}$$

From (1) & (2) - If  $T \uparrow$ ,  $[I_{CO} \uparrow \text{ & } V_{BE} \downarrow]$  and/or  $\beta \uparrow$ , then  $I_c \uparrow$   
then  $V_C \downarrow$  but  $I_B \downarrow$  with  $V_C \uparrow \Rightarrow I_c \downarrow$  → -ve feedback

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Therefore, rise in  $I_c$  is compensated and  $I_c$  is almost constant.

This circuit will compensate for all type of variations, i.e.,  $T_{\text{ao}}$ ,  $V_{\text{BE}}$  or  $\beta$ .

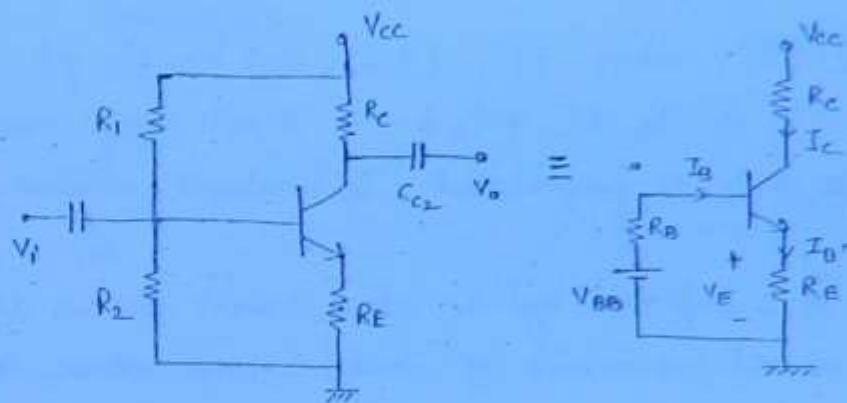
Self-Biased Circuit :

$$\rightarrow V_{BB} = V_{Th} = \frac{R_2}{R_1 + R_2} V_{CC}$$

$$\rightarrow R_{Th} = R_E = R_1 \parallel R_2$$

$$\rightarrow I_c = \beta I_B + (1+\beta) I_{Co} \quad \text{--- (1)}$$

$$\rightarrow S = \frac{1+\beta}{1 - \beta \frac{dI_B}{dI_c}} \quad \text{--- (2)}$$



Writing KVL at i/p —

$$V_{BB} = I_B R_E + V_{BE} + (I_B + I_c) R_E$$

$$\rightarrow \text{Differentiating wrt } I_c \rightarrow 0 = (R_E + R_E) \frac{dI_B}{dI_c} + R_E$$

$$\Rightarrow \frac{dI_B}{dI_c} = -\frac{R_E}{R_E + R_E} \quad \text{--- (3)}$$

$\rightarrow$  Substituting in eqn (2) —

$$S = \frac{1+\beta}{1 + \beta \frac{R_E}{R_E + R_E}}$$

$< (1+\beta) \rightarrow$  Hence ckt is stable.

$$\Rightarrow S = (1+\beta) \cdot \frac{R_E + R_E}{R_E + (1+\beta)R_E}$$

$\rightarrow$  If  $(1+\beta)R_E \gg R_E$ , then,

$$S = 1 + \frac{R_E}{R_E}$$

$\rightarrow \left\{ \begin{array}{l} \text{Advantage} - \\ \text{Independent of load resistance} \end{array} \right\}$

$\rightarrow$  Ideally,  $R_E = \infty$ ,  $S=1$ , Hence,

$R_E \uparrow$  and/or  $R_E \downarrow$ , then  $S \uparrow$

\* Input & o/p resistance will increase.

\* It is current series feedback.

Biasation :-

$$- V_E = (I_B + I_C) R_E \approx I_C R_E \quad \left\{ \begin{array}{l} I_C > I_B \\ \end{array} \right\} \quad \text{--- (1)}$$

$$- I_B = \frac{V_{BE} - V_{BE0} - V_E}{R_B} \quad \text{--- (2)}$$

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from (1) & (2), when  $T \uparrow$ ,  $[I_{C0} \uparrow, V_{BE0}]$  and/or  $\beta \uparrow$ , then  $I_C \uparrow \Rightarrow V_E \uparrow$ .

$\Rightarrow I_B \downarrow \Rightarrow T_c \downarrow \rightarrow I_C$  will control variation due to all factors.

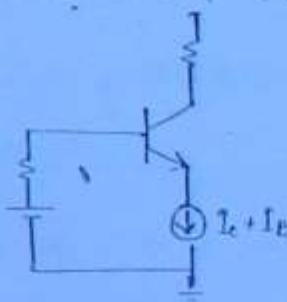
A rise in  $I_C$  is compensated,  $I_C$  is almost constant.

\* If  $R_E$  is replaced by an ideal current source, then  $S$  will become 1.  
(as internal resistance of active (current) source is very high, ideally  $\infty$ ).

Ideal current source,

$$R_S = R_E = \infty$$

$$\rightarrow S = 1$$



Practical current source,

$$R_S = \text{very high}$$

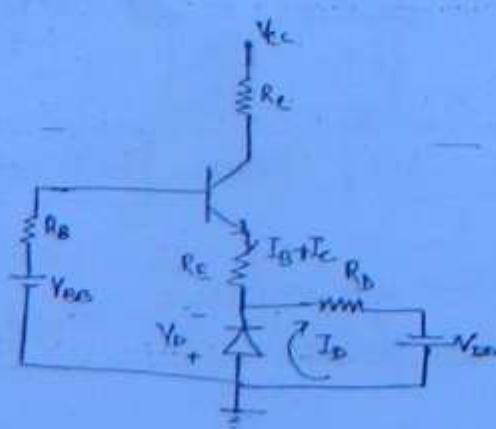
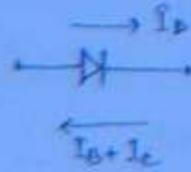
\* Self-biased circuit is also called as voltage divider / Potential divider or emitter bias circuit.

### Compensation Techniques :- (Bias compensation)

→ Compensation techniques refers to use of temp-sensitive devices like diode, thermistor, transistor etc.

### Diode Compensation :-

a) for  $V_{BE}$  :-



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$$\rightarrow I = I_B + (I_A + I_C) \quad \left. \begin{array}{l} \text{From eqn, for diode to be FB} \\ \text{---} \end{array} \right\}$$

$$\rightarrow I_B = \frac{V_{BE} - V_D}{R_B} \quad \text{--- (2)} \quad \left. \begin{array}{l} I > 0 \rightarrow I_B > I_A + I_C \\ \text{Hence, if we set } I_B \text{ based on eqn(2) then we can} \\ \text{make D forward bias.} \end{array} \right\}$$

→ When  $D \rightarrow FB$ , then

→ Transistor is in active region

$$\rightarrow I_C = \beta I_B + (1+\beta) I_{AO} \quad \text{--- (1)}$$

$$\text{and, } \frac{I_C - (1+\beta) I_{AO}}{\beta} = I_B \quad \text{--- (2)}$$

$$\text{from circuit, } V_{EB} = I_B R_B + V_{EE} + (I_C + I_B) R_E - V_D \quad \text{--- (3)}$$

from (2) & (3) :-

$$I_C = \frac{\beta [V_{EB} - (V_{EE} - V_D)] + (R_B + R_E)(1+\beta) I_{AO}}{R_B + R_E(1+\beta)} \quad \text{--- (4)}$$

If, transistor & diode are of similar materials -

$$\therefore \frac{dV_{EE}}{dT} = \frac{dV_D}{dT} = -2.5 \text{ mV/}^{\circ}\text{C}$$

$\therefore I_C$  depends on  $(V_{EB} - V_D)$

$$\text{for } I_C \text{ change in } T, \quad (V_{EB} - 2.5) - (V_D - 2.5) = (V_{EB} - V_D)$$

Hence,  $I_C$  will remain constant, even if  $V_{EE}$  is changing.

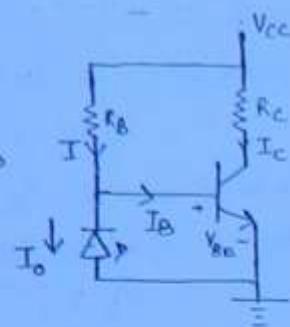
→ The change of  $V_{EB}$  with temp contribute significantly to change in  $I_C$  of Silicon transistor, therefore circuit is useful for stabilising Si transistor.

→ The diode is kept forward biased by source  $V_D$  and resistance  $R_D$ .

If the diode is of same material & type, voltage  $V_D$  across diode will have same temp. coeff as  $V_{EB}$ . then from eqn (4), it is clear that  $I_C$  will be insensitive to variation in  $V_{EE}$ .

(b) for  $I_{CO}$  :-

→ For Ge,  $V_{BE} = 0.2V$  = voltage across diode since they are in parallel



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$$\rightarrow I_B = I - I_0.$$

$$\rightarrow I = \frac{V_{CC} - V_{BE}}{R_B} = \text{constant} \quad \left\{ \text{considering } V_{BE} = \text{constant} \right\}$$

$$\rightarrow I_C = \beta I_B + (1 + \beta) I_{CO}$$

$$\Rightarrow I_C = \beta [I - I_0] + (1 + \beta) I_{CO} \Rightarrow I_C = \beta I - \beta I_0 + \beta I_{CO} \quad \left\{ \because \beta \gg 1 \right\}$$

$$\Rightarrow I_C = \beta I + \beta (I_{CO} - I_0) \quad \downarrow \text{constant}$$

→ for Ge transistor, change in  $I_{CO}$  with temp. play more important role in collector current stability, therefore, this ckt is useful for stabilizing Ge Br.

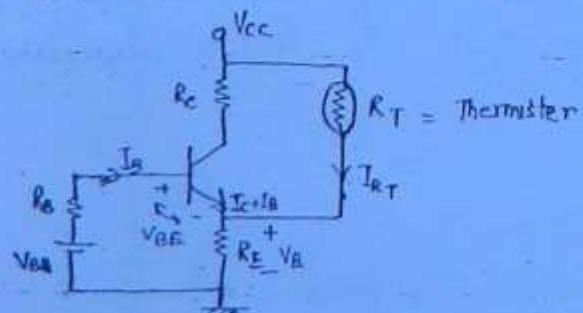
→ If the diode & Br. are of same type, then  $I_0$  of diode will vary with T at same rate as  $I_{CO}$ . Therefore,  $I_C$  will be insensitive to variation in  $I_{CO}$ .

## 2) Thermistor and Sensistor compensator :-

→ Thermistor → NTC of resistivity ;  $T \uparrow \sigma \uparrow$   
(lightly doped)

→ Sensistor → PTC of resistivity ;  $T \uparrow, \sigma \downarrow$   
(highly doped)

$$\Rightarrow I_B = \frac{V_{BB} - V_{BE} - V_E}{R_B}$$



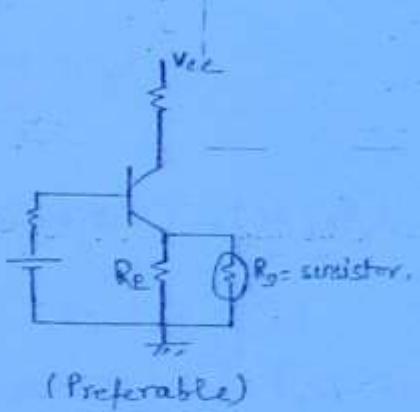
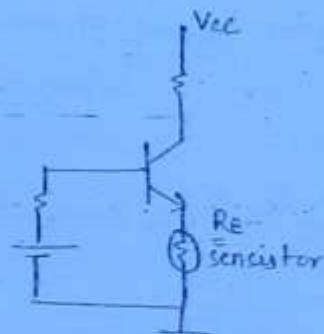
$$\rightarrow V_E = (I_B + I_C + I_{R_T}) R_E \approx (I_C + I_{R_T}) R_E$$

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Now, When  $T \uparrow$ , ( $I_{CO} \uparrow, V_{BE} \downarrow$ ), then  $I_C \uparrow, R_T \downarrow \Rightarrow I_{R_T} \uparrow \Rightarrow V_E \uparrow$   
 $\Rightarrow I_B \downarrow \Rightarrow I_C \downarrow$

Hence, rise in  $I_C$  is compensated.

By using sensistor -



\*  $R_E$  replaced by a sensistor or we can place a sensistor parallel to  $R_E$ .

$$T \uparrow \quad \text{---} \quad R_E = 1\text{ k}\Omega \quad T \uparrow \quad \text{---} \quad R_E = 2\text{ k}\Omega \quad \Delta R_E = 100\%$$

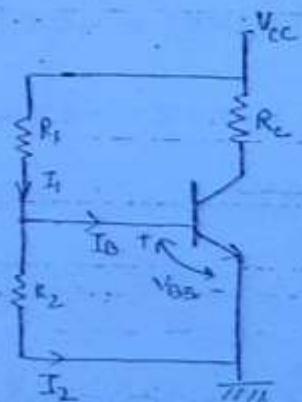
$$2\text{ k}\Omega \quad \text{---} \quad R_E = 1\text{ k}\Omega \quad T \uparrow \quad \text{---} \quad \frac{1}{2} \text{ k}\Omega \quad R_E = 4\text{ k}\Omega \quad \Delta R_E = 333\%$$

→ Hence, controlled feedback by using sensistor.

$$I_B = I_1 - I_2$$

$$I_1 = \frac{V_{CC} - V_{BE}}{R_1}$$

$$I_2 = \frac{V_{BE}}{R_2}$$



Now, when  $T \uparrow$ , ( $I_{CO} \uparrow, V_{BE} \downarrow$ )  
then  $I_C \uparrow$ , then to compensate  
we want  $I_B \downarrow$ . or  
 $\Rightarrow I_1 \uparrow$  and/or  $I_2 \uparrow$   
 $\Rightarrow R_1 \uparrow$  and/or  $R_2 \downarrow$

Hence,  $R_1$  can be replaced by sensistor &  $R_2$  can be replaced by thermistor. or  $R_1$  can be replaced by sensistor in II &  $R_2$  with thermistor in II.

Ques: In two stage ckt, assume  $\beta = 100$  for each transistor.

(a) Determine R so that

Quiescent conditions are

$$V_{CE_1} = -4V, V_{CE_2} = -6V$$

(b) Explain how Q-point stabilization is obtained.

Take  $V_{BE} = 0.2V$ .

Soln: Since  $\beta \gg 1$ ,  $I_{B_2} \ll I_{C_2}$  &  $I_{B_1} \ll I_{C_1}$ .  $\Rightarrow$  we will neglect  $I_{B_1}$  &  $I_{B_2}$ .

By NR—

$$-24 - 17.8(I_{C_1}) - V_{CE_1} - 2.2(I_{C_1}) = 0 \quad \Rightarrow \quad -24 - (-4) = 17.8I_{C_1} + 2.2I_{C_1}$$

$$\Rightarrow -20I_{C_1} = 20$$

$$\Rightarrow I_{C_1} = -1mA$$

By KVL—

$$-24 - 8I_{C_2} - V_{CE_2} - 1(I_{C_2}) - 3(I_{C_2}) = 0$$

$$\Rightarrow I_{C_2} = -1.5mA$$

Now,

$$R = \frac{V_A - V_B}{I_{B_1}} ; \quad V_A = 3K \times (-1.5) = -4.5V$$

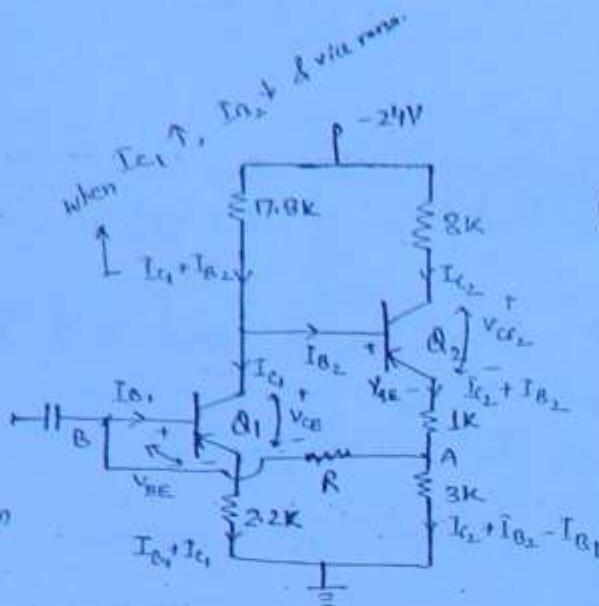
$$V_B = V_{BE} + I_{C_1} \times 2.2 = -0.2 - 1 \times 2.2$$

$$\therefore V_B = -2.4V$$

$$\Rightarrow I_{B_1} = \frac{I_{C_1}}{\beta} = -0.01mA$$

$$\therefore R = \frac{-4.5 - (-2.4)}{-0.01} = 210K\Omega$$

- (b) When  $T \uparrow$ ,  $|I_{C_2}| \uparrow$ ,  $|V_A| \uparrow$ ,  $|I_{C_1}| \uparrow$ ,  $|I_{C_1}| \uparrow$ ,  $|I_{B_2}| \downarrow$ ,  $|I_{C_2}| \downarrow \rightarrow$  compensated.  
When  $T \uparrow$ ,  $|I_{C_1}| \uparrow$ ,  $|I_{B_2}| \downarrow$ ,  $|I_{C_2}| \downarrow$ ,  $|V_A| \downarrow$ ,  $|I_{B_1}| \downarrow$ ,  $|I_{C_1}| \downarrow \rightarrow$  ".



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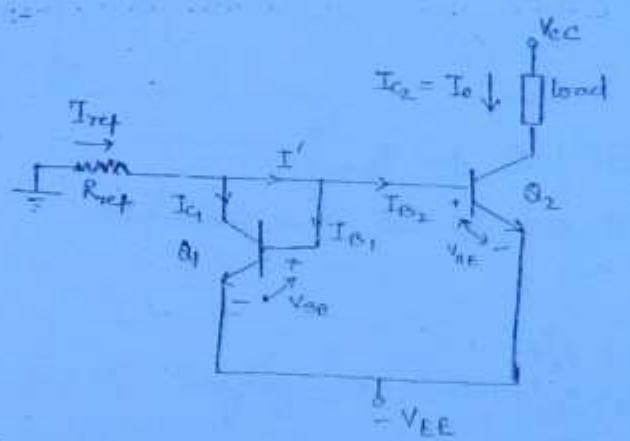
04<sup>th</sup> September, 2012

## Current Mirror circuit :-

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- The output current is forced to equal the i/p current, i.e., o/p current is a mirror image of i/p current.
- They are widely used in designing of differential amplifiers & etc.
- Their major advantages are-
  - Simplicity in circuit design.
  - Easy to fabricate.
  - Minimum no. of components are reqd.
  - Low cost.

### - Basic Diagram :-



### Reqd. conditions :-

- Both Tr. are in active region
- Both Tr. are identical, i.e.,  $\beta_1 = \beta_2 = \beta$  &  $V_{BE1} = V_{BE2} = V_{BE}$ .
- $\beta$  should be very large.

Writing KVL -

$$0 - I_{ref} \cdot R_{ref} - V_{EE} = -V_{EE}$$

$$\Rightarrow I_{ref} = \frac{V_{EE} - V_{EE}}{R_{ref}} \rightarrow \text{independent of load.}$$

from figure -

$I_{e1} = I_{e2}$  { }  $\because I'$  is divided among two identical paths { }

$$\therefore \beta_1 = \beta_2 \Rightarrow [I_{c1} = I_{c2}]$$

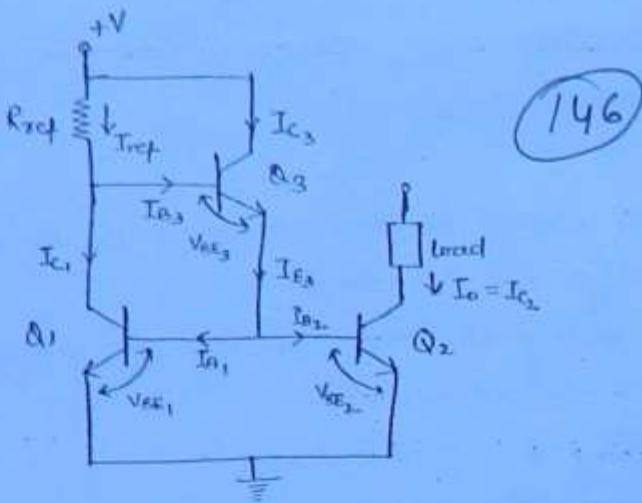
$$\text{Since, } I_{ref} = I_{c1} + I' \Rightarrow I_{ref} = I_{c2} + 2I_{e2}$$

$$\Rightarrow I_{ref} = I_{c2} + \frac{2I_{c2}}{\beta}$$

$$\therefore [I_{ref} \approx I_{c2}] \text{ if } \beta \text{ is very large.}$$

- $\rightarrow Q_1, Q_2, Q_3$  are in active region  
 $\rightarrow Q_1, Q_2, Q_3$  should be identical,  
 i.e.,  $\beta_1 = \beta_2 = \beta_3 = \beta$  &  $V_{BE_1} = V_{BE_2} = V_{BE_3} = V_{BE}$

$\rightarrow$



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By applying KVL -

$$V_+ = I_{ref} \cdot R_{ref} + V_{BE_3} + V_{BE_2}$$

$$\Rightarrow \boxed{I_{ref} = \frac{V_+ - 2V_{BE}}{R_{ref}}} \quad \text{--- (1)} \quad \left\{ \text{Independent of load} \right\}$$

Now,

$$\boxed{I_{ref} = I_{c_1} + I_{B_3}} \quad \text{--- (2)}$$

from fig.,  $I_{B_1} = I_{B_2}$   $\left\{ \because \text{identical paths for } T_{E_3} \right\}$ .

$$\Rightarrow \boxed{I_{c_1} = I_{c_2}} \quad \left\{ \because \beta_1 = \beta_2 \right\} \quad \text{--- (3)}$$

$$\rightarrow I_{B_3} + I_{c_3} = I_{E_3} \Rightarrow I_{E_3} = I_{B_3} + \beta_3 I_{B_3}$$

$$\Rightarrow I_{E_3} = (\beta_3 + 1) I_{B_3}$$

$$\Rightarrow 2I_{B_2} = (\beta_3 + 1) I_{B_3}$$

$$\Rightarrow \frac{2I_{c_2}}{\beta} = (\beta_3 + 1) I_{B_3}$$

$$\Rightarrow \boxed{I_{B_3} = \frac{2I_{c_2}}{\beta(1+\beta_3)}} \quad \text{--- (4)}$$

from (2) & (4) in (2) -

$$\therefore I_{ref} = I_{c_2} + \frac{2I_{c_2}}{\beta(1+\beta_3)} \Rightarrow \boxed{I_0 = \frac{I_{ref}}{1 + \frac{2}{\beta(1+\beta_3)}}} \quad \left\{ \because I_0 \in I_{c_2} \right\}$$

Since  $\beta \gg 1$  -

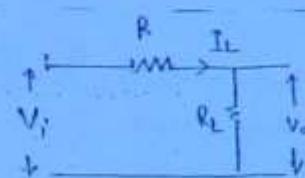
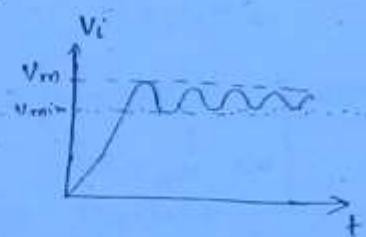
$$I_0 = \frac{I_{ref}}{1 + \frac{2}{\beta^2}} \Rightarrow \boxed{I_0 \approx I_{ref}} \quad \Rightarrow \boxed{I_0 = \left( \frac{\beta^2 + \beta}{\beta^2 + \beta + 2} \right) \cdot I_{ref}}$$

→ It is necessary that  $\Omega_1$  &  $\Omega_2$  are identical. If  $\Omega_3$  is not identical then

$$T_0 = \frac{T_{ref}}{1 + \frac{2}{\beta(1 + \beta_3)}} \quad \left\{ \beta_3 \rightarrow \text{for } Q_3 \right\}. \quad (147)$$

→ In this circuit, it is not required to have very high  $\beta$ , since a term of  $\beta^2$  is appearing in denominator which will be very large.

## Voltage Regulator circuit :-



→ Line variation :- variation in  $V_0$  due to variation in line voltage  $V_L$ .

→ load variation :-  $R_L$   $\rightarrow$  load resistance  $R_L$ .

Line Regulation :-  $V_i = \text{varying}$ ,  $R_L = \text{constant}$ ,  $V_o$  should be constant.

$V_o = I_L R_L$ , hence  $I_L$  should be constant for  $V_o$  to be constant.

Hence in line regulation, line voltage is varying but load current remains constant.

Load Regulation:  $V_i = \text{constant}$ ,  $R_L = \text{varying}$ ,

for  $V_o$  to be constant, when  $R_L, T_L$  should  $\downarrow$  and vice versa.

Hence, in load regulation,  $R_L$  varying but  $V_0$  remains constant.

## Voltage Regulator:

It regulates load voltage.

- In regulator circuit, load voltage  $V_o$  will be maintained almost constant irrespective of load variations & input voltage variations (the variant).

- Performance of a regulator ckt is analysed by its regulation, i.e.,

$$\% \text{ Regulation} = \frac{V_{NL} - V_{FL}}{V_{FL}} \times 100\%$$

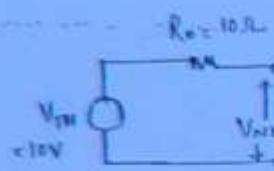
(148)

$\rightarrow V_{NL}$  = No load voltage ,  $I_L \rightarrow 0$  or  $R_L \rightarrow \infty$ .

$V_{FL}$  = Full load voltage ,  $I_L \rightarrow I_{L\max}$  or  $R_L \rightarrow R_{L\min}$ .

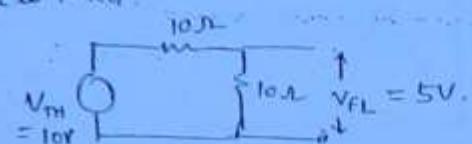
$\rightarrow$  Ideally,  $V_{NL} = V_{FL}$  &  $\% \text{ Regulation} = 0\%$ .

Q



$$V_{NL} = 10V$$

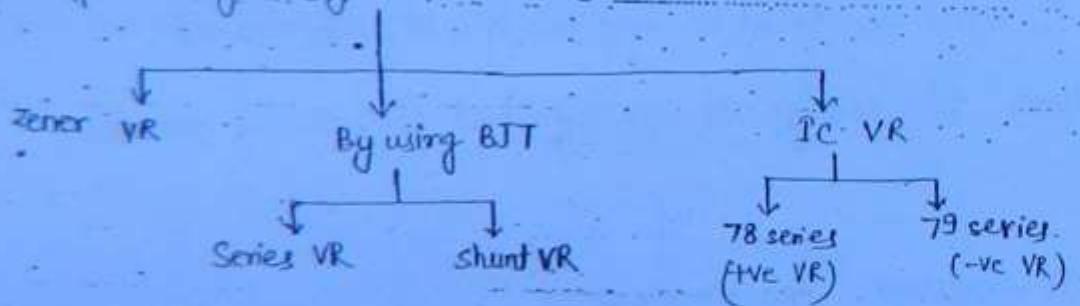
$R_L = 10\Omega \text{ to } 10K\Omega$



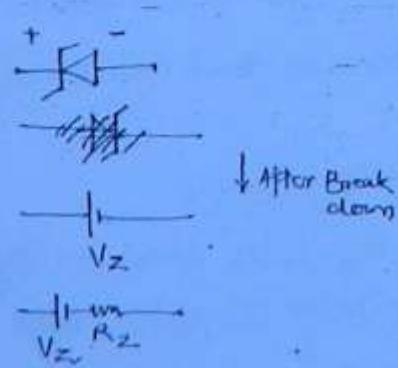
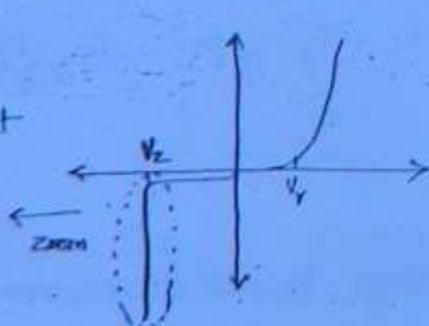
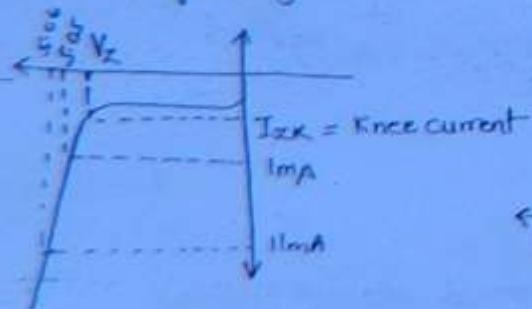
$$\% \text{ Regulation} = \frac{10-5}{5} \times 100 = 100\% \rightarrow \text{very poor.}$$

Note:- for better performance of ckt, % regulation should be as low as possible.

### Types of Voltage Regulator :-



### Zener Voltage Regulator :-



\*  $I_{ZK}$  = knee current or minimum current reqd. for zener diode to go in breakdown

$$\rightarrow P_{Zmin} = I_{ZK} \times V_Z$$

(14g)

$I_{Zmax}$  = maximum current across zener diode without damaging it.

$$\rightarrow P_{Zmax} = I_{Zmax} \times V_Z \Rightarrow I_{Zmax} = \frac{P_{Zmax}}{V_Z}$$

$V_Z$  is almost constant but not exactly constant.

From plot, Slope =  $\frac{1}{R_2} = \frac{11-1}{5.06 - 5.05} = 1000 \text{ mA/V} = 1 \text{ A/V}$

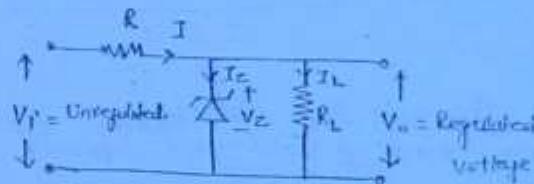
$$\Rightarrow R_2 = 1 \Omega$$

Hence, exact representation of zener diode BD is battery followed by  $R_Z$ .

### - Zener Voltage Regulator -

$V_i$  = unregulated voltage

$V_o$  = Regulated "



for voltage regulation,

Zener diode should be in BD for entire range of  $V_i$  ( $V_{min}$  to  $V_{max}$ ).

$$V_o = V_z$$

$$\therefore I = \frac{V_i - V_o}{R} \quad \text{and} \quad I = I_z + I_L$$

$$\therefore I_L = \frac{V_o}{R_L} = \frac{V_z}{R_L} \quad \text{Case If } R_L = \text{constant}, \text{ then } I_L = \text{constant}$$

Now,  $V_i \rightarrow$  varying then  $I \rightarrow$  varying &  $I_L = \text{constant}$

$\therefore I_z = \text{varying}$

Hence, for satisfactory performance of ckt

$$I \geq I_{ZK} + I_L$$

$\left\{ \begin{array}{l} \text{Range of } I_z, I_{ZK} \leq I_z \leq I_{Zmax} \\ \text{to be working in BD} \end{array} \right\}$

$$\therefore I_{min} = \frac{V_{min} - V_z}{R}, \quad I_{max} = \frac{V_{max} - V_z}{R}$$

$$\boxed{I_{min} \geq I_{ZK} + I_L} \quad \star$$

Case 2:  $V_i = \text{constant}$ ,  $R_L = \text{varying}$

$$\Rightarrow I = \text{constant} = \frac{V_i - V_z}{R}$$

$I_L = \text{variable}$

$$\rightarrow I = I_Z + I_L$$

$$\rightarrow I_{L\max} = \frac{V_i - V_z}{R_{\min}} \quad \text{if } R_L = R_{\min}$$

$$\Rightarrow I = I_{Z\min} + I_{L\max} \quad \& \quad I_{Z\min} \geq I_{ZK}$$

$$\therefore \text{When } I_{L\min} = \frac{V_i - V_z}{R_{\max}}$$

$$\Rightarrow I = I_{Z\max} + I_{L\min} \quad \& \quad \text{hence} \quad I_{Z\max} \leq \frac{P_{Z\max}}{R_Z}$$

$\rightarrow$  Combining both cases, the eqn for satisfactory operation of regulator circuit -

$$\frac{V_{i\min} - V_z}{R} \geq I_{ZK} + \frac{V_z}{R_{\min}} \quad **$$

$\rightarrow$  zener diode to be in BD.

$\rightarrow$  Power dissipation across zener diode  $\leq P_{\max}$ , hence the following condition should be satisfied -

$$\frac{V_{i\max} - V_z}{R} \leq I_{Z\max} + \frac{V_z}{R_{\max}} \quad **$$

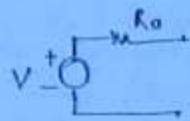
$\rightarrow$  ZD not to burn.

Workbook - Chap.1. Pg.29.

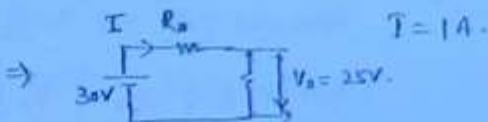
(15)

Ques.13 :-  $V_{NL} = 30V, V_{FL} = 25V$   $\eta \cdot \text{Regulation} = \frac{30-25}{25} \times 100\% = 20\%$ .

(a)



$$V = V_{NL} = 30V.$$

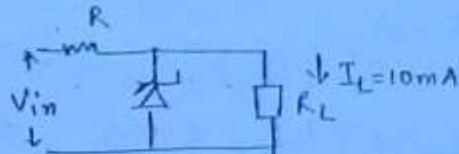


$$T = 1A.$$

$$\therefore R_{L\min} = \frac{25}{1} = 25\Omega, \text{ o/p resistance} \\ = R_o = \frac{5V}{1A} = 5\Omega.$$

Ques.15 :-

(a)



$$V_2 = V_o = 10mA.$$

$$V_{in} = 30 \text{ to } 50V. \text{ for satisfactory o/p} - I \geq I_{zu} + I_L$$

$$\Rightarrow \frac{V_{min} - V_2}{R} \geq (I_{zu} + I_L) \Rightarrow \frac{30 - 10}{R} \geq 11mA \\ \Rightarrow R \leq 1818\Omega$$

Ques.16 :-  $I_L \rightarrow 100 \text{ to } 500mA.$

(d)  $V_{in} = 12V$

$I_{ZK} \approx 0.$

$$\text{When } \frac{V_{in} - V_2}{R} \geq I_{zu} + I_{max}$$

$$\left\{ I_{max} = \frac{V_2}{R_{max}} \right.$$

$$\Rightarrow \frac{12 - 5}{R} = 0 + 500mA \Rightarrow R = 14\Omega.$$

Ques.17 :-  $V_i \rightarrow 20 \text{ to } 30V.$

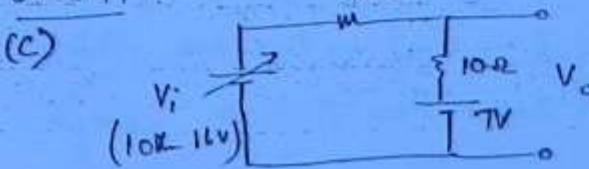
(c) load current max.  $\Rightarrow$  min zener current.

$$\frac{V_{in\min} - V_2}{R} \geq I_{ZK} + I_{max} \Rightarrow \frac{20 - 5.8}{1k\Omega} \geq 0.5mA + I_{max}$$

$$\therefore I_{max} \leq 14.2 - 0.5$$

$$\Rightarrow I_{max} \leq 13.7mA$$

Ques.19 :-



when  $V_i = 10V -$

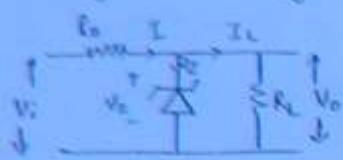
$$i = \frac{10 - 7}{210} = 1.43mA$$

$$V_o = 7 + \frac{1}{70} \times 10 = 7.14V$$

when  $V_i = 16V -$

$$i = \frac{16 - 7}{210} = 3.81mA \quad \therefore V_o = 7 + \frac{3}{70} \times 10 = 7.14V$$

- Line Regulation using Zener diode -



$V_i \rightarrow \text{varying}, R_L = \text{constant}$

$$I = \frac{V_i - V_z}{R} , \quad I = I_z + I_L \Rightarrow I_L = I - I_z$$

When  $V_i \uparrow, I \uparrow, V_z \uparrow (\text{slightly}), I_z \uparrow \uparrow, I_L \text{ remains constant}$

When  $V_i \downarrow, I \downarrow, V_z \downarrow (\text{slightly}), I_z \downarrow \downarrow, I_L \downarrow \downarrow$

(152)

- Load Regulation using Zener diode -

$V_i = \text{constant}, R_L = \text{varying}. \quad I \rightarrow \text{constant} \Rightarrow I_L = I - I_z.$

When  $R_L \uparrow, V_o \uparrow (\text{slightly}), V_z \uparrow (\text{slightly}), I_z \uparrow \uparrow, I_L \downarrow \downarrow$

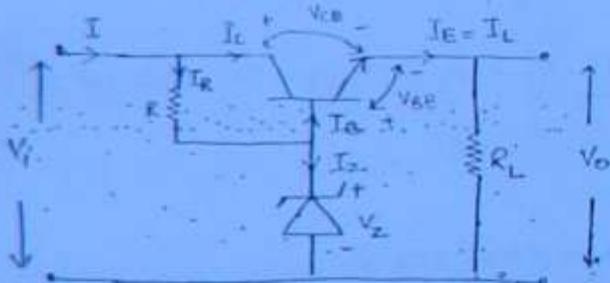
$$\therefore V_o = I_L R_L = \text{constant}$$

When  $R_L \downarrow, V_o \downarrow (\text{slightly}), V_z \downarrow (\text{slightly}), I_z \downarrow \downarrow, I_L \uparrow \uparrow$

$$\therefore V_o = I_L R_L = \text{constant}.$$

Voltage Regulation by using BJT :-

Series Voltage Regulator



→ BJT should be in active region & zener diode in breakdown region for full range of  $V_i$  from  $V_{\min}$  to  $V_{\max}$ .

from ekt -

$$\rightarrow V_o = V_z - V_{BE}. \quad (\Rightarrow \text{Regulated Voltage})$$

$$\rightarrow I_R = \frac{V_i - V_z}{R}$$

$$\rightarrow I_R = I_B + I_Z.$$

$$\rightarrow I_C = \beta I_B \quad \text{and} \quad I_L = I_E = I_C + I_B \Rightarrow I_E = I_L = (1 + \beta) I_B.$$

$$\rightarrow V_E = V_i - V_o = URV - RV$$

power dissipation :-

Across zener diode :-

Across BJT :-

$P_z = V_z \cdot I_z \leq P_{z\max}$
$P_T = I_C \cdot V_{CE}$

## Line Regulation -

$$V_i \rightarrow V_{\text{any}} \quad R_L = \text{constant} \quad I_B = I_R - I_Z, \quad (IS3)$$

$V_i \uparrow \Rightarrow T \uparrow, I_R \uparrow$  ( $I_C$  is not controlled by  $V_i$ , it is controlled by  $I_B$ ),  
then  $V_Z \uparrow$  (slightly)  $I_Z \uparrow \Rightarrow I_B = \text{constant} \Rightarrow I_E = \text{constant}$

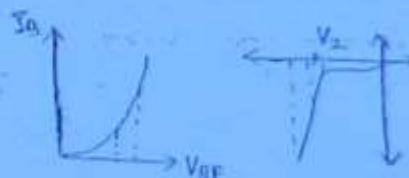
$I_E = I_L = \text{almost constant}$ , therefore  $V_o = \text{constant}$ .

## Load Regulation :

$$V_i = \text{constant}, R_L = \text{vary}. \quad V_o = V_Z - V_{BE}, \quad I_R = I_E + I_{B,b} = \text{constant}.$$

$R_L \uparrow, V_o \uparrow, \left( \begin{array}{l} V_Z \uparrow \quad I_Z \uparrow \\ V_{BE} \downarrow \quad I_{B,b} \end{array} \right), I_E \downarrow \left\{ \because I_{B,b} \right\}, I_L \downarrow$

$$\therefore V_o = I_L R_L = \text{constant}.$$



\* The circuit is in common collector configuration and hence this regulator is also called emitter follower VR.

Note:

\* Let  $I_Z$  variation is  $\Delta I_Z = 1 \text{ to } 11 \text{ mA} \Rightarrow \Delta I_Z = 10 \text{ mA}$ .

$$\left. \begin{array}{l} \Delta I_L = 10 \text{ mA} \\ \Delta R_L = \frac{V_2}{\Delta I_L} \end{array} \right\} \text{for zener diode ckt}$$

$$\left. \begin{array}{l} \Delta I_Z = 10 \text{ mA} = \Delta I_B \\ \Delta I_E = (\mu\beta) \Delta I_B = 1000 \text{ mA} \end{array} \right\} \text{for BJT ckt.}$$

for  $\beta = 99$

Hence, BJT ckt regulation can bear more variations in  $R_L$  as compared to zener ckt.

But, for  $V_i$  variation, same problem is present in both.

for BJT, As  $V_i \uparrow, T \uparrow, I_Z \uparrow$  hence for large  $V_i$  variation,  $I_Z$  will vary to  $I_{Z,\max}$  and  $P_Z$  will cross  $P_{Z,\max}$ .

## Shunt Regulator :-

$$\rightarrow V_o = V_z + V_{BE} \rightarrow RV$$

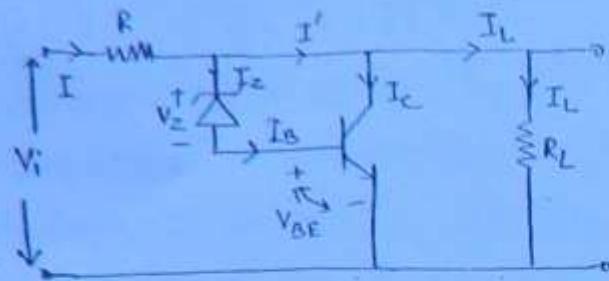
$$\rightarrow I = \frac{V_i - V_o}{R}$$

$$\left\{ \begin{array}{l} = \frac{URV - RV}{R} \\ \text{, limiting resistor} \end{array} \right.$$

$$\rightarrow P_T = V_{CE} \cdot I_C \quad \rightarrow I_B = I_Z$$

$$\rightarrow P_Z = V_Z \cdot I_Z \quad \rightarrow I_C = \beta I_B$$

$$\rightarrow I = I_Z + I_C + I_L \quad \text{--- (1)}$$



(154)

Tr  $\rightarrow$  Active  
Vz  $\rightarrow$  BD

\* Transistor is in common emitter configuration.

Line Regulation :-  $V_i = \text{vary}, R_L = \text{constant}$

When  $V_i \uparrow, I \uparrow, \left\{ \begin{array}{l} V_Z \uparrow \quad I_Z \uparrow \\ V_{BE} \uparrow \quad I_B \uparrow \end{array} \right\} \text{ due to } I_B \right\}, I_L \text{ (constant).}$

$\therefore \Delta I = 1000 \mu A, \text{ then } \Delta I_Z = \Delta I_C = 10 \mu A,$

$$\Delta I_C = \beta \Delta I_B = 990 \mu A$$

Hence the total change is distributed b/w  $I_Z$  &  $I_C$ .  $\left\{ \text{from (1)} \right\}$   
 $\& I_L = \text{constant.}$

Load Regulation :-  $V_i = \text{constant}, R_L = \text{vary}.$

$\rightarrow V_i = \text{constant} \Rightarrow I = \text{constant.}$

+ when  $R_L \uparrow, V_o \uparrow, \left\{ \begin{array}{l} V_Z \uparrow \quad I_Z \uparrow \\ V_{BE} \uparrow \quad I_B \uparrow \end{array} \right\}, I_C \uparrow, I_L \downarrow. \left\{ I = I_C + I_L + I_Z \text{ constant} \right\}$

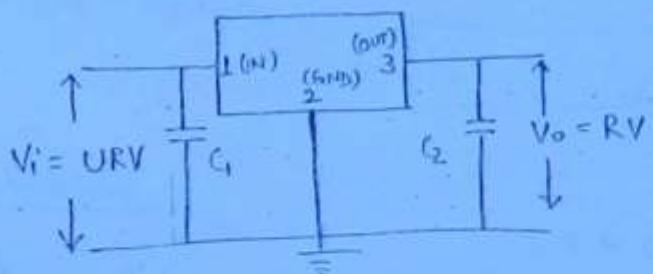
$$\therefore R_L \cdot I_L = V_o = \text{constant.}$$

$\rightarrow$  This circuit is suitable for high variation of  $R_L$  as well as  $V_i$ .

## Regulator:

(155)

Three terminal voltage regulator, IN, OUT and GROUND.



$C_1$  &  $C_2$  is connected to bypass high frequency noise.

78 series  
(+ve o/p voltage)

	<u><math>V_o</math></u>
7805	+5V
7810	+10V
7812	+12V
7815	+15V
7824	+24V

79 series  
(-ve output voltage)

	<u><math>V_o</math></u>
7905	-5V
7910	-10V
7912	-12V
7915	-15V
7924	-24V

## Low frequency Analysis of BJT :-

(156)

h-parameters :-

$$V_1 = h_{11}I_1 + h_{12}V_2$$

$$I_2 = h_{21}I_1 + h_{22}V_2$$

$$\rightarrow h_{11} = \left. \frac{V_1}{I_1} \right|_{V_2=0} = \text{i/p impedance when o/p is s.c.}$$

$$\rightarrow h_{12} = \left. \frac{V_1}{V_2} \right|_{I_1=0} = \text{Reverse voltage gain when i/p is o.c.}$$

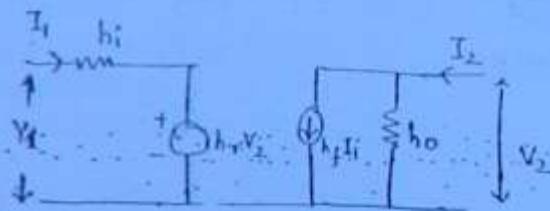
$$\rightarrow h_{21} = \left. \frac{I_2}{I_1} \right|_{V_2=0} = \text{forward current gain when o/p is s.c.}$$

$$\rightarrow h_{22} = \left. \frac{I_2}{V_2} \right|_{I_1=0} = \text{o/p admittance with i/p o.t.}$$

$h_{11} = h_i$	$h_{12} = h_r$
$h_{21} = h_f$	$h_{22} = h_o$

Hence,  $V_1 = h_i I_1 + h_r V_2$

$$I_2 = h_f I_1 + h_o V_2$$



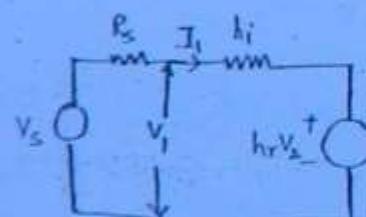
Derivation of  $A_I$ ,  $R_i$ ,  $A_V$ ,  $A_{VS}$ ,  $R_o$  :-

Current Gain  $A_I$

$$A_I = \frac{I_o}{I_1} = -\frac{I_2}{I_1}$$

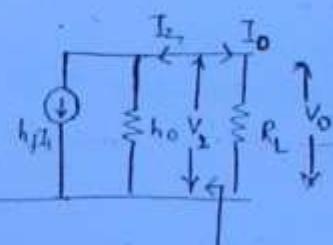
$$I_2 = h_f I_1 + h_o V_2 \quad \text{--- (1)}$$

$$V_2 = I_o R_L = -I_2 R_L \quad \text{--- (2)}$$



from (1) & (2) -

$$I_2 (1 + h_o R_L) = h_f I_1$$



$$V_o = I_o R_o$$

$$\Rightarrow A_I = \frac{-h_f}{1 + h_o R_L}$$

Input Resistance,  $R_i$  :-

$$\rightarrow R_i = \frac{V_1}{I_1}$$

(57)

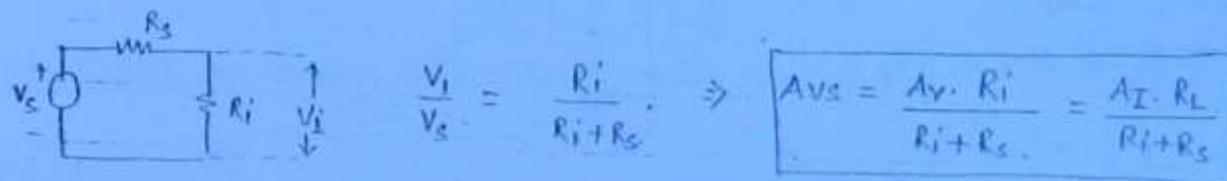
$$\begin{aligned} \rightarrow V_1 &= h_i I_1 + h_r V_2 \\ \rightarrow V_2 &= -I_2 R_L = A_I I_1 R_L \end{aligned} \quad \left\{ \begin{array}{l} V_1 = h_i I_1 + A_I I_1 R_L h_r \\ \Rightarrow R_i = h_i + h_r A_I \cdot R_L \end{array} \right.$$

Voltage Gain,  $A_v$  :-

$$A_v = \frac{V_2}{V_1} = \frac{-I_2 R_L}{I_1 R_i} \Rightarrow A_v = \frac{A_I \cdot R_L}{R_i} \quad \text{or} \quad A_v R_i = A_I \cdot R_L$$

Overall voltage Gain,  $A_{vS}$  :-

$$A_{vS} = \frac{V_o}{V_s} = \frac{V_2}{V_s} = \frac{V_2}{V_1} \times \frac{V_1}{V_s} = A_v \cdot \frac{V_1}{V_s}$$



\* If current source is present instead of  $V_s$  -

$$A_{IS} = \frac{I_2}{I_s} = -\frac{I_2}{I_s} = -\frac{I_2}{I_1 + I_s} = A_I \cdot \frac{I_1}{I_s}$$

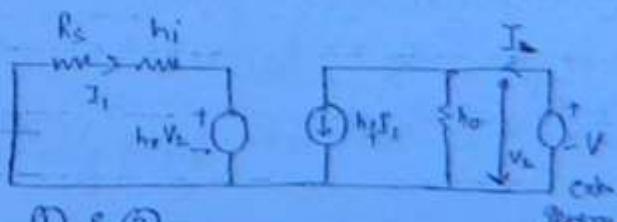
$$\frac{I_1}{I_s} = \frac{R_s}{R_s + R_i} \quad \therefore A_{IS} = A_I \cdot \frac{R_s}{R_s + R_i}$$

Output Resistance,  $R_o$  :-

$$\rightarrow I = h_f I_1 + h_o V \quad \text{--- (1)}$$

$$\rightarrow \text{KVL at } ip \rightarrow -h_r V_2 = I_1 \quad \text{--- (2)}$$

$$\rightarrow R_o' = R_o \parallel R_L$$



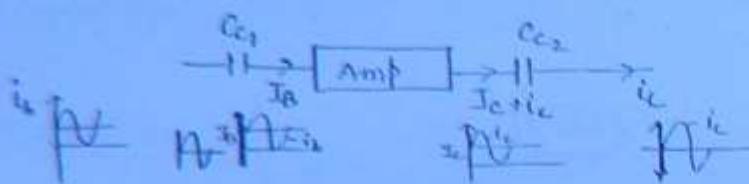
$$R_o = \frac{V}{I} = h_o - \frac{h_f \cdot h_r}{R_s + h_i} = \frac{1}{h_o}$$

05/09/2012

$$I_c + i_c = \beta I_B + \beta_{ab} i_b$$

$$|\Delta I| = \frac{i_c}{i_b}$$

|58



Tr in active region.

\* We can neglect DC sources in AC analysis as long as they are keeping ~~active~~ in

\* During AC analysis—

- All DC sources = 0, i.e., voltage source = S.C., current source = D.C.
- Coupling capacitors  $C_{c1}$  &  $C_{c2}$  ( $C_{b1}$  &  $C_{b2}$ ) & bypass capacitor acts as S.C.

$$\beta_{dc} = \frac{|I_C|}{|I_B|} = h_{FE}; \quad \beta_{ac} = \frac{|i_{cb}|}{|i_{ce}|} = h_{fe}$$

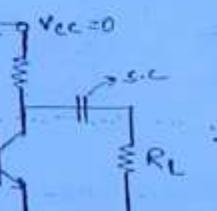
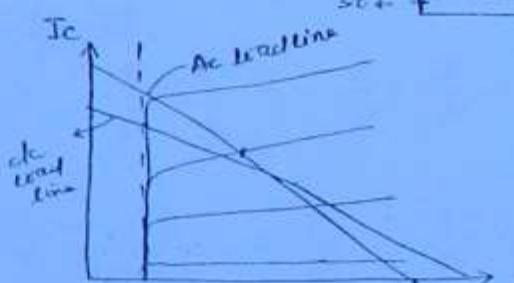
AC load line :-

\* Slope of dc load line =  $-1/R_C$

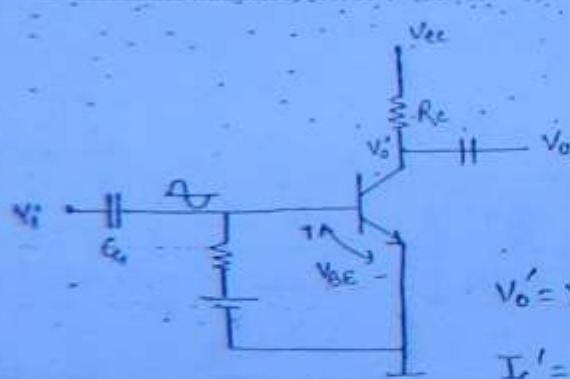
$$R_L' = R_C || R_L < R_C$$

Slope of ac load line =  $-1/R_L'$

$$-\frac{1}{R_L'} = -\left(\frac{1}{R_C} + \frac{1}{R_L}\right)$$

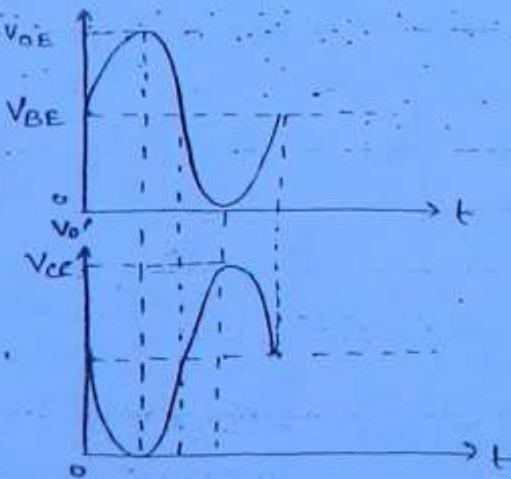


(For AC analysis)



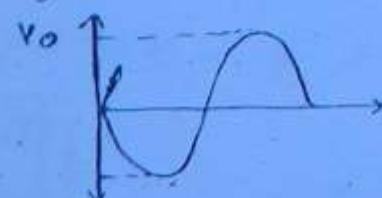
$$V'_o = V_{cc} - I'_c R_C$$

$$I'_c = I_c + i_c$$



for +ve half cycle,  $V_{cc} \uparrow, I_B \uparrow \Rightarrow I_c \uparrow \Rightarrow V_o' \downarrow$

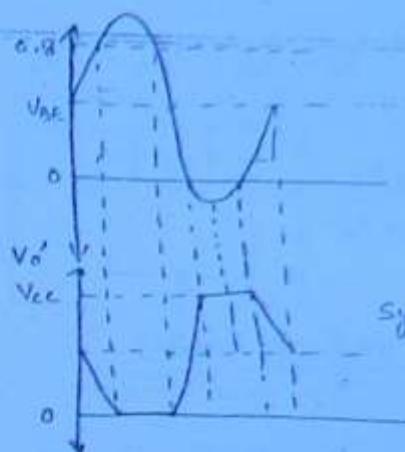
for -ve " ",  $V_{cc} \downarrow, I_B \downarrow \Rightarrow I_{cb} = 1 \Rightarrow V_o' \uparrow$ .



Symmetrical clipping :-

Ideally, voltage swing =  $V_{CC}$

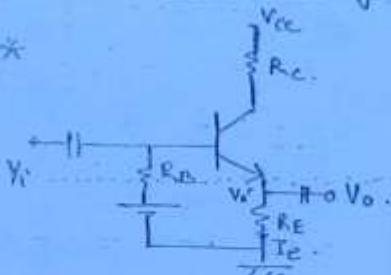
Practically, " =  $V_{CC} - V_{CESat}$



(59)

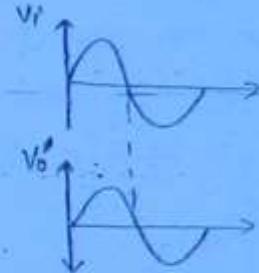
Symmetrical clipping.

Common Collector Config. :-



$$V_O = I_E \cdot R_C$$

$$\left\{ \begin{array}{l} V_{BE} \uparrow, I_B \uparrow, I_E \uparrow, \\ V_O \uparrow \end{array} \right.$$

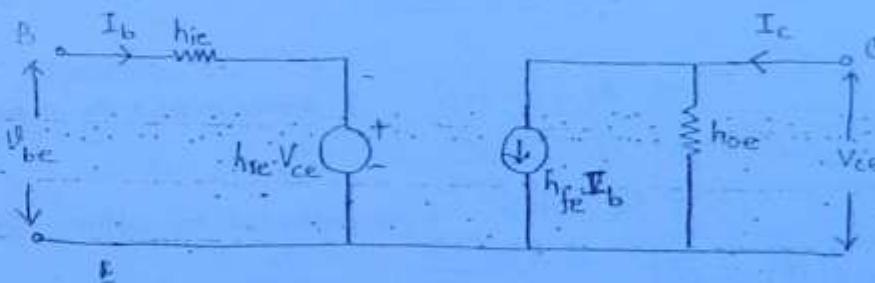


Hence, for cc configuration,

phase shift = 0°

Common collector

Hybrid Model for Common Emitter Configuration :-



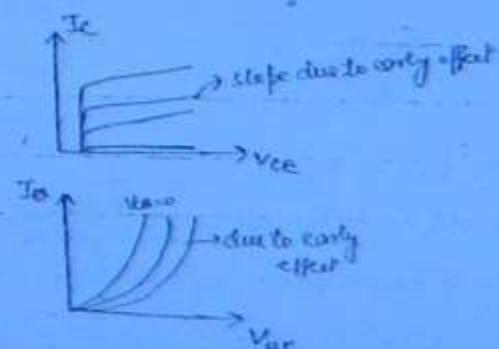
$$V_{BE} = h_{ie}I_b + h_{re}V_{CE} \quad \text{--- (1)}$$

↓ due to early effect.

$$I_C = h_{fe}I_b + h_{oe}V_{CE} \quad \text{--- (2)}$$

↓ due to early effect

$$h_{oe} = \frac{1}{r_o} \quad ; \quad r_o = \frac{V_A}{I_C} \quad ; \quad V_A = \text{early voltage.}$$



\* Typical values -  $h_{ie} = 11K\Omega$ ,  $h_{re} = 2.5 \times 10^{-4}$

$h_{fe} \approx 50$ ,  $h_{oe} = 1/70K$ .

$$\rightarrow A_I = -\frac{h_{fe}}{1+h_{oc}R_L}; \quad R_i = h_{ie} + h_{re} \cdot A_I \cdot R_L; \quad A_v = \frac{A_I R_L}{R_i}$$

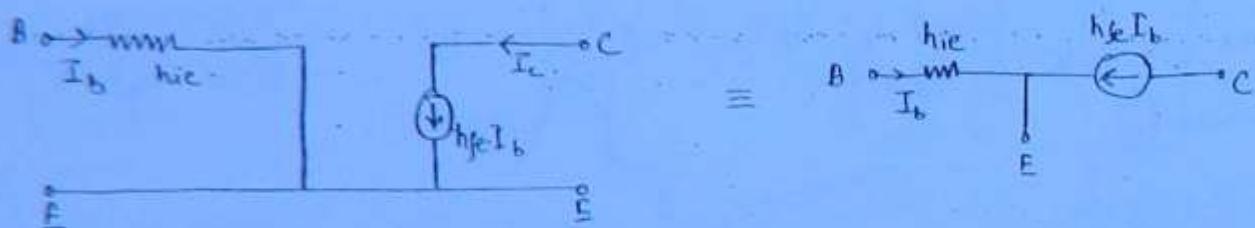
(169)

$$A_{vS} = \frac{A_v R_i}{R_S + R_i} = \frac{A_I R_L}{R_S + R_i}; \quad Y_0 = \frac{1}{R_0} = h_{oc} - \frac{h_{re} h_{fe}}{R_S + h_{ie}}$$

### Simplified/ Approximate Hybrid Model -

→ If  $h_{oc}R_L \leq 0.1$ , then error in approx calculation  $\leq 10\%$ , therefore we can use approximate model, ie. we can neglect early effect.

$$h_{oc} = 0, \quad h_{ie} = 0 \quad (\Rightarrow \text{admittance} = 0 \Rightarrow \text{resistance} = \infty \Rightarrow \text{open})$$



→ It is valid for CE, CC & CB configuration and for n-p-n as well as p-n-p Tr.

#### \* Exact

$$\text{for } h_{oc} = 0.1$$

$$\rightarrow A_I = -\frac{h_{fe}}{1.1}$$

$$\rightarrow R_i = h_{ie} + \underbrace{h_{re} A_I R_L}_{-ve}$$

$$\rightarrow A_v = \frac{A_I R_L}{R_i}$$

$$\rightarrow Y_0 = h_{oc} - \frac{h_{re} h_{fe}}{R_S + h_{ie}} \approx \frac{1}{40k}$$

$$\Rightarrow R_0 = 40k$$

#### Approximate

$$\rightarrow A_I = -h_{fe} \rightarrow \text{overestimated by approx. } 10\%$$

$$\rightarrow R_i = h_{ie} \rightarrow \text{overestimated by approx. } 5\%$$

$$\rightarrow A_v \text{ is overestimated by } 5\%.$$

$$\rightarrow R_0 = 0 \Rightarrow$$

$$\rightarrow R_0 = \infty \rightarrow \text{overestimated (but not large)}$$

### Miller's Theorem:

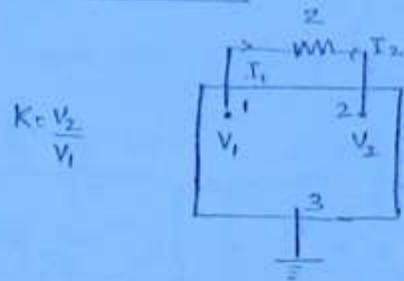


Fig. 1.

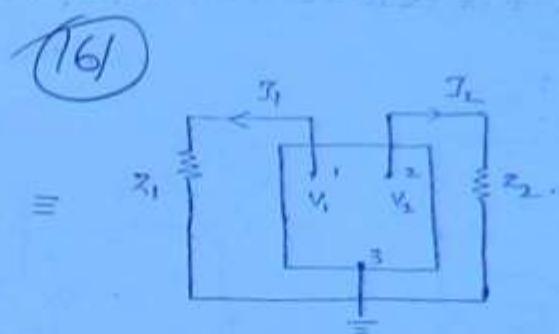


Fig. 2.

From fig. 1 —

$$I_1 = \frac{V_1 - V_2}{Z} = \frac{V_1}{Z_1} \quad (\text{from fig. 2})$$

$$\Rightarrow Z_1 = \frac{V_1 Z}{V_1 - V_2} \Rightarrow Z_1 = \frac{Z}{\frac{V_2}{V_1} - 1} \Rightarrow Z_1 = \frac{Z}{1 - K}$$

Similarly,

$$I_2 = \frac{V_2 - V_1}{Z} = \frac{V_2}{Z_2}$$

$$\Rightarrow Z_2 = \frac{KZ}{K-1}$$

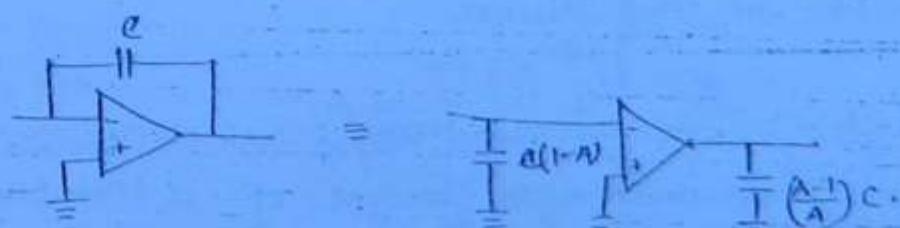
When  $Z = \text{capacitor}$  —

$$Z_1 = \frac{Z}{1-K} \Rightarrow \frac{1}{\omega C_1} = \frac{1/\omega C}{1-K} \Rightarrow C_1 = (1-K)C$$

$$Z_2 = \frac{KZ}{K-1} \Rightarrow \frac{1}{\omega C_2} = \frac{K(1/\omega C)}{K-1} \Rightarrow C_2 = \left(\frac{K-1}{K}\right)C$$

Workbook

Chap 10 Q.23

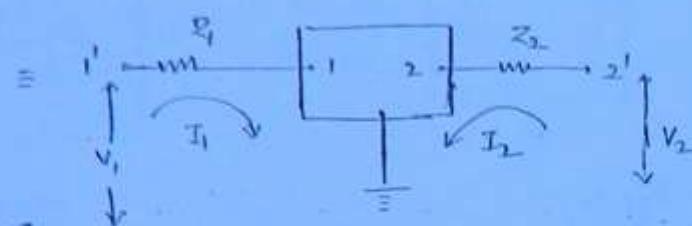
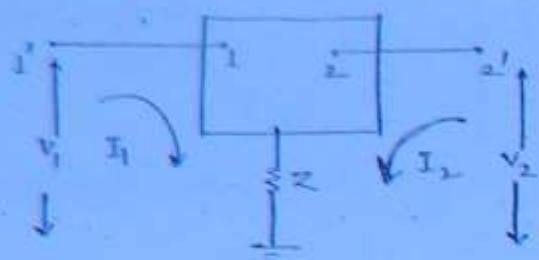


∴ Hence i/p & o/p capacitances increases and impedance will decrease.  
 {parallel cap.}

2 Due to this capacitance, i/p path will be short (low impedance) & i/p to o/p  
 o/p amp will be low  $\frac{1}{A}$ . Gain will be  $\frac{1}{A}$ .

## Dual of Miller's Theorem

(162)



$$A_{\Sigma} = -\frac{I_2}{I_1}$$

from fig ① & ② -

$$\begin{aligned} V_1 &= (I_1 + I_2)z = \bar{I}_1 z \\ \Rightarrow z_1 &= \left[ 1 + \frac{I_2}{I_1} \right] z \Rightarrow z_1 = (1 - A_1)z \end{aligned}$$

Similarly,  $V_2 = (I_1 + I_2)z = I_2 z_2$

$$\Rightarrow z_2 = \left( 1 - \frac{1}{A_1} \right) z \Rightarrow z_2 = \left( \frac{A_1 - 1}{A_1} \right) z$$

## Advantage of h parameters

- 1) They are real nos at low frequency.
- 2) They are graphically obtained from i/p & o/p characteristics of transistor.

## Disadvantages

- 1) All four h-parameters are temp. sensitive.

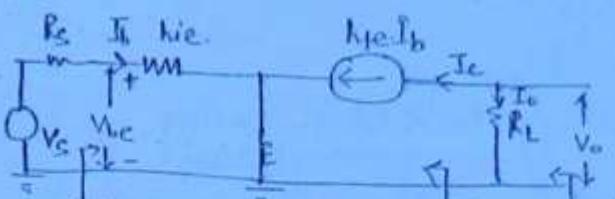
## Application

- 1) They are obtained only for small signal analysis of a transistor amplifier.

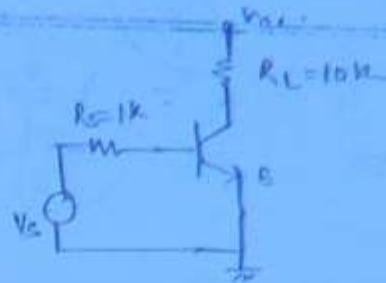
Ques If  $R_L = 10K\Omega$ ,  $R_S = 1K\Omega$ . find the various gains & i/p & o/p impedances.

$$h_{ie} = 1K\Omega, h_{fe} = 50, h_{re} = h_{oe} = 0,$$

Sol<sup>n</sup> Since  $h_{re} = h_{re} = 0$  then we can use simplified model.



(163)



$$\text{Current gain } A_I = \frac{I_o}{I_b} = -\frac{I_c}{I_b} = -\frac{h_{fe} I_b}{I_b}$$

$$\Rightarrow A_I = -h_{fe} = 50$$

O/p Resistances:

$$R_i = \frac{V_{be}}{I_b} = h_{ie} = 1.1 K\Omega$$

Internal voltage gain

$$A_V = \frac{V_o}{V_{be}} = -\frac{h_{fe} \cdot R_L \cdot I_b}{V_{be}} = -\frac{h_{fe} \cdot R_L}{R_{in}}$$

$$\therefore A_V = -454$$

Voltage gain

$$A_{VS} = \frac{V_o}{V_S} \quad A_{VS} = \frac{V_o}{V_S} = \frac{V_o}{V_{be}} \times \frac{V_{be}}{V_S}$$

$$\therefore A_{VS} = A_V \cdot \frac{R_i}{R_i + R_S} \Rightarrow A_{VS} = -2.37$$

O/p resistances

$$R_o' = R_o || R_L = \infty || R_L = R_L = 10k$$

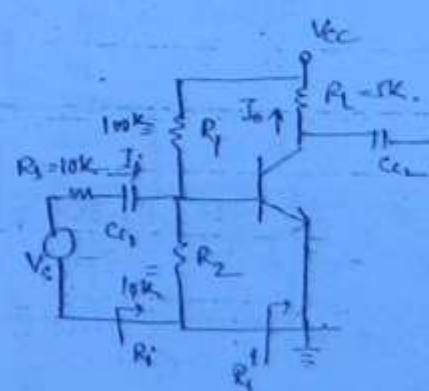
Ques: Given  $h_{fe} = 50$

$$h_{ie} = 1.1k$$

$$h_{re} = h_{oe} = 0$$

Sol<sup>n</sup>

—mn

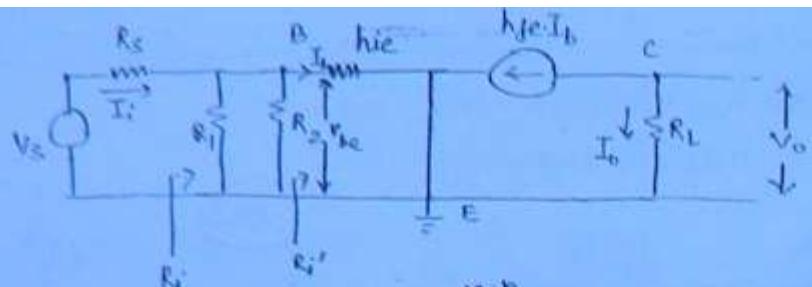


find  
 $A_I = I_o / I_b$   
 $R_i, R_o', A_V, A_{VS}$

$$\rightarrow A_I' = \frac{I_0}{I_b} = -h_{FE} \cdot \frac{I_b}{I_i}$$

$$A_I' = -h_{FE} = -50$$

(164)

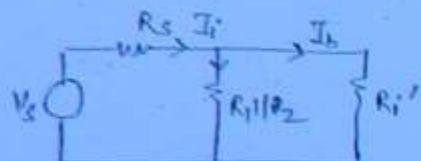


$$\rightarrow R_i' = \frac{V_{be}}{I_b} = h_{ie} = 11k\Omega$$

$$\rightarrow R_i = R_1 \parallel R_2 \parallel R_3 = (10k) \parallel (10k) \parallel (1.1)k = 980\Omega$$

$R_i' > R_i \rightarrow$  Biasing problem  
 $R_1 \& R_2$  is reducing i/p resistance

$$\rightarrow A_I = \frac{I_0}{I_i}$$



$$= \frac{-h_{FE} I_b}{R_1 \parallel R_2} \times \frac{I_b}{I_i}$$

$$I_b = \frac{R_1 \parallel R_2 \cdot I_i}{R_i' + (R_1 \parallel R_2)} \Rightarrow \frac{I_b}{I_i} = \frac{9.09}{1.1 + 9.09}$$

$$\therefore A_I = -50 \times \frac{9.09}{10.19} \approx -45$$

$$\rightarrow A_V = \frac{V_o}{V_{be}} = -\frac{I_b \cdot R_L}{I_b \cdot R_i'} = -\frac{I_b \cdot h_{FE} \cdot R_L}{I_b \cdot r_i'} = -227.3$$

$$\rightarrow A_{VS} = \frac{V_o}{V_S} = \frac{V_o}{V_{be}} \times \frac{V_{be}}{V_S} = A_V \cdot \frac{V_{be}}{V_S} = A_V \cdot \frac{R_i}{R_i + R_S} = -20.5$$

very small clug  
 to low i/p resistance

$$\text{Given: } h_{ie} = 11k\Omega$$

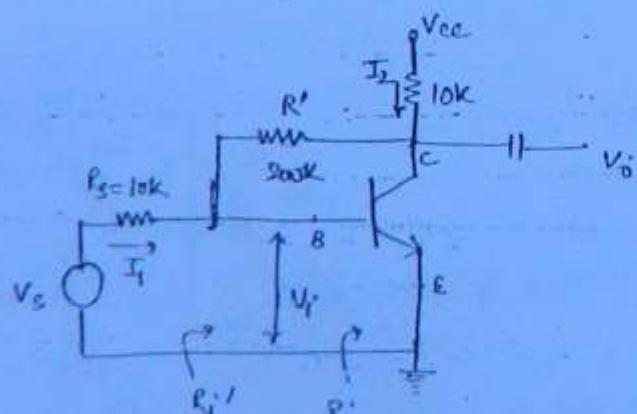
$$h_{FE} = 50$$

$$h_{re} = h_{ce} = 0$$

Calculate

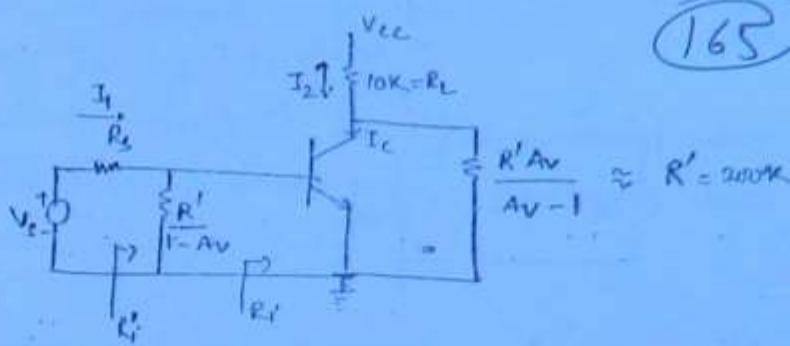
$$R_i, R_i', A_I$$

$$A_I' = \frac{-I_2}{I_1}, A_V, A_S$$



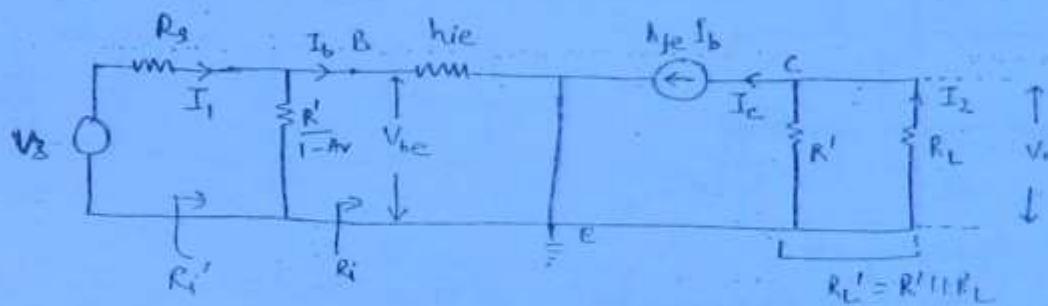
Soln  $|Av| \gg 1$  for CE configuration.

Apply Miller's theorem -



(165)

Using approximate model -



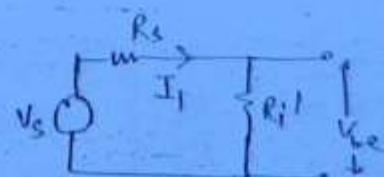
$$Av = \frac{V_o}{V_{be}} = \frac{-V_o R_L}{I_b h_{ie}} = \frac{(R' I_b)}{(R' + R)} \frac{h_{fe}}{h_{ie}} = \frac{(-h_{fe} I_b) \cdot R_L'}{I_b \cdot h_{ie}} = -\frac{h_{fe} R_L'}{h_{ie}} = -433$$

$$\Delta I = -\frac{I_c}{I_b} = -h_{fe} = -50$$

$$R_i' = \frac{V_{be}}{I_b} = h_{ie} = 11k$$

$$\therefore \frac{R'}{1-A_v} = \frac{200}{1-(-433)} = 0.46k\Omega$$

$$\left. \begin{array}{l} R_i' = R_i \parallel \left( \frac{R'}{1-A_v} \right) = 11 \parallel 0.46 = 0.30k\Omega \end{array} \right\}$$



$$Av_S = \frac{V_o}{V_s} = \frac{V_o}{V_{be}} \times \frac{V_{be}}{V_s} = -433 \times \frac{R_L'}{R_s + R_L'}$$

$$\rightarrow \Delta I' = -\frac{I_2}{I_1} = \frac{V_o / R_L}{R_s / R_s + R_i'}$$

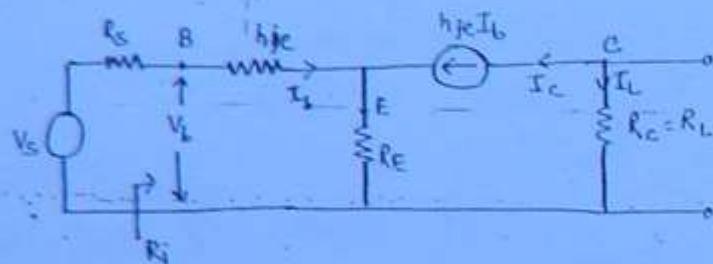
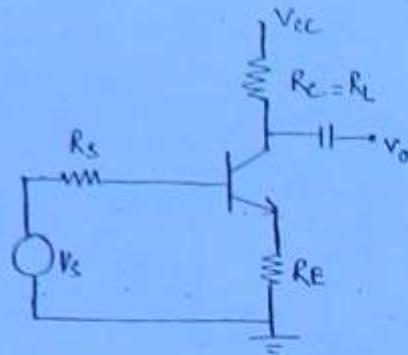
Ans = -12.6 due.

$$\therefore \Delta I' = \frac{V_o}{V_s} \left( \frac{R_s + R_i'}{R_L} \right) = (-12.6) \left( \frac{10 + 0.3}{10} \right) = -12.99 \text{ due}$$

## Common Emitter with unbypassed emitter resistor, $R_E$ -

(166)

→ If  $h_{fe}(R_E + R_L) \leq 0.1$  then we can use approximate model.



$$\rightarrow A_I = -\frac{h_{fe}I_b}{I_b} = -h_{fe} \Rightarrow \text{it will remain unaffected.}$$

$$\rightarrow \text{if resistance } R_i = \frac{V_b}{I_b} \quad \text{Applying KVL -}$$

$$V_b = I_b \cdot h_{ie} + R_E (1+h_{fe}) I_b$$

$$\therefore \boxed{R_i = h_{ie} + R_E (1+h_{fe})} \Rightarrow R_i \text{ increases}$$

$$\rightarrow A_V = \frac{V_o}{V_b} = \frac{-h_{fe} I_b \cdot R_L}{I_b [h_{ie} + R_E (1+h_{fe})]} \Rightarrow \boxed{A_V = \frac{-h_{fe} R_L}{h_{ie} + (1+h_{fe}) R_E}} \Rightarrow A_V \downarrow \text{due to -ve feedback}$$

$$\text{If } (1+h_{fe}) R_E \gg h_{ie} \text{ & } h_{fe} \gg 1, \text{ then } \boxed{A_V = -\frac{R_L}{R_E} = -\frac{R_L}{R_E} \quad (\text{approx.})}$$

Due to -ve feedback, gain is highly stable as it is independent of  $T_n$  parameters (which in turn depends on temp.).

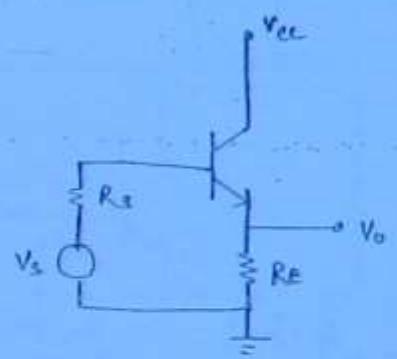
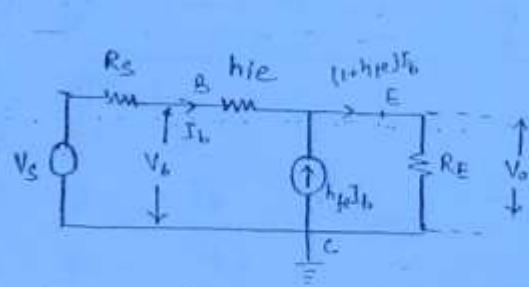
$$\rightarrow A_{Vs} = A_V \cdot \frac{R_i}{(R_i + R_S)} ; \text{ if } R_i \gg R_S \text{ then } \boxed{A_{Vs} \approx A_V \approx -\frac{R_L}{R_E}} \quad (\text{approx.})$$

## Effect of using $R_E$ -

- Current gain will remain unaffected.
- O/p resistance  $\text{f}_{\text{e}} \text{ by } (1+h_{\text{fe}})R_E$
- Voltage gain is stabilized, i.e.,  $A_V$  is independent of any Tr. parameters.
- O/p resistance  $\text{f}_{\text{e}}$ . (current series feedback, check dual of Miller effect).

(167)

## Common Collector or Emitter Follower -



$$\rightarrow A_I = \frac{V_o}{I_b} = \frac{(1+h_{\text{fe}})I_b}{I_b} \Rightarrow [1+h_{\text{fe}} = A_I] \quad , \phi = 0^\circ$$

$$\rightarrow \text{O/p resistance} \quad R_i = \frac{V_b}{I_b} \quad \text{By applying KVL}$$

$$V_b = h_{ie}I_b + (1+h_{\text{fe}})I_b \cdot R_E$$

$$\Rightarrow R_i = h_{ie} + (1+h_{\text{fe}}) \cdot R_E \quad (\text{high due to } R_E)$$

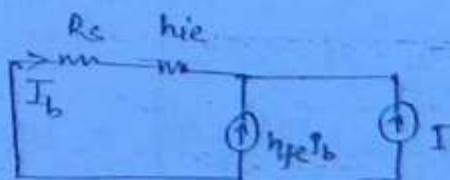
$$\rightarrow \text{Voltage Gain} \quad \therefore A_V = \frac{V_o}{V_b} = \frac{(1+h_{\text{fe}}) R_E \cdot I_b}{R_i I_b} \Rightarrow A_V = \frac{(1+h_{\text{fe}}) R_E}{h_{ie} + (1+h_{\text{fe}}) R_E} \quad (< 1)$$

$$\text{If } (1+h_{\text{fe}}) R_E \gg h_{ie}, \quad [A_V = 1]$$

$$\rightarrow \text{O/p resistance} : R_o -$$

$$V = (R_s + h_{ie})(I + h_{\text{fe}}I_b)$$

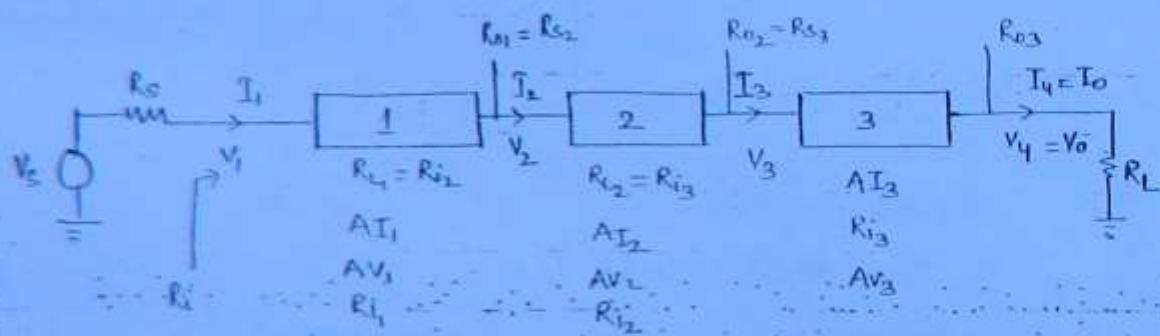
$$I_b + h_{\text{fe}}I_b + I = 0 \\ \Rightarrow (1+h_{\text{fe}})I_b = -I$$



$$\therefore V = (R_s + h_{ie}) \left( I - \frac{I h_{\text{fe}}}{1+h_{\text{fe}}} \right) \\ \Rightarrow R_o = \frac{(R_s + h_{ie})}{(1+h_{\text{fe}})}$$

	CE	CE with RE	CC
$A_I$	$-h_{fe}$	$-h_{fe}$	$(1+h_{fe})$
$\Delta R_i$	$h_{ie}$	$h_{ie} + (1+h_{fe})R_E$	$h_{ie} + (1+h_{fe})R_E$
$A_V$	$\frac{A_I \cdot R_L}{R_i}$	$\frac{-h_{fe} \cdot R_C}{h_{ie} + (1+h_{fe})R_E}$	$\frac{(1+h_{fe})R_E}{h_{ie} + (1+h_{fe})R_E}$
$R_o$	$\infty$	$\infty$	$\frac{h_{ie} + R_S}{1 + h_{fe}}$
$R'$	$R_O    R_L = R_L$	$R_O    R_L = R_L$	$R_O    R_L$

### Cascaded Amplifier :-



$$\rightarrow A_V = \frac{V_0}{V_1} = \frac{V_0}{V_3} \times \frac{V_3}{V_2} \times \frac{V_2}{V_1} = A_{V_3} \cdot A_{V_2} \cdot A_{V_1}$$

$$\rightarrow 20 \log A_V = 20 \log A_{V_1} + 20 \log A_{V_2} + 20 \log A_{V_3}$$

$$\rightarrow A_I = \frac{I_0}{I_1} = \frac{I_0}{I_3} \times \frac{I_3}{I_2} \times \frac{I_2}{I_1} = A_{I_3} \cdot A_{I_2} \cdot A_{I_1}$$

$$\rightarrow 20 \log A_I = 20 \log A_{I_3} + 20 \log A_{I_2} + 20 \log A_{I_1}$$

$$\rightarrow A_p = A_V \cdot A_I$$

\* 
$$= A_{V_1} \times \left( \frac{R_1}{R_1 + R_0} \right) \times A_{V_2} \times \left( \frac{R_2}{R_2 + R_0} \right)$$

(If there is no proper impedance matching)

- \* If source is voltage source, then input stage should be common emf current " " " common base
- \* If is delivering voltage, then last stage should be common collector current, " " " base.
- \* All the intermediate stages should be Common emitter config.
- \* CC is used in first & last stage due to high  $R_i$  & low  $R_o$  respect.
- \* CB " " " " " now  $R_i$  & high  $R_o$  " .

### Darlington Pair (cc-cc)

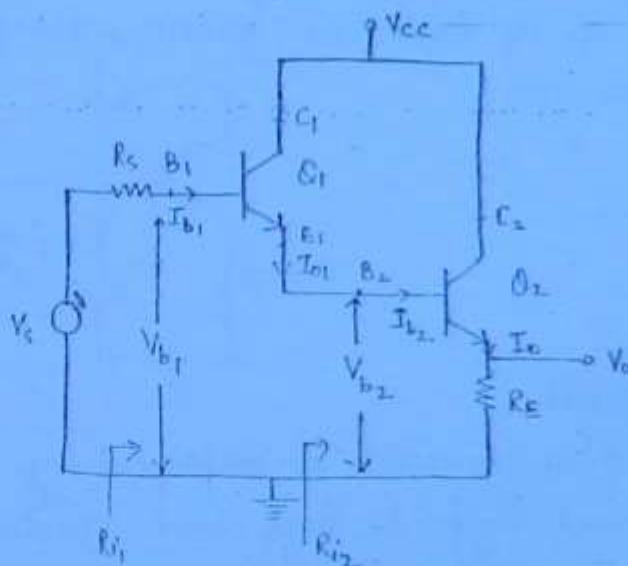
2<sup>nd</sup> stage - cc

$$A_{I_2} = \frac{I_o}{I_{b_2}} = (1+h_{fe})$$

$$R_{i_2} = \frac{V_{b_2}}{I_{b_2}} = h_{ie} + (1+h_{fe})R_E$$

$$R_{i_2} \approx (1+h_{fe}) R_E$$

$$A_{V_2} = \frac{A_{I_2} \cdot R_L}{R_{i_2}} \Rightarrow A_{V_2} \ll 1$$



1<sup>st</sup> stage - cc

$$\rightarrow R_{L1} = R_{i_2} = (1+h_{fe}) R_E$$

$$\rightarrow A_{I_1} = \frac{I_{o1}}{I_{b_1}} = (1+h_{fe})$$

$$\rightarrow R_{i_1} = \frac{V_{b1}}{I_{b_1}} = h_{ie} + (1+h_{fe}) R_{L1}$$

$$\Rightarrow R_{i_1} = h_{ie} + (1+h_{fe})^2 R_E$$

$$\Rightarrow [R_{i_1} \approx (1+h_{fe})^2 \cdot R_E] \rightarrow \text{Very large.}$$

$$\rightarrow A_{V_1} \leq 1$$

Overall current gain

$$A_I = \frac{I_o}{I_{b_1}} = \frac{I_o}{I_{b_2}} \times \frac{I_{b_2}}{I_{b_1}}$$

$$A_I = A_{I_1} \times A_{I_2}$$

$$\Rightarrow [A_I = (1+h_{fe})^2]$$

$$\rightarrow A_V = A_{V_1} \cdot A_{V_2} \leq 1$$

For  $n$ -stages in cascade:-

Assuming  $h_{ie} = h_{re} = h_{oe} = 0$ ,

$$R_i = (1 + h_{fe})^n \cdot R_E$$

$$A_I = (1 + h_{fe})^n$$

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Advantage:-

- Very high current gain. Darlington integrated transistor pairs are commercially available with  $h_{fe}$  as high as 30,000, therefore this is also called super p transistor.
- Very large i/p resistance.

Disadvantage:-

- Highly expensive circuit.
- leakage current of first transistor is amplified by second, hence the overall leakage current may be high and darlington connection of 3 or more transistors is usually impractical.

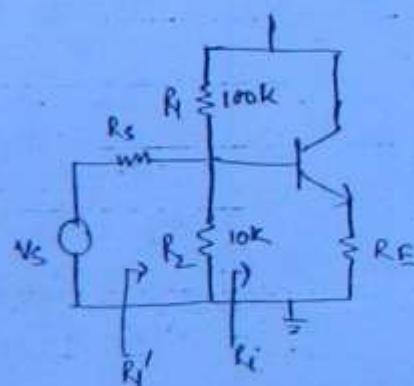
Biasing Problem :-

for CC -

$$R_i = h_{ie} + (1 + h_{fe}) R_E$$

$$\text{for } h_{ie} = 1\text{k}, h_{fe} = 99, R_E = 2\text{k}$$

$$\therefore R_i \approx 200\text{k}$$



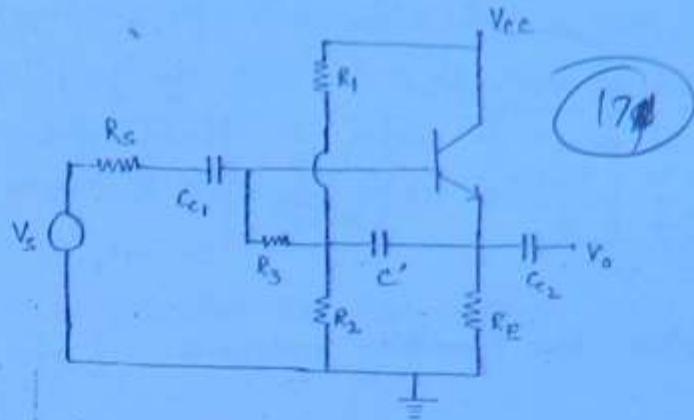
Now  ~~$R_i'$~~   $R_i' = R_1 \| R_2 \| R_E$  and Resultant  $R_i' < 10\text{k}$ . But we need  $R_i'$  to be high so that whole Vs is transferred to i/p

→ Even if a darlington pair is attached with  $R_i = 2.5\text{M}\Omega$ , then also  $R_i' < 10\text{k}$ . This is called biasing problem.

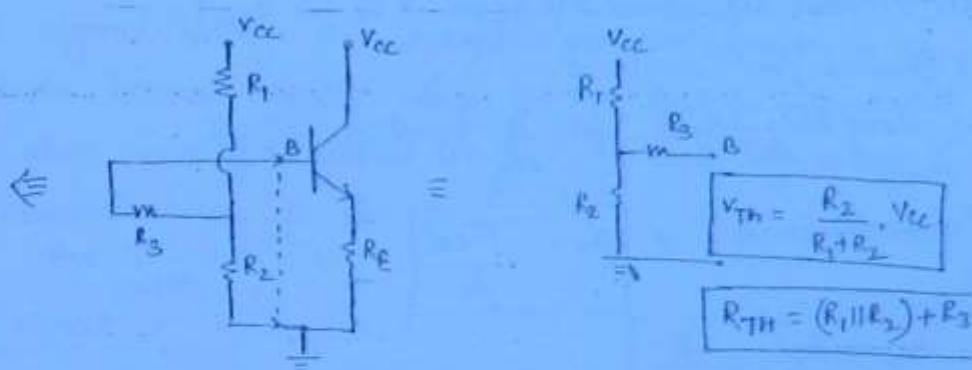
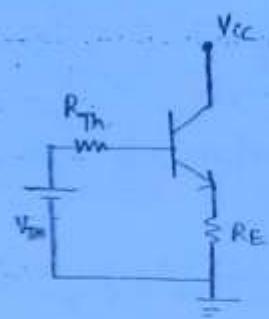
## Boot Strapping :-

→ value of  $C'$  should be very high so that it acts as SC for AC.

→ for dc analysis,  $C_{C_1}, C_{C_2}$  &  $C'$  will act as O.C.



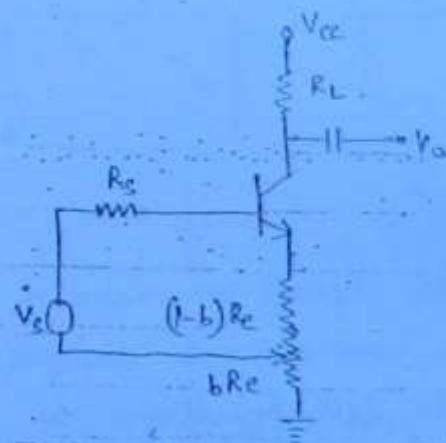
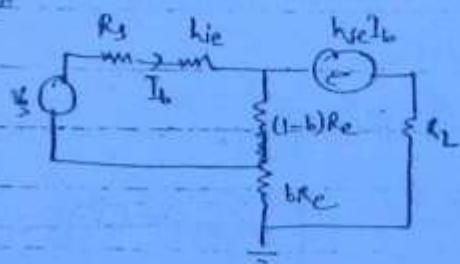
## DC analysis :



Now calculate  $A_{VS} = \frac{V_o}{V_s}$

$$R_i = \frac{V_s}{I_b}$$

Now



$$V_s - I_b(R_s + h_{ie}) - (1-b)R_E(1+h_{fe})I_b = 0$$

$$\therefore \frac{V_s}{I_b} = (R_s + h_{ie}) + (1-b)(1+h_{fe})R_E = R_i$$

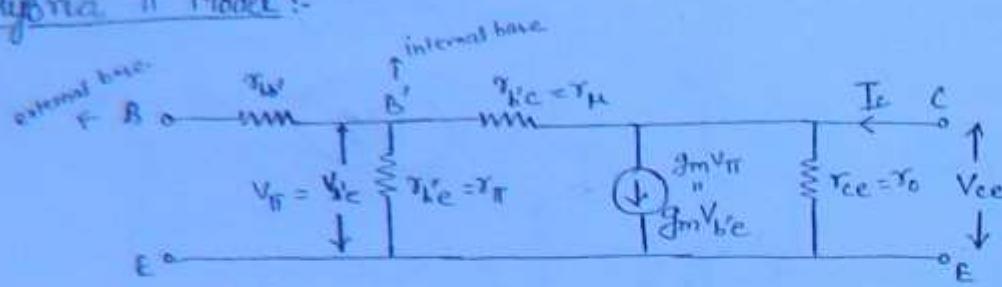
$$V_o = -h_{fe}I_b \cdot R_L$$

if  $b \gg 1$ , then  $R_i \approx R_E$

$$A_{VS} \approx \frac{V_o}{V_s} = \frac{V_o}{I_b} \times \frac{I_b}{V_s}$$

$$\Rightarrow A_{VS} = \frac{-h_{fe}R_L}{(R_s + h_{ie}) + (1-b)(1+h_{fe})R_E}$$

gain less.

Hybrid TI Model :-

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→  $r_{bb'}$  or  $r_b$  = ohmic base spreading resistance (small A,  $R \uparrow$  for base).

→  $r_{ce} = r_o$  → early effect.

→  $g_m V_{BE}$  → shows dependence of  $I_c$  on  $V_B$  (or  $V_{BE}$ ).

→  $r_{be}$  → forward junction resistance.

→  $r_{bc}$  → shows early effect. for  $J_c$  junction  $\rightarrow$  high.

$$\rightarrow g_m = \text{Transconductance} \Rightarrow \boxed{g_m = \frac{I_{C\text{dc}}}{V_T}} ; \quad V_T = \frac{T}{11600} \text{ volt}$$

$$\rightarrow r_{be} = r_{pi} = \frac{h_{fe}}{g_m} = \frac{\beta}{g_m}$$

$$\rightarrow I_c = g_m V_{BE} + \frac{V_{CE}}{r_o}$$

$\uparrow r_o$   
early effect.

→  $g_m$  and  $r_{pi}$  in model depends on value of dc quiescent current  $I_{C\text{dc}}$ . and hence provide more accurate analysis of transistor.

→ Model is applicable to both pnp & npn tn w/o change of polarities.

→  $r_b$  is represented as a vccs.

→  $r_{bb'} \div$  Base region of Tr is very thin compared to emitter & collector region & its resistance lies  $10\Omega$  to  $400\Omega$ . The ohmic resistance of E & C is usually of order of  $10\Omega$  and can be neglected in comparison to that of base-region.

- $r_{TF}$  → Incremental resistance of E-B diode which is FB in active region.
- $r_p$  → It accounts for feedback from o/p to i/p due to base width modulation or early effect. The value of  $r_p$  is usually very high (several M $\Omega$ ) and will be neglected in analysis.

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- $r_0$  → o/p resistance and is also due to Early effect.

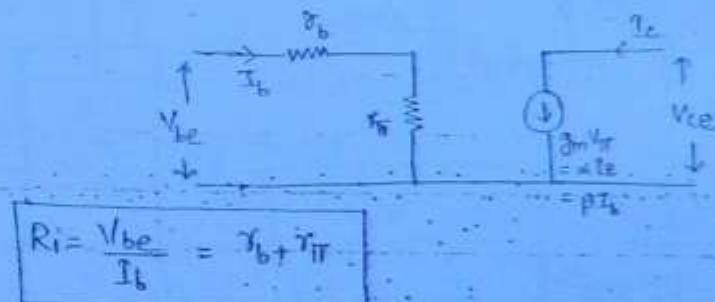
$$r_0 = \frac{V_A}{|I_{col}|}$$

- $g_m V_T$  → any small signal voltage  $V_T$  at emitter junction results in a signal collector current  $g_m V_T$  when  $V_{CE} = 0$ . BJT is represented as a VCCS when controlled current is  $g_m V_T$  & controlling voltage is  $V_T$ .  $g_m$  represents transconductance of Tr.

### Simplified / Approximate Model :-

$$g_m = \frac{|I_{col}|}{V_T}, \quad r_{TF} = \frac{g_m}{\beta}$$

$$r_{TF} = \frac{h_{FE}}{g_m} = \frac{\beta}{g_m}$$

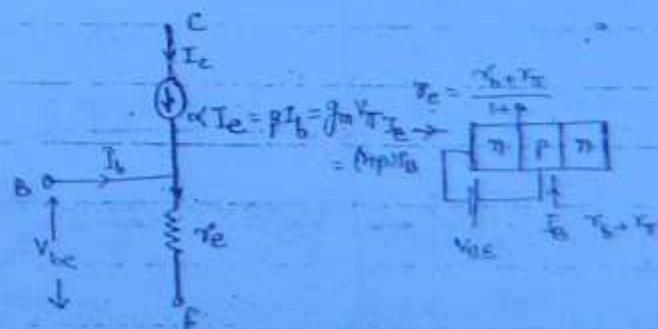


$$R_i = \frac{V_{be}}{I_b} = r_b + r_{TF}$$

### $r_e$ or T-Model :-

$$r_e = \frac{V_T}{|I_{col}|}$$

$$\frac{V_{be}}{I_b} = R_i = (1+\beta)r_e$$



$$* \quad r_b + r_{TF} = (1+\beta)r_e = h_{ie} \quad ; \quad g_m V_T = \beta I_b = \alpha I_e$$

$$r_b \ll r_{TF}, \quad \beta \gg 1 \quad ; \quad r_{TF} = \beta r_e = h_{ie}$$

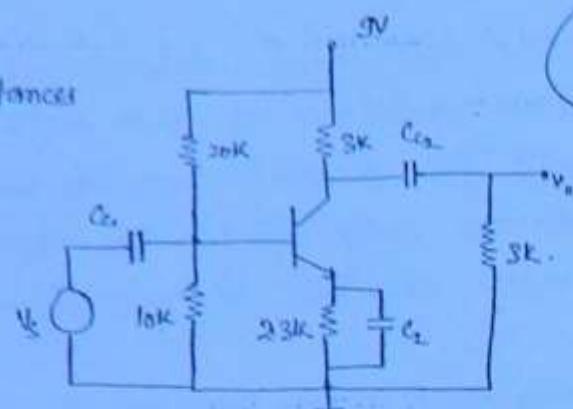
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Given:  $V_{BE} = 0.7V$ ,  $\beta \approx \infty$  & all capacitances are  $\propto$  voltage.

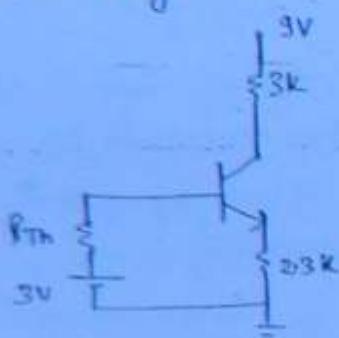
$$V_E = 25mV$$

$\rightarrow$  Find the biasing current  $I_B$

$\rightarrow$  Find midband voltage gain.



Soln METHOD 1  
DC analysis -



$$I_B \approx 0$$

$$3 - 0.7 = 2.3k I_B$$

$$\Rightarrow I_B \approx 1mA$$

$$\therefore V_E = \frac{25mV}{1mA} = 25\Omega$$

AC analysis -

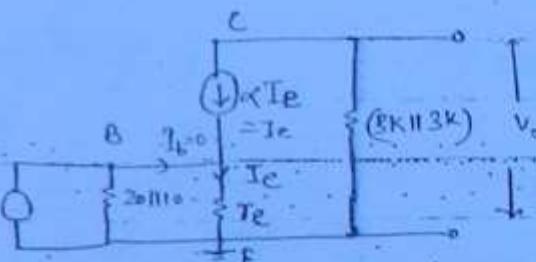
$C_1, C_2, C_3$  will act as short.

$$I_b \approx 0, \alpha = 1 \quad \left\{ \begin{array}{l} \text{if } \beta = \text{very large} \\ \text{if } \beta = \text{large} \end{array} \right\}$$

$$V_S = I_B r_e$$

$$V_O = (-1.5K) \alpha I_E$$

$$\therefore \frac{V_O}{V_S} = -\frac{-1.5 \times 10^3}{25} = -60$$

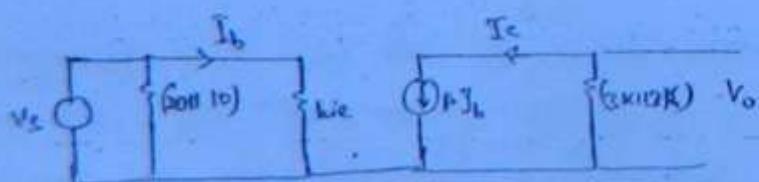


$$r_e = \frac{V_T}{I_E} = \frac{V_T}{|I_E|} \quad \left\{ \begin{array}{l} \text{if } \beta = \text{large} \\ \text{if } \beta = \text{very large} \end{array} \right\}$$

$$\therefore g_m = 1/25$$

$$\frac{V_O}{V_S} = \frac{g_m r_h (1.5K)}{r_b + r_e + r_c} = \frac{-\beta I_b (1.5K)}{I_b (\beta + 1)} = -60$$

METHOD 2



$$\frac{V_O}{V_S} = \frac{-\beta I_b \cdot (1.5K)}{I_b \cdot h_{ie}} = \frac{-1.5K}{r_e} = -60$$

$$h_{ie} + h_{re} = h_{ie} = (1 + \beta) r_e$$

$$h_{ie} = \beta r_e$$

$$\beta h_{ie} = r_e$$

Note

\* If  $R_E$  is unbypassed -  $\left[ \frac{V_o}{V_s} = -\frac{R_E}{R_E + r_E} \approx -\frac{R_E}{r_E} \right]^{**}$   $\rightarrow$  (overestimated)

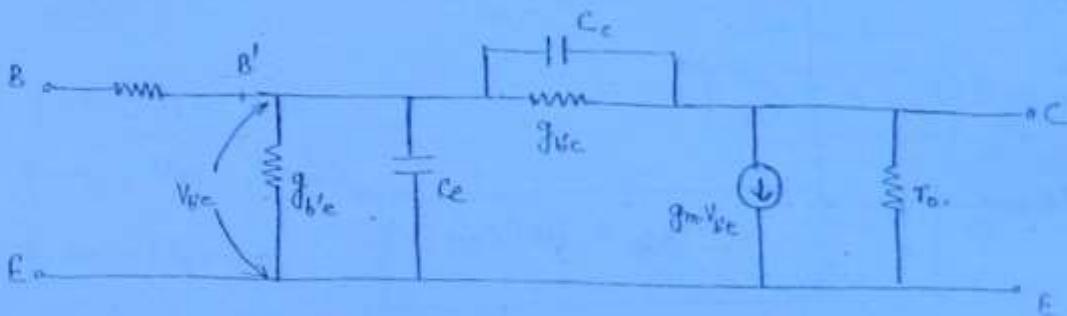
\* If  $R_E$  is bypassed -  $\left[ \frac{V_o}{V_s} = -\frac{R_E}{r_E} \right]^{**}$

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\* If an extra  $R_L$  is present in o/p -

$$\left. \begin{aligned} R_E \text{ unbypassed} &= \frac{-(R_C \parallel R_L)}{r_E} \\ R_E \text{ bypassed} &= \frac{-(R_C \parallel R_L)}{r_E} \end{aligned} \right\}^{**}$$

### High Frequency Analysis of BJT :-



Giacoletto Model

$$\rightarrow j_{bc} = \frac{1}{r_{bc}} = \frac{j_m}{h_{fe}} \quad \left. \begin{array}{l} \dots \\ \dots \end{array} \right\} \because r_{bc} \gg r_{be}$$

$$\rightarrow j_{ec} = \frac{1}{r_{bc}} \quad \left. \begin{array}{l} \dots \\ \dots \end{array} \right\} \Rightarrow j_{bc} \ll j_{ec}$$

$$\rightarrow g_m = \frac{|j_e|}{V_T}$$

$$\rightarrow C_{eff} = C_e = C_D \rightarrow \text{Diffusion capacitance}$$

$$\rightarrow C_e = C_l = C_{ob} = C_{\mu} \rightarrow \text{Promotion capacitance}$$

Typical Values

$$\rightarrow j_m = 50 \text{ mA/V}$$

$$\rightarrow r_b = r_{bb'} = 100 \Omega$$

$$\rightarrow r_{bc} = r_{ff} = 1K$$

$$\rightarrow r_{be} = 4 \text{ M}\Omega$$

$$\rightarrow r_o = r_{ee} = 80K$$

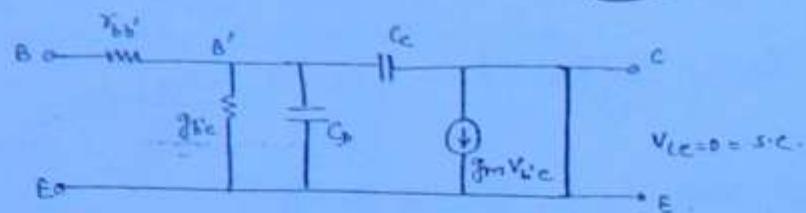
$$\rightarrow C_e \approx 5 \text{ pF}$$

$$\rightarrow C_b = C_e = 100 \text{ pF}$$

## CE short circuit current gain -

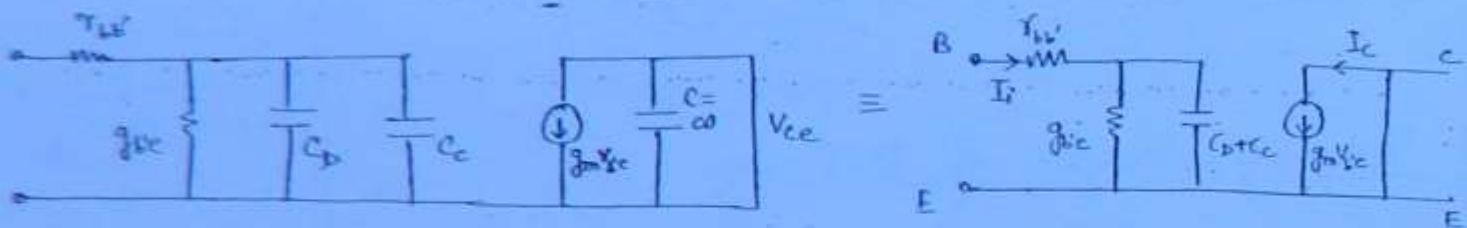
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$$\rightarrow A_V = \frac{V_{CE}}{V_{BE}} = 0.$$



Now applying Miller's theorem -

$$C_1 = C_C (1 - A_V) = C_C ; \quad C_2 = \frac{C_C (A_V - 1)}{A_V} = \infty \text{ (short).}$$



i/p capacitance:  $C_i = C_D + C_C$

i/p conductance:  $\gamma_i = \frac{I_i}{V_{BE}} = g_{BE} + j\omega(C_D + C_C)$

Current gain:

$$A_I = \frac{I_o}{I_i} = \frac{-g_m V_{BE}}{V_{CE} \cdot \gamma_i} \Rightarrow A_I = -\frac{g_m}{\gamma_i}$$

$$\Rightarrow A_I = \frac{-g_m}{g_{BE} + j\omega(C_D + C_C)} \rightarrow \text{This will act as LPF at higher frequency.}$$

Rearranging,  $A_I = \frac{-g_m / g_{BE}}{1 + j\omega \frac{(C_D + C_C)}{g_{BE}}} , \text{ Now } \because \gamma_{BE} = \frac{1}{g_{BE}} = \frac{h_{FE}}{g_m} \Rightarrow g_m / g_{BE} = h_{FE}.$

$$\Rightarrow A_I = \frac{-h_{FE}}{1 + j\omega \frac{(C_D + C_C)}{g_{BE}}} \quad \star$$

$$\rightarrow |A_1| = \frac{h_{fe}}{\sqrt{1 + \left[ \frac{w(c_b + c_c)}{g_{be}} \right]^2}}, \quad |A_1|_{max} = h_{fe} \text{ at } w=0. \quad (177)$$

$$\rightarrow \text{At } w=w_p, |A_1| = |A_1|_{max}/\sqrt{2} \Rightarrow \frac{h_{fe}}{\sqrt{2}} = \frac{h_{fe}}{\sqrt{1 + \left[ \frac{w_p(c_b + c_c)}{g_{be}} \right]^2}}$$

On solving, -

$$w_p = 2\pi f_p = \frac{g_{be}}{c_b + c_c}$$

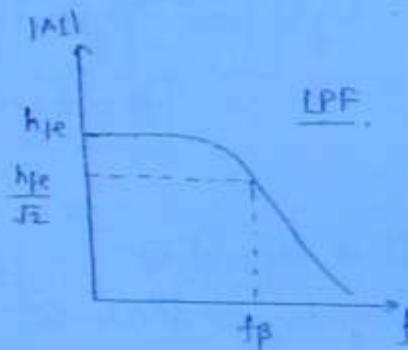
or

$$f_p = \frac{1}{2\pi g_{be} (c_b + c_c)}$$

$f_p = 3\text{dB}$   
cutoff freq.

Hence,

$$|A_1| = \frac{-h_{fe}}{1 + j(w/w_p)} = \frac{-h_{fe}}{1 + j(f/f_p)}$$



$$\rightarrow \text{At } f=0, |A_1| = h_{fe}$$

$$\text{At } f=f_p, |A_1| = h_{fe}/\sqrt{2}$$

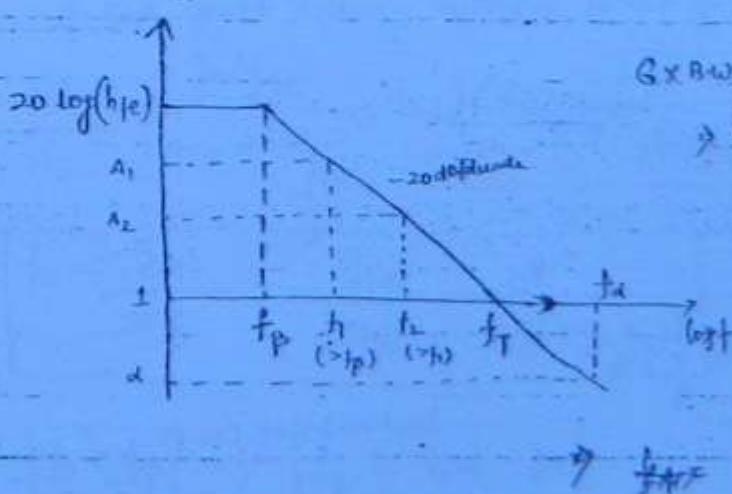
$$\text{At } f=\infty, |A_1| = 0$$

$\rightarrow$  At  $f=f_T$ ,  $|A_1|=1$ .  $\rightarrow$  frequency till transistor will act as amplifier.

$$1 = \frac{h_{fe}}{\sqrt{1 + (f_T/f_p)^2}} \Rightarrow \left(\frac{f_T}{f_p}\right)^2 = h_{fe}^2 - 1 \Rightarrow \left(\frac{f_T}{f_p}\right)^2 = h_{fe}^2$$

$$\Rightarrow f_T = h_{fe} \cdot f_p \quad \Rightarrow f_T \gg f_p.$$

Bode Plot :-



$$G \times \text{BW} = \text{constant}$$

$$\Rightarrow h_{fe} \cdot f_p = A_1 \cdot f_1 = A_2 \cdot f_2 = f_T = \text{a.f.c.}$$

$f_T = \text{unity gain bandwidth product}$

$$f_T = h_{fe} f_p = \frac{h_{fe} \cdot g_{be}}{2\pi (c_b + c_c)}$$



$$V_B = \frac{R_E}{R_E + R_B} \times 22V = 6.67V \quad (\approx 10V)$$

Hence zener is not  
on BD.

R<sub>in</sub>, I<sub>Z</sub> = 0, R<sub>L</sub> = 0, V<sub>O</sub> = 6.67V ( $\neq 10V$ )

$$f_T = \frac{\beta m}{2\pi(C_B + C_E)}$$

$\approx 10^3$

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$f_P \ll f_T < f_A$ ;  $f_A$  = frequency at which gain  $< 1/\mu$ , gain of CB = 1

$$\mu_{FE} \cdot f_P = \frac{h_{FE}}{1+h_{FE}} \cdot f_A \Rightarrow f_A = (1+h_{FE}) f_P$$

\* &  $f_T = h_{FE} f_P$

$f_P$   $\Rightarrow$  'P' cutoff frequency and also called as bandwidth of CE at high freq. Typical value -

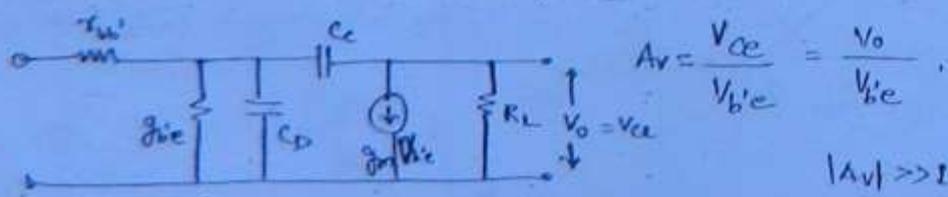
$$f_P = 1.6 \text{ MHz.}$$

$f_T \Rightarrow$  It is defined as

- 1) frequency at which SC CE-gain attains unit magnitude.
- 2) Highest freq. upto which CE tr. will be working as an amplifier.
- 3) freq. where CE tr.  $\beta$  reduces to unity.
- 4) Unity gain bandwidth product of CE tr. and thus ( $G \times B_W$ ) is limited by junction capacitance.

$f_A \Rightarrow$  'A' cutoff frequency. It is also called BW of CB transistor at high frequency. The BW of CB is always greater than BW of CE or BW of CC. tr.

Common Emitter with Resistive load R<sub>L</sub>:



$$A_V = \frac{V_{CE}}{V_{B'E}} = \frac{V_O}{V_{B'E}}$$

$|A_V| \gg 1$  for CE Tr.

Apply Miller's theorem -

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$$C_2 = C_C (1 - A_V)$$

$$C_2 = \frac{C_C (\lambda V - 1)}{\lambda V} \approx C_C \quad \left\{ \text{if } \lambda V \gg 1 \right\}$$

$$Z_{C_1} = \frac{1}{2\pi f C_1} \quad ; \quad C_1 \equiv \text{pf} \quad \text{if } f \equiv \text{MHz} \Rightarrow Z_C \approx 10^6 \Omega \Rightarrow Z_C \parallel R_L = R_L$$

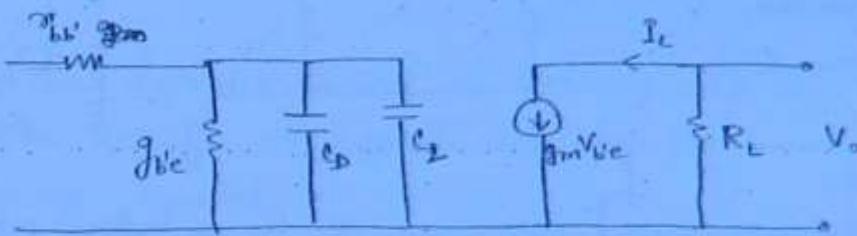
Therefore, approximate model -

Now,

$$A_V = -\frac{g_m V_{BE} \cdot R_L}{V_{BE}}$$

$$\Rightarrow A_V = -g_m R_L$$

$$C_2 = C_C (1 + g_m R_L)$$



$$\rightarrow \text{Input capacitance} : \quad C_i = C_B + C_C (1 + g_m R_L)$$

$$\rightarrow \text{Input conductance} : \quad G_i = g_{BE} + j\omega C_i$$

$\rightarrow$  Due to Miller's effect,  $C_i \uparrow$ ,  $G_i \downarrow$ ,  $Z_i \downarrow \Rightarrow$  Gain  $\downarrow$ .

$$\rightarrow I_o = -g_m V_{BE} \quad ; \quad A_I = \frac{I_o}{I_i} = -\frac{g_m}{Y_i} \Rightarrow A_I = \frac{-g_m}{g_{BE} + j\omega C_i}$$

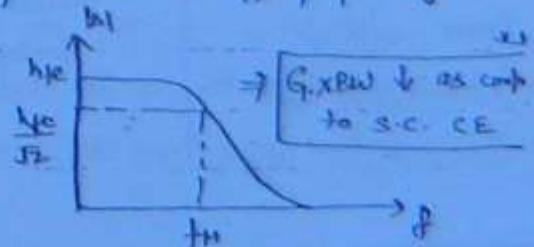
$$\therefore A_I = \frac{-g_m / g_{BE}}{1 + j\omega C_i / g_{BE}} \Rightarrow A_I = \frac{-h_{FE}}{1 + j(\omega / \omega_H)} = \frac{-h_{FE}}{1 + j(f_H / f_H)}$$

$$\rightarrow f_H = \frac{g_{BE}}{2\pi C_i} = \frac{g_{BE}}{2\pi (C_B + C_C (1 + g_m R_L))}$$

;  $f_H = 3\text{dB}$  cut-off frequency.

$$\rightarrow f_H < f_B \quad ; \quad f_B = \lim_{R_L \rightarrow 0} f_H$$

$$\Rightarrow G_B \omega_R < G_B \omega_{SC}$$



## Multistage Amplifiers :-

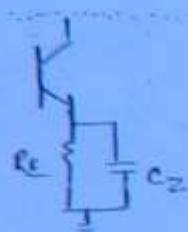
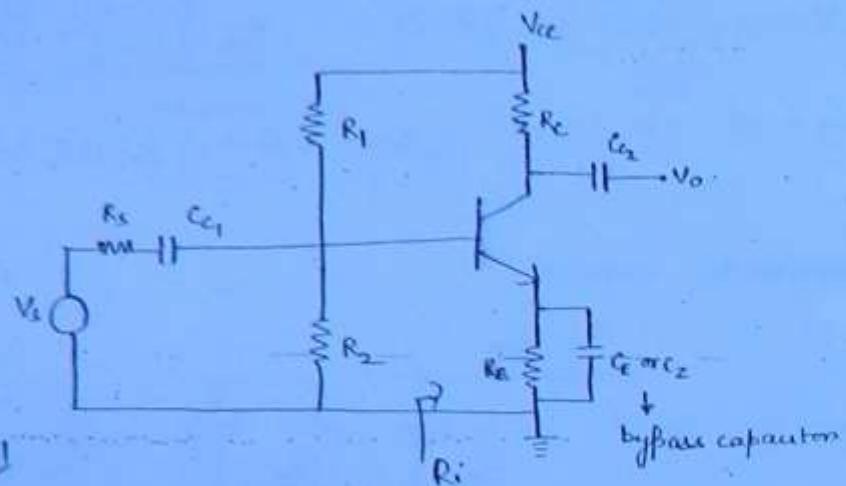
(180)

### RC Coupled Amplifier :-

i) Single stage -

→ Audit freq. amplifier  
(20Hz - 20KHz).

→ CE configuration, i.e.,  
180° phase shift



$$R_i = h_{ie} + (1+\beta)R_E$$

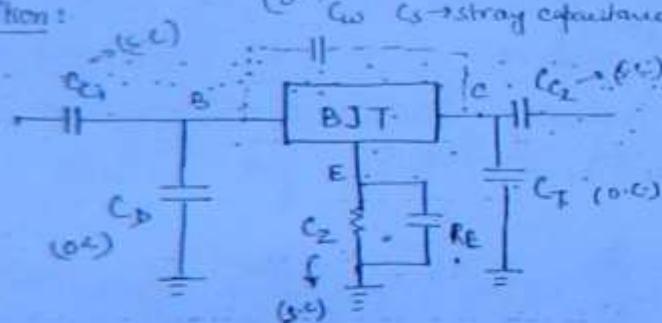
$$R_o = h_{ce} + k_T$$

$R_i \uparrow, A_v \downarrow$

$A_v \uparrow, R_i \downarrow$

\*  $C_z$  is bypass capacitor & its value should be high so that it will act as short for AC.

Ideal condition:



$C_{c1}, C_{c2}, C_z \rightarrow$  very high

$C_D, C_T \rightarrow pF$

$C_S, C_W \rightarrow 10^{-14} F$

$$Z_C = \frac{1}{2\pi f C}$$

### Low Frequency

→ As  $f \downarrow, Z_C \downarrow$ .

All capacitive impedances  $\rightarrow \infty$ , and they will act as D.C. Gain will  $\downarrow$  due to  $C_{c1}, C_{c2}$  &  $C_z$ .

### Mid frequency

→  $Z_C$  is not decided by  $f$ , decided by the value of  $C$ . Hence, ideal condn achieved. and gain is independent of freq.

### High frequency

→  $A_B - f \uparrow, Z_C \uparrow$ ,

∴ All capacitive impedances  $\rightarrow 0$  and they will act as short. Gain will  $\downarrow$  due to  $C_D, C_T, C_S, C_W$ .

→ frequency Response Curve :-

Important Point -

→ It is audio freq. amplifier.

→ Single stage RC couple introduce a phase shift of  $180^\circ$  & two stages introduce  $360^\circ$  or  $0^\circ$ .

→ Coupling capacitors ( $C_4$  &  $C_2$ ) are also called dc blocking capacitor ( $C_b$ ,  $C_{b1}$  &  $C_{b2}$ ) and are used to couple ac signals and simultaneously block dc current or biasing current.

→ By using emitter resistor, w/o a bypass capacitor, there will be a -ve feedback across  $R_E$  and this reduces the voltage gain and ip resistance of amplifier.

→ Bypass capacitor ( $C_2$  or  $C_B$ ) is used to bypass ac signal current through it. For dc current or biasing current,  $C_2$  is open.  
By using  $C_2$ , -ve feedback (due to ac signal) across  $R_E$  is eliminated, therefore voltage gain  $\uparrow$  and ip resistance  $\downarrow$ .

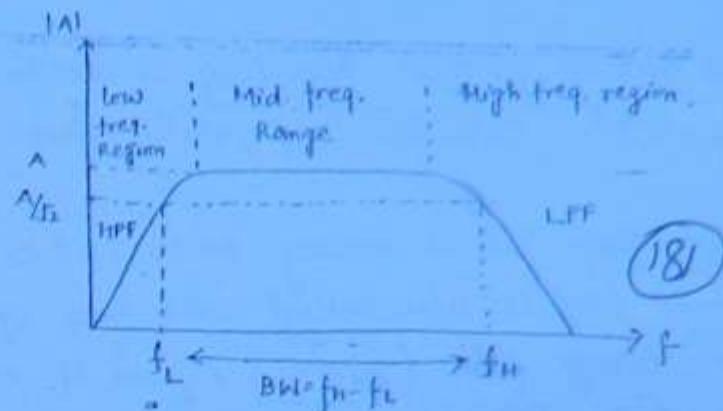
→ In an amplifier, for better performance,  $R_E$  &  $C_E$  combination is used.

freq. Response curve :-

→ The fall of gain in low freq. region is due to effect of  $C_c$ ,  $C_{c2}$  and  $C_2$ .

→ In mid frequency region, all coupling & bypass capacitor will be treated as ac short. All junc<sup>n</sup> capacitors ( $C_3$  &  $C_7$ ), cutting capacitor ( $C_W$ ) & stray capacitor ( $C_s$ ) will be treated as open.

→ The gain of amp is more & almost independent of f. at mid freq. and hence the amplifier analysis is generally done at mid freq. range.



→ We fall of gain at high freq is due to the effect of jum<sup>n</sup> capacitor ( $C_b, C_f$ ) &  $C_o, C_s$ . and early effect.

(182)

→ High freq. fall is mainly due to  $C_D$  &  $C_C$ .

→ cutoff freq is also called 3dB freq. or half power freq.

→ At cutoff freq. ( $f_H$  or  $f_L$ ), gain of amp reduces to 70.7% of peak value, i.e.,  $|A_{mid}|/\sqrt{2}$  and o/p power of amplifier is reduced to 50% of peak value.

→ At cutoff frequency, the relative gain of amp is reduced by 3dB from its peak value.

Bandwidth :-

→ It is the band of i/p signal frequencies where the gain of amp. is almost constant.

$$\boxed{BW = f_H - f_L}$$

→ Larger BW indicates better reproduction of i/p signal.

→ In an amplifier, gain-bandwidth product is always constant, i.e., when one rises, other lies & vice versa.

Disadvantage :- Smaller gain  $\times$  BW.

Note - Amplifiers are connected in cascade to get larger Gain  $\times$  BW product.

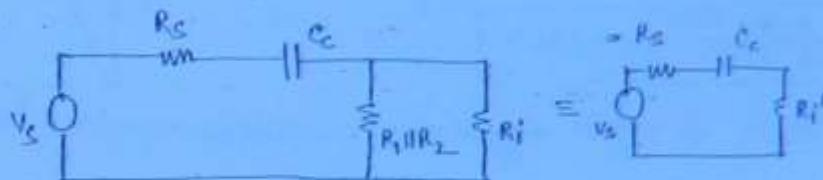
Calculation of  $f_L$ :

(183)

→  $f_L$  due to  $C_C$ . Assume  $C_1 \& C_2 \rightarrow \infty$  & acts as  $\infty$ .

→ We can replace transistor with its input resistance.

$$\rightarrow R_i = h_{ie} = \beta r_e = T_{IE} + r_b$$



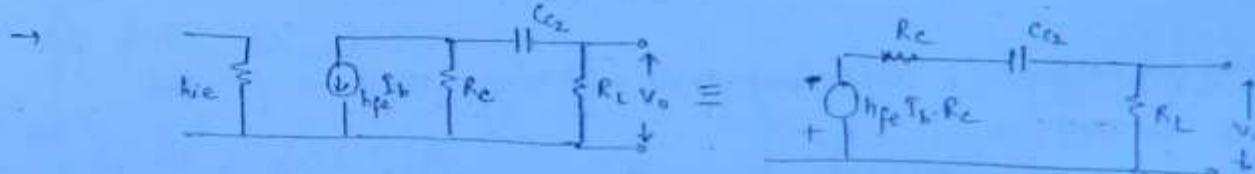
$$\rightarrow R_i' = R_1 \parallel R_2 \parallel R_i \approx R_i$$

$$\therefore f_L = \frac{1}{2\pi R_{eq} C} \Rightarrow f_L = \frac{1}{2\pi (R_i + R_i') \cdot C_C}$$

→ for high BW,  $C_C$  &  $R_i'$  values should be as high as possible.

( $R_S$  should not be high as it will  $\uparrow$  noise in i/p)

→  $f_L$  due to  $C_C$ : Assume  $C_1 \& C_2 \rightarrow \infty$  & acts as  $\infty$ .



$$f_L = \frac{1}{2\pi (R_i + R_L) \cdot C_C}$$

\* If  $f_L$  due to  $C_1$  &  $C_2$  is different then take the bigger value.

\* If  $f_H$  due to  $C_W/C_S$  or  $C_D/C_T$  .. " " " smaller value.

### Low frequency Analysis

amplifier

→ RC coupled act as HPF for low freq.

$$\rightarrow \text{Total phase shift} = 180^\circ + \tan^{-1} \left( \frac{f_L}{f} \right) \xrightarrow{\text{due to E config}} \xrightarrow{\text{due to HPF}} \Rightarrow \boxed{\text{At } f=f_L, \phi_T = 225^\circ}$$

$$\therefore A = \frac{1}{1-j(1/f)}$$

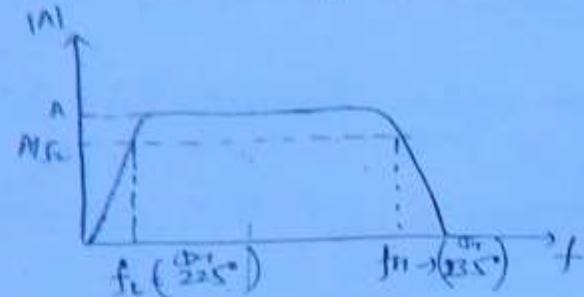
## High freq. analysis :-

(184)

$\rightarrow A = \frac{1}{1 + j(\frac{f}{f_m})}$  : RC coupled amplifier acts as LPF at high freq.

$\rightarrow \phi_T = 180 - \tan^{-1} \left( \frac{f}{f_m} \right)$ .

$\rightarrow \boxed{\text{At } f=f_m, \phi_T = 135^\circ}$



## Cascaded Amplifier / Multistage Amplifier :-

i) Amplifiers are connected in cascade to get larger gains.

→ When amplifiers are connected such that off of one is given to i/p to other, they are said to be cascaded.

- When amplifiers are cascaded, proper impedance matching must be provided in b/w stages so that -

1) o/p will not be distorted.

2) Max power will be transferred from one to another stage.

Note If mismatch is more in amplifier, o/p will be highly distorted.

## Different types of coupling -

i) RC coupling → (for voltage amplifiers)

ii) Transformer coupling → (for power amplifiers)

iii) Direct coupled → (basically used for dc amplification).

→ In a multistage amplifier,  $G_{XBW} = \text{constant}$ .

Note →  $G_{XBW}$  of two stage amplifier is greater than that of single stage amp.

→ In multistage amp, BW reduces.

## (8 marks)

### Bandwidth of Multistage Amplifier :-

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- $BW^* = f_H^* - f_L^*$       }  $BW^*$  → Bandwidth of multistage Amp.
- $BW = f_H - f_L$       }  $BW$  → " " single stage amp.
- $f_H/f_L^*$  → High/Low 3dB cutoff freq. of multistage
- $f_H/f_L$  → " " " " " single stage

Case -  $n$ - identical non-interacting (paper impedance matching) stages in cascade

### Derivation of $f_H^*$ :-

Gain for individual stage ,  $|A| = \frac{1}{\sqrt{1 + (f/f_H)^2}}$

for  $n$ - such stages,  $|A^*| = \left[ \frac{1}{\sqrt{1 + (f/f_H)^2}} \right]^n$

→ At  $f=0$ ,  $|A^*|_{max} = 1$ .

→ At  $f=f_H^*$ ,  $|A^*| = 1/\sqrt{2} \Rightarrow \frac{1}{\sqrt{2}} = \left[ \frac{1}{1 + (f_H^*/f_H)^2} \right]^{n/2}$

$$\Rightarrow f_H^* = f_H \left[ \sqrt[n]{2}^{n-1} \right]$$

$$\rightarrow \boxed{f_H^* < f_H}, \quad \boxed{n=2, \quad f_H^* = 0.64 f_H}$$

$$\boxed{n=3, \quad f_H^* = 0.51 f_H}$$

### Derivation of $f_H^*$ :-

Gain for individual stage,  $|A| = \frac{1}{\sqrt{1 + (f/f_H)^2}}$

for  $n$ - such stages,  $|A^*| = \left[ \frac{1}{\sqrt{1 + (f/f_H)^2}} \right]^n$

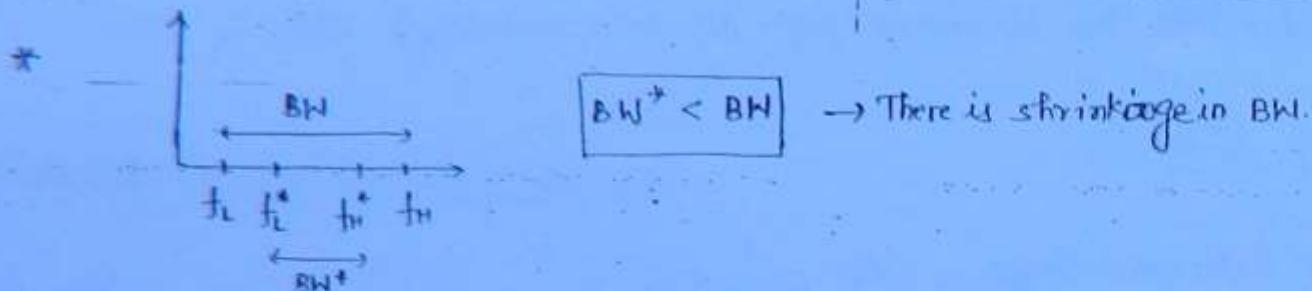
(186)

 $\rightarrow$  At  $f = \infty$ ,  $|A_{\max}^*| = 1$ .

$$\rightarrow \text{At } f = f_L^*, |A^*| = 1/\sqrt{2} \Rightarrow \frac{1}{\sqrt{2}} = \left[ \frac{1}{\sqrt{1 + (f_L/f_L^*)^2}} \right]^n$$

$$\Rightarrow f_L^* = \frac{f_L}{\sqrt{2^{1/n} - 1}}$$

$$\rightarrow [f_L^* > f_L] ; \begin{cases} n=2 & f_L^* = 1.56 \\ n=5 & f_L^* = 1.96 \end{cases}$$



Approximate Bandwidth :-  $\rightarrow BW = f_H - f_L \approx f_H$  ( $f_H \gg f_L$ )

$$\rightarrow BW^* = f_H^* - f_L^* \approx f_H^* \quad (f_H^* \gg f_L^*)$$

$$\Rightarrow [BW^* = (\sqrt{2^{1/n} - 1}) \cdot BW] ; (BW^* < BW)$$

Case :-  $n$ - non-identical interacting (ie, no proper impedance matching). stages in cascade.

$$\rightarrow \frac{1}{f_H^*} = 1.1 \times \sqrt{\frac{1}{f_{H1}^2} + \frac{1}{f_{H2}^2} + \dots + \frac{1}{f_{Hn}^2}}$$

Note. Disadvantages

$\rightarrow BW \downarrow$

$\rightarrow$  Rise time  $\uparrow$

(due to multi-stage)  
 $\therefore$  (slow response)

$$\rightarrow f_L^* = 1.1 \times \sqrt{f_{L1}^2 + f_{L2}^2 + \dots + f_{Ln}^2}$$

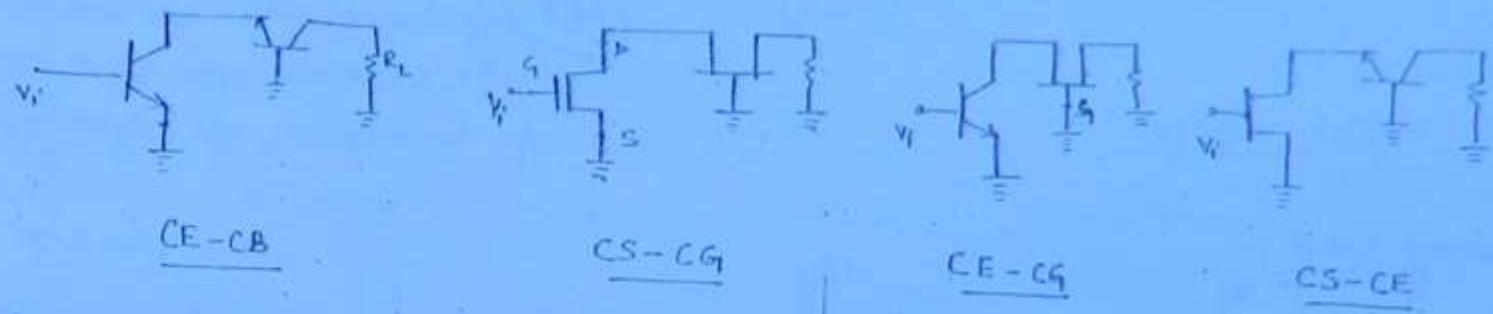
$$\rightarrow \text{Rise time, } t_r^* = 1.1 \times \sqrt{t_{r1}^2 + t_{r2}^2 + \dots + t_{rn}^2}$$

$$\rightarrow \text{If } t_{r0} = \text{rise time of signal} , t_r^* = 1.1 \times \sqrt{t_{r0}^2 + t_{r1}^2 + t_{r2}^2 + \dots + t_{rn}^2}$$

07/Sept/2012

## Cascade Amplifier:

(187)

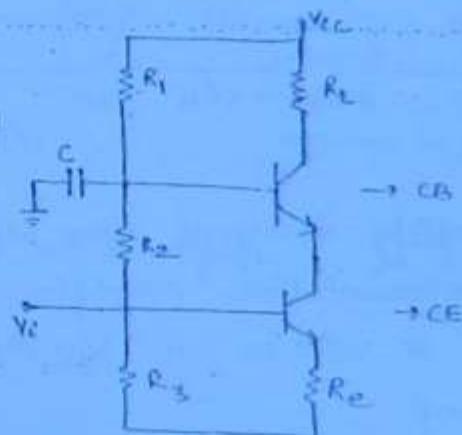


→ These all are series connections.

### Basic Diagram:

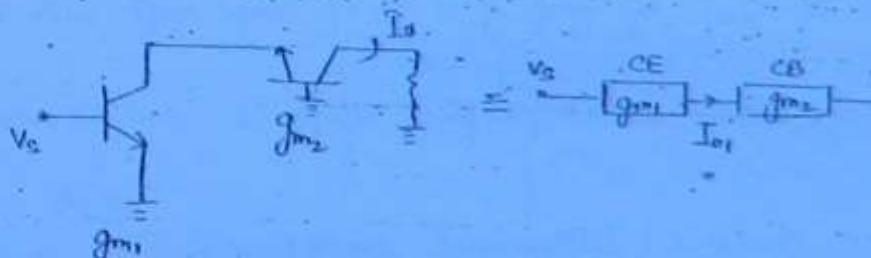
→ C → bypass capacitor; its value should be very low so that it charges quickly.

→ Purpose of this 'C' is to maintain CB in active region.



### Transconductance

$$g_{m1} = \frac{I_{o1}}{V_s}$$



For CB,  $A_1 \approx 1$ , it acts as buffer for current.

$$\therefore g_m = \frac{I_o}{V_s} = \frac{\alpha I_{o1}}{V_s} \Rightarrow \boxed{g_m = \frac{\beta}{1+\beta} \cdot g_{m1}} \quad (\text{exact}) \quad g_m \approx g_{m1}$$

if  $\beta \gg 1$ ,

### Imp. Points:

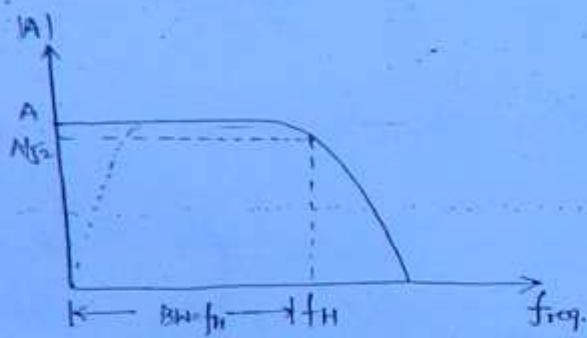
→ It is specially designed multistage amplifier the type of coupling provided is direct coupled, therefore suitable to amplify ac & dc signal but major application is as a high freq. amplifier.

→ The input resistance is equal to input resistance CE & output resistance is decided by output resistance of CB.

(188)

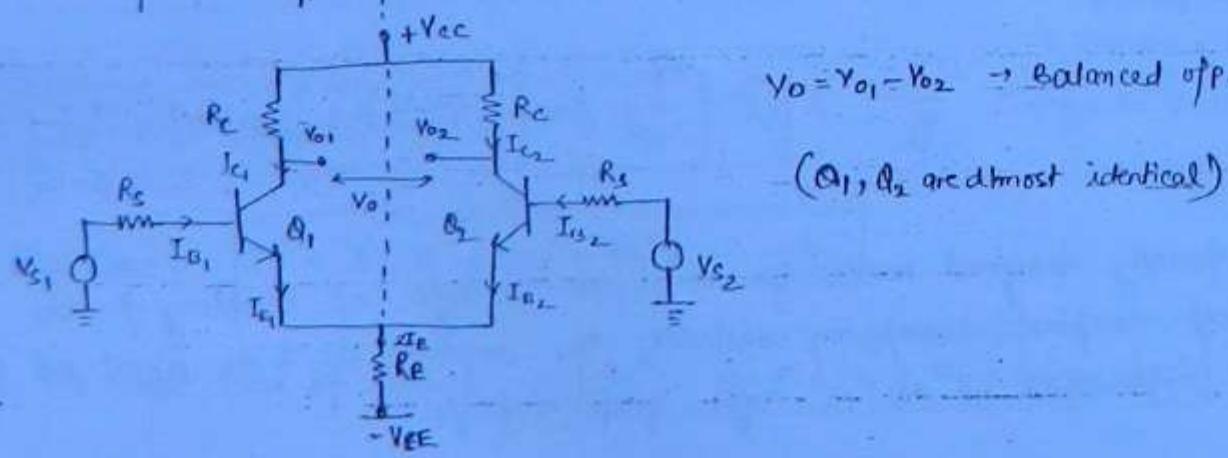
### Direct Coupled Amplifier :-

Frequency curve :-



- It is suitable to amplify dc signal along with a wideband of ac signals.
- Widely used as instrumentation amplifier.
- There is no proper dc isolation in b/w the stages, therefore stability is less.
- Any dc amplifier suffers from drift problem. Drift problem is mainly due to  $I_{CO}$ . { gain of or op of amp drift with temp as  $I_{CO}$  changes }
- Popularly used direct coupled amp is emitter coupled differential amp.

### Emitter Coupled Differential Amplifier :-



Mode of operation:-

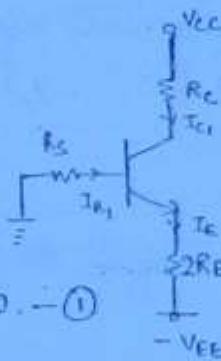
- 1) Dual i/p balanced o/p.
- 2) Dual i/p unbalanced o/p.
- 3) Single i/p balanced o/p.
- 4) Single i/p unbalanced o/p.

(189)

DC Analysis :-

$$\rightarrow V_{S1} = V_{S2} = 0$$

Applying KVL -



$V_{CE} = I_C \cdot R_C + V_{CE} + (1+\beta)I_B \cdot 2R_E - V_{EE}$  (because of feedback)  
(potential should be equal to  $2I_E R_E$   
but  $I_E$  cannot be doubled, hence  
resistance is doubled)

$$I_B R_B + V_{BE} + (1+\beta) I_B \cdot 2R_E - V_{EE} = 0 \quad \text{--- (1)}$$

$$V_{CE} = I_C \cdot R_C + V_{CE} + (1+\beta) I_B \cdot 2R_E - V_{EE} \quad \text{--- (2)}$$

$$I_C = \beta I_B \quad \text{--- (3)}$$

Now, if  $V_{CE} > 0.2$ , transistor is in active region.

AC analysis :-  $V_{EE} = V_{CC} = 0$ ,  $V_{S1} = V_{S2} = V_S$ .

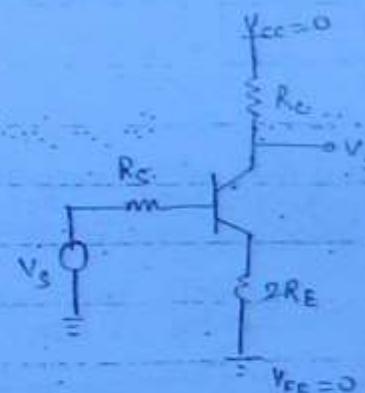
$\rightarrow A_c \rightarrow$  common mode gain.

$\rightarrow A_d \rightarrow$  differential " "

Now, for  $A_c$  -  $V_{S1} = V_{S2} = V_S$   
 $V_o = A_c V_C + A_d V_d$ .

$$V_d = V_{S1} - V_{S2} = 0 \Rightarrow V_o = A_c V_S$$

$$V_C = \frac{V_{S1} + V_{S2}}{2} = V_S \Rightarrow A_c = \frac{V_o}{V_S}$$



Common emitter with  
emitter resistance  $2R_E$ .

From circuit,

$$A_I = -h_{fe}, R_i = h_{ie} + (1+h_{fe})(2R_E)$$

$$A_{VS} = \frac{V_o}{V_S} = \frac{A_I R_L}{R_S + R_i} \Rightarrow A_c = \frac{-h_{fe} \cdot R_C}{h_{ie} + (1+h_{fe}) \cdot 2R_E}$$

Approximate value,  $A_c = \frac{-R_C}{2R_E}$   $\rightarrow$  when  $h_{fe} \gg 1$ .

$\rightarrow$  Ideally,  $A_C = 0 \Rightarrow R_E \rightarrow \infty$ . (Possible with current mismatch).

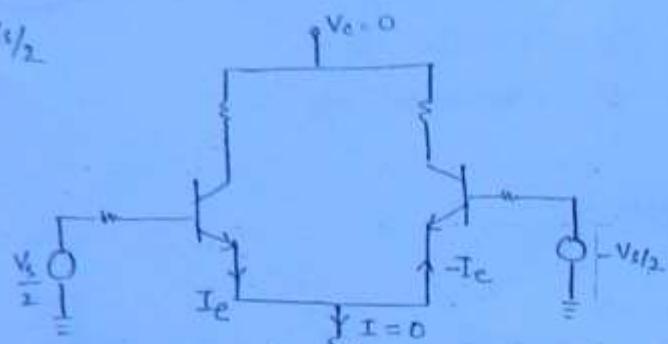
$\rightarrow$  As  $R_E \uparrow$ ,  $A_C \downarrow$ .

(190)

for Ad —  $V_{S2} - V_{S1} = -V_{t/2} \Rightarrow V_{S1} = V_{t/2}$

$\therefore V_d = V_S ; V_C = 0$

$$A_d = \frac{V_0}{V_S}$$



from fig(1) —

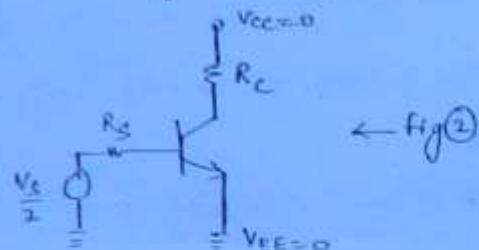
$A_V = -h_{fe} ; R_i = h_{ie}$

$$A_{V2} = \frac{V_0}{(V_{t/2})} = \frac{-h_{fe} \cdot R_c}{R_c + h_{ie}}$$

$$\Rightarrow \text{as } A_d = \frac{-h_{fe} \cdot R_c}{R_s + h_{ie}}$$

$$\Rightarrow A_d = \boxed{\frac{-h_{fe} \cdot R_c}{2(R_s + h_{ie})}}$$

Now, dividing the okt.



$\rightarrow$  ( $I_b$  does not depend on  $R_E$ )

$$\Rightarrow CMRR = \frac{|A_d|}{|A_C|} \Rightarrow CMRR = \frac{R_s + h_{ie} + [1 + h_{fe}] \cdot 2R_E}{2[R_s + h_{ie}]}$$

if  $(1+h_{fe})2R_E \gg R_s + h_{ie}$ , then

$$\boxed{CMRR = \frac{[1+h_{fe}] \cdot R_E}{R_s + h_{ie}}}$$

(As  $R_E \rightarrow \infty$ ,  $CMRR \rightarrow \infty$ )  
(ideal value)

Effect of increasing  $R_E$  :-

\*  $g_m = \frac{|I_C|}{V_T} \rightarrow$  as  $R_E \uparrow$ ,  $V_{ENF} \uparrow \rightarrow$  feedback  $\uparrow$   
 $\Rightarrow I_{OB} \downarrow \Rightarrow I_C \downarrow$ .

1) Negative feedback across  $R_E$  Tcs.

2)  $R_E \uparrow$ , CMRR  $\uparrow$ ,  $g_m \downarrow$ , gain  $\downarrow$  { $\uparrow$  gain of  $g_m$ }.

3)  $R_E \uparrow$

## Application :

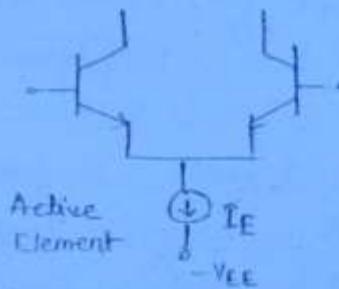
(19)

- It is used as the first internal stage in op-amp.
- As an instrumentation amp.
- As a very good clipper
- As a linear amplifier, i.e., we can apply superposition theorem.
- It is used in designing of AVC (Automatic voltage control) or AGC (auto-gain control).

→ Any 4

Note

- \* Any ideal diff. amplifier can be designed by connecting an ideal current source in place of  $R_E$ .



Ideal source -

$$\text{source resistance} = \infty = R_E$$

$$A_C = 0$$

$$\text{CMRR} = \infty$$

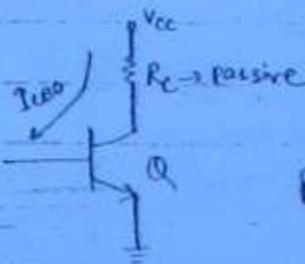
Practically

$R_E$  = Very high

Very low

Very high.

→ In a practical diff. amplifier, active load is connected to get best performance. In place of passive load  $R_E$ , pnp transistor is used to get maximum peak-to-peak o/p voltage or maximum swing.



$$\text{Ideal swing} = V_{cc}$$

$$\text{Prac. swing} = V_{cc} - \frac{I_{CE0}R_E}{\text{High}}$$

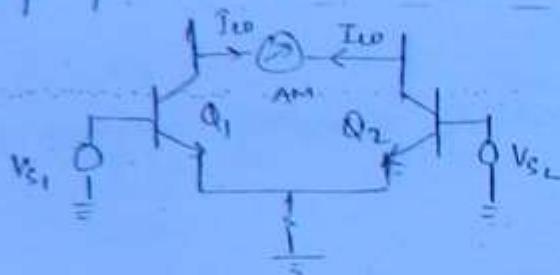
$\theta$	Ideal $V_o$	Practical $V_o$
$V_i > 0$	saturation	0
$V_i < 0$	cutoff	$V_{cc} - I_{CE0}R_E$

$V_i$	$\theta_1$	$\theta_2$	$R_{E2}$	$V_o$ (Prac.)
$V_i > 0$	on	off	$\approx 0$	$V_{cesat}$
$V_i < 0$	off	on	$\approx 0$	$\approx V_{cc}$
	$\therefore \text{swing} \uparrow$			$\rightarrow I_{CE0}$ is still present but $R_E \approx 0$

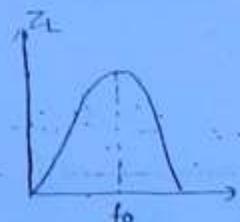
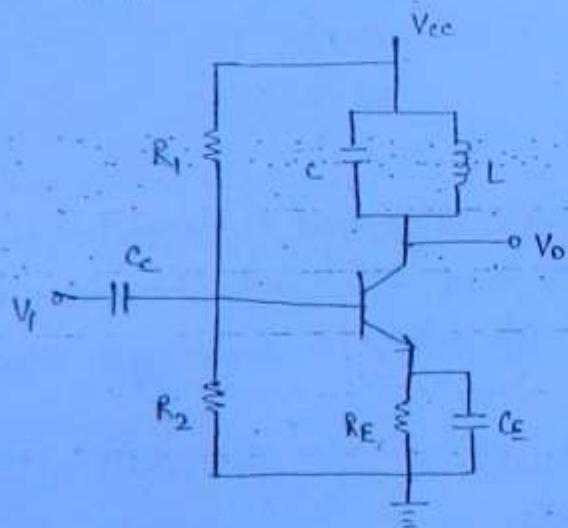
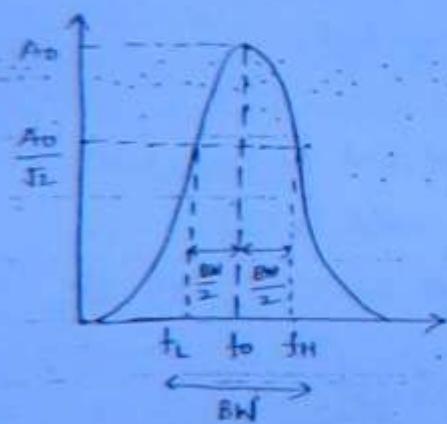
→ Differential amplifier is often used in dc application. It is difficult to design dc amplifier using  $T_r$  because of drift due to variations of  $V_{BE}$ ,  $V_{CE}$  &  $I_{CEO}$  with temp.

(192)

→ With  $Q_1$  and  $Q_2$  having almost identical characteristic, any parameter changes due to temp. will cancel out and  $\text{off}$  will not vary. for eg., leakage current of  $Q_1$  &  $Q_2$  are equal in magnitude but flowing in opposite direction into ammeter & they get cancelled, and hence drift problem is eliminated in emitter coupled diff. amp.



### Tuned Amplifier (class C amplifiers):-



$$A_y = \frac{A_I \cdot Z_L}{R_i}$$

$Z_L \rightarrow$  LC tank circuit.

$$\rightarrow \text{Resonance freq.} : f_0 = \frac{1}{2\pi\sqrt{LC}}$$

$$\rightarrow BW = \frac{f_0}{Q} ; Q \rightarrow \text{quality factor}$$

$$\rightarrow [A_s \uparrow, BW \downarrow] \Rightarrow [\text{selectivity} \propto Q]$$

{ To change the BW, Q should be changed and not  $f_0$ , as changing  $f_0$  will change the centre frequency.

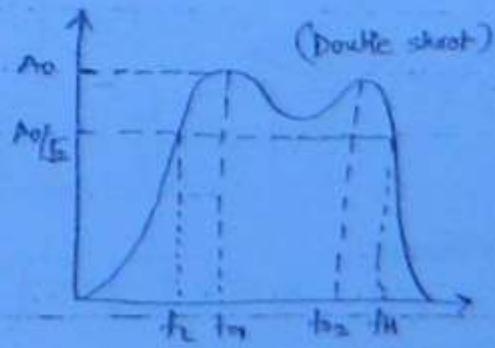
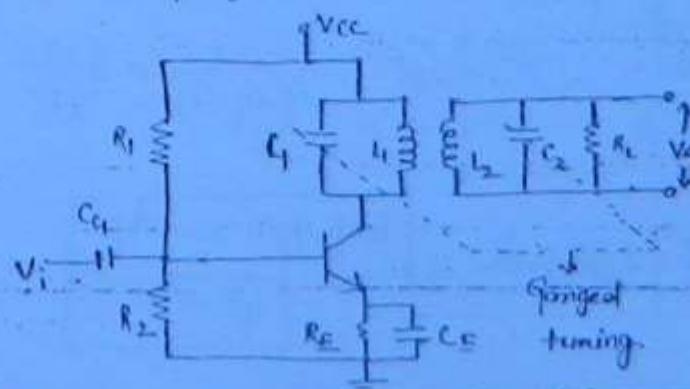
$$\rightarrow f_{H1} = f_0 + \frac{BW}{2}; \quad f_{L1} = f_0 - \frac{BW}{2}$$

(93)

- It is also called tuned voltage amplifier.
- Up signal freq. range :- 30KHz to 300KHz. (RF band, hence also called RF amplifier).
- Working principle is parallel resonance.
- Ability of amplifier to reject unwanted frequencies is called selectivity.
- It has ability to select a particular station signal for amplification by rejecting all other unwanted station signals, i.e., selectivity is very high.
- front end selectivity of receiver is done by RF amplifier, therefore tuned amplifier is first stage in superheterodyne receiver.
- Tuned amp is class C amplifier and it is a non-linear amp.
- for a tank circuit, Q is very large (100-500).
- BW is very small and this is due to -
  - i) larger Q.
  - ii) larger gain. ( $\because$  Gain  $\propto$  BW = constant).
- It is also called Narrow Band amplifier.

Disadvantage: Narrow BW. (with P in quality, BW requirement is but with fed BW, gain less).

### Double-Tuned Amplifier



$$\rightarrow f_{01} = \frac{1}{2\pi\sqrt{L_1C_1}}, \quad f_{02} = \frac{1}{2\pi\sqrt{L_2C_2}}$$

(194)

- In double tuned amplifier, two tank circuits which are tuned to resonant freq. are inductively coupled and placed in collector ckt.
- BW can be fed up reducing gain of amp, hence gain  $\times$  BW is not a constant

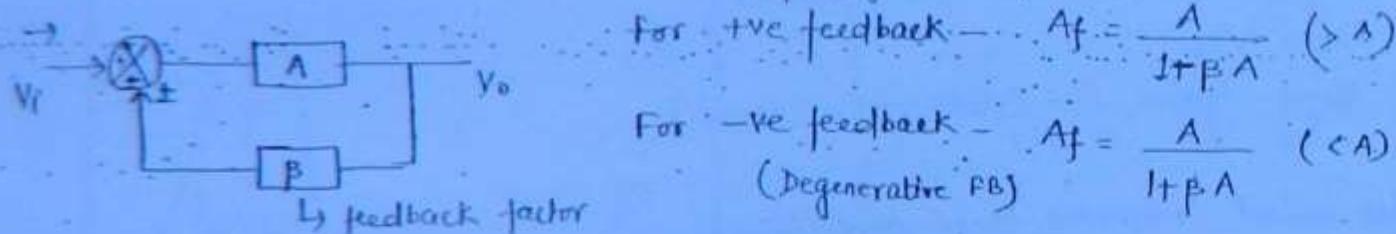
Advantage:-

- A larger BW when compared to a single tuned voltage amp.

### FEEDBACK AMPLIFIERS

$$\rightarrow V_i \rightarrow [A] \rightarrow V_o \quad A_{OL} = \frac{V_o}{V_i} = A$$

↑ Regenerative feedback

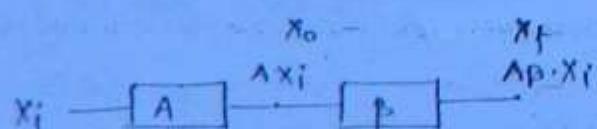


$$\text{for +ve feedback} \dots A_f = \frac{A}{1 + \beta A} (> A)$$

$$\text{For -ve feedback} \dots A_f = \frac{A}{1 + \beta A} \quad (\ll A) \quad \text{(Degenerative FB)}$$

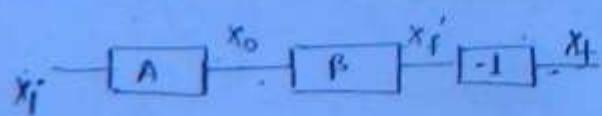
→ Loop Gain (Return Ratio) :- } OLTF } <sup>\* (by me)</sup>

→ +ve feedback -



$$\text{Loop gain} = \frac{x_f}{x_i} = A_B$$

→ -ve feedback:-



$$\text{Loop gain} = -A_B$$

Return Difference :-  $D = 1 - \text{loop gain}$

→ for +ve feedback -  $D = 1 - A\beta$

→ for -ve feedback -  $D = 1 + A\beta$ .

(195)

Advantage of Negative feedback :-

→ Stability of transfer gain increases.

(a) Desensitivity of transfer gain

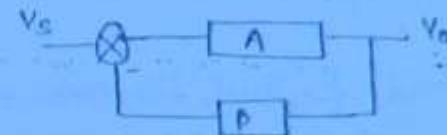


$$V_O = AV_S$$



$$\Delta V_O = dA \cdot V_S$$

$\frac{dA}{A}$  = fractional variation in  $A$   
w/o feedback.



$$V_O = \frac{A}{1 + \beta A} \cdot V_S \Rightarrow A_f = \frac{A}{1 + \beta A}$$

$\frac{dA_f}{A_f}$  = fractional variation with  
feedback.

→ if  $\left| \frac{dA_f}{A_f} \right| < \left| \frac{dA}{A} \right|$ , then gain after feedback is stable.

Sensitivity :-  $S = \frac{dA_f/A_f}{dA/A}$

→ for stability ;  $|S| < 1$

→ Desensitivity ,  $|D| = |S| \Rightarrow |D| > 1$  for stability after feedback.

Now,  $\frac{dA_f}{dA} = \frac{1}{(1 + \beta A)^2} \Rightarrow \frac{dA_f}{A_f} = \frac{dA/A}{(1 + \beta A)}$

→  $S = \frac{1}{1 + \beta A}$ ;  $|D| = 1 + \beta A$

(b) If feedback n/w contains only stable passive elements then there is improvement in stability

$$A_f = \frac{A}{1+Ap} = \frac{1}{\beta} \text{ if } Ap \gg 1.$$

196

Hence,  $\beta$  should consist of stable passive elements.

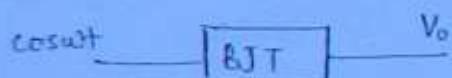
### Reduction in Frequency Distortion :-

frequency Distortion :- Variation in magnitude of gain with frequency.

Phase Distortion :- Variation in phase of gain with freq.

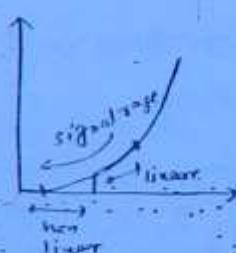
→ If  $A_f = \frac{1}{\beta}$  and feedback n/w does not contain reactive element, then overall gain is not a func<sup>h</sup> of freq., and there is reduction in frequency & phase distortion.

### Reduction in non-linear distortion :-



$$V_o = B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t + B_3 \cos 3\omega t + \dots$$

↓      ↓  
 dc      desired (fundamental component)



→  $\omega \uparrow$ , Amplitude  $\downarrow$ ,  $B_1 \gg B_2 \gg B_3 \dots$

$$\rightarrow D_2 = 2^{\text{nd}} \text{ Harmonic Distortion} = \frac{|B_2|}{|B_1|}$$

$$D_3 = 3^{\text{rd}} \text{ " } = \frac{|B_3|}{|B_1|}$$

$$D_4 = 4^{\text{th}} \text{ " } = \frac{|B_4|}{|B_1|}$$

→ After -ve feedback -

$$D_{2f} = \frac{D_2}{1+Ap}$$

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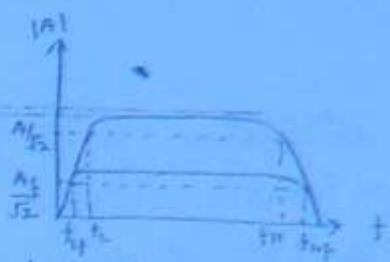
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Note:- Guys Be Cool Dude I am here for help You ☺

→ Bandwidth Increases -  $BW_f = BW [1 + AP]$



→ Since  $G \times BW$  constant & gain is by  $(1+AP)$  after feedback.

→ Reduction in Noise :-  $N_{of} = \frac{N_o}{1+AP}$  (197)

Other advantages :-

→ It modifies i/p & o/p resistance.

→ It increases thermal stability & freq. stability of o/p signal.

Disadvantage :-

- It reduces gain.

Application :-

-ve feedback is widely used in designing of amp. ckt and control system.

Positive feedback :-

→ Advantage :- inc. gain of amp.

→ Disadvantage :-

→ Reduces BW, hence reproduction of i/p signal is very bad.

- It ↑ noise & harmonic distortion at the o/p.

- It reduces stability of amp.

Application

- In designing of oscillator circuits.

Ques An amplifier w/o feedback gives a fundamental o/p of 36V with 7% 2nd harmonic distortion when i/p is 0.028V.

(198)

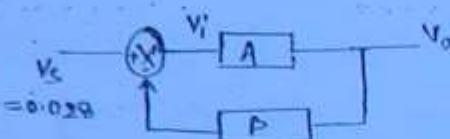
a) If 1.2% of o/p is feedback into i/p in a -ve voltage series feedback ckt, what is o/p voltage.

b) If fundamental o/p is maintained at 36V, but the 2<sup>nd</sup> harmonic distortion is reduced to 1%, what is i/p voltage.

Soln)  $V_i = V_s = 0.028$   $\boxed{A}$  36 + D<sub>2</sub>  
= 7%.

$$\therefore A = \frac{36}{0.028} = 1285$$

a)  $B = 1.2\% = 0.012$

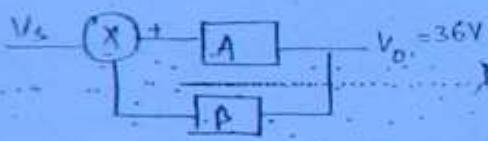


$$A_f = \frac{V_o}{V_s} = \frac{A}{1+AB}$$

$$A_f = \frac{1285}{1+1285 \times 0.012} = 78.2, \therefore V_o = 78.2 \times 0.028 = 2.19 \text{ V}_o$$

b)

$$D_{2f} = \frac{\lambda_2}{1+AB}$$



$$\Rightarrow 1 = \frac{7}{1+AB} \Rightarrow 1+AB = 7$$

$$\therefore A_f = \frac{A}{1+AB} = \frac{A}{7}, \quad \therefore \frac{V_o}{V_{S1}} = A, \quad \frac{V_o}{V_{S2}} = A_f$$

$$\Rightarrow \frac{V_{S2}}{V_{S1}} = \frac{A}{A_f} \Rightarrow V_{S2} = 7 \times 0.028 = 0.196 \text{ V}_{S2}$$

→ Feedback is often expressed in dB.

$$N_{dB} = 20 \log \left| \frac{A_f}{A} \right|$$

- For +ve feedback,

$$N_{dB} = 20 \log \left( \frac{1}{1 - A\beta} \right) \Rightarrow N_{dB} > 0 \text{ or } +ve.$$

(J99)

- For -ve feedback ,

$$N_{dB} = 20 \log \left( \frac{1}{1 + A\beta} \right) \Rightarrow N_{dB} < 0 \text{ or } -ve.$$

Ques: An amp with open loop voltage gain of 1000 delivers 10W of o/p power at 10%. 2<sup>nd</sup> harmonic distortion, when i/p is 10mV.

If 10 dB -ve voltage series feedback is applied and o/p power is to remain at 10W , determine

- Required i/p signal.
- 2<sup>nd</sup> harmonic distortion.

Sol

$$\rightarrow 40 = 20 \log \left( \frac{1}{1 + A\beta} \right) \Rightarrow 1 + A\beta = 100$$

$$\Rightarrow \beta = \frac{99}{1000}.$$

$$\rightarrow A_f = \frac{A}{1 + A\beta} \Rightarrow A_f = 10$$

$$\rightarrow D_f = \frac{D_2}{1 + A\beta} = \frac{10}{100} = 0.1 \%$$

$$\rightarrow V_i' = \frac{1000}{10} V_i = 100 \times 10mV = 1V$$

### Classification of Amplifiers:-

1> Voltage Amplifiers

2> Current "

3> Transconductance .

4> Trans resistance.

## Voltage Amplifiers :-

→  $R_i \gg R_s$ ,  $R_i = \infty$  (ideally)

→  $R_o \ll R_L$ ,  $R_o = 0$  (ideally).

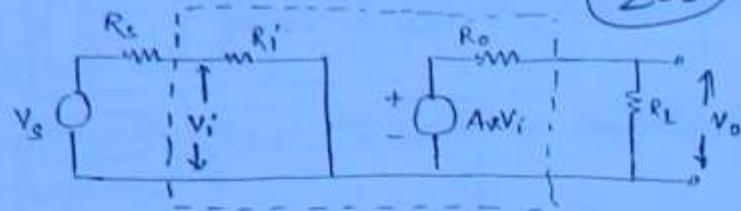
→  $A_v \rightarrow$  internal gain,  $A_{v\text{ext}}$  external gain.

$$\rightarrow V_o = \frac{A_v V_i \cdot R_L}{R_o + R_L} \Rightarrow A_v = \frac{A_v \cdot R_L}{R_o + R_L}$$

$$A_v = \frac{A_v \cdot R_L}{R_o + R_L}$$

$$A_v = \lim_{R_L \rightarrow \infty} A_v$$

When  $R_L = \infty$ ,  
external gain =  
internal gain



20B

## Current Amplifier :-

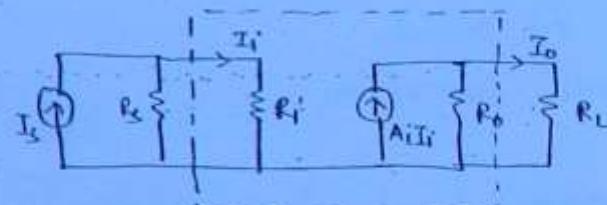
→  $R_i \ll R_s$ , i.e.,  $R_i = 0$  (ideally)

so that whole current passes through  $R_i$

→  $R_o \gg R_L$ , i.e.,  $R_o = \infty$  (ideally)

so that max current is delivered to load.

→  $A_I = \text{ext. gain}$ ,  $A_i = \text{internal gain}$ .



$$A_I = \frac{I_o}{I_i} = \frac{A_i R_o}{R_o + R_L}$$

$$A_I = \lim_{R_L \rightarrow 0} A_I$$

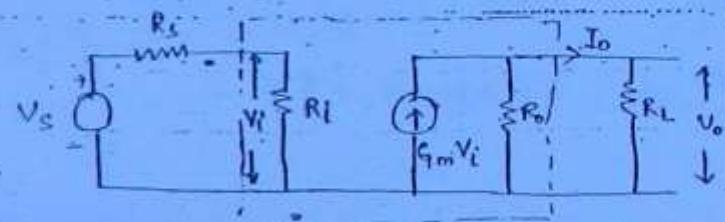
## Transconductance :-

→  $R_i \gg R_s$ ; ideally  $R_i = \infty$

→  $R_o \gg R_L$ , ...  $R_o = \infty$ .

$$\rightarrow I_o = \frac{G_m V_i \cdot R_o}{R_o + R_L} \Rightarrow G_m = \frac{G_m R_o}{R_o + R_L}$$

$$G_m = \lim_{R_L \rightarrow \infty} G_m$$

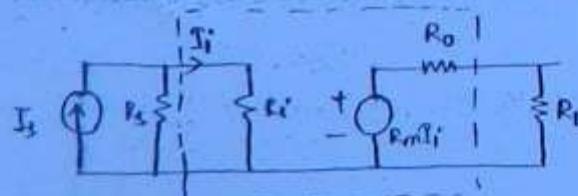


## Transresistance :-

→  $R_s \ll R_i$ , ideally  $R_s = 0$

→  $R_o \ll R_L$ , ...  $R_o = 0$

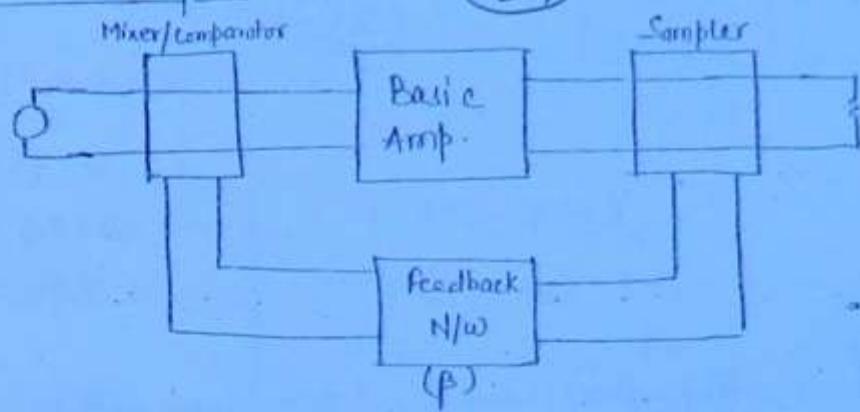
$$\rightarrow Y_o = \frac{R_m I_i \cdot R_L}{R_o + R_L} \Rightarrow R_m = \frac{R_m R_L}{R_o + R_L}$$



$$R_m = \lim_{R_L \rightarrow \infty} R_m$$

## Feedback Concept :-

(20)

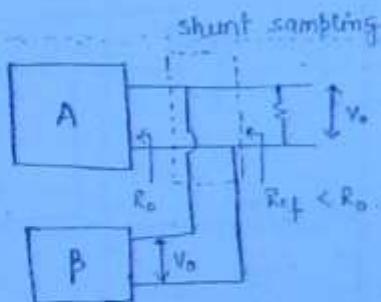


## Sampler :-

### a) Voltage Sampler :-

→ sampled voltage in feedback is same as o/p voltage.

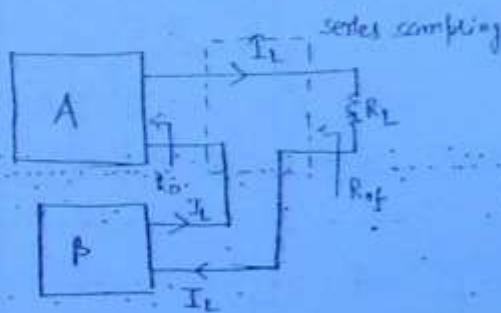
$$\rightarrow R_{of} < R_o$$



### b) Current Sampler :-

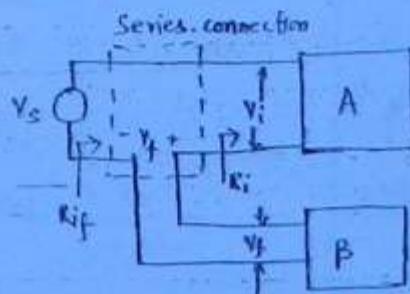
$$\rightarrow R_{of} > R_o$$

→ sampled current in feedback is same as o/p current.



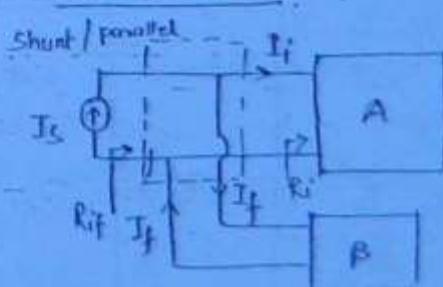
## Mixer :-

### (a) Voltage Mixer :-



$$\rightarrow R_{if} > R_i ; \text{ Before mixing } V_i = V_s \\ \text{After } \rightarrow V_i = V_s - V_f$$

### (b) Current Mixer :-



$$R_{if} < R_i ; \text{ Before mixing } I_i = I_s \\ \text{After } \rightarrow I_i = I_s - I_f$$

voltage  
current

After voltage i/p  
short with current i/p

## Feedback Topology :-

(202)

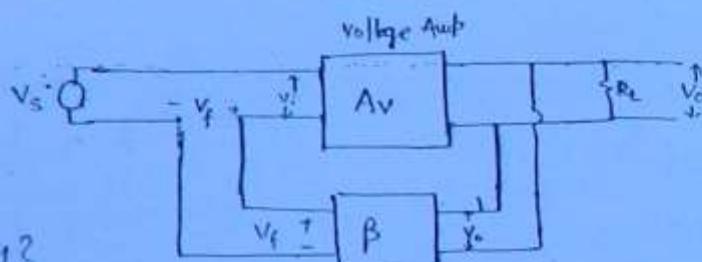
- 1) Voltage Series feedback.
- 2) Current Series "
- 3) Voltage Shunt "
- 4) Current Shunt "

Derivation of  $R_{if}$  (input resistance with feedback) and  $R_{of}$  (o/p resistance with FB)  
for Voltage Series feedback :-

$$\rightarrow V_f = \beta V_o$$

$$\Rightarrow \beta = \frac{V_f}{V_o} = \text{unit less.}$$

$$A_f^* = \frac{A_v}{1 + \beta A_v} = \frac{1}{\beta} \quad \{ \beta A_v \gg 1 \}$$



$$\rightarrow R_{of} < R_o \quad \& \quad R_{if} > R_i$$

Calculation of  $R_{if}$  -

Before feedback :-

$$V_i = V_s$$

$$\text{i/p resistance} = \frac{V_s}{I_i} = \frac{V_i}{I_i} = R_i$$

After feedback :-

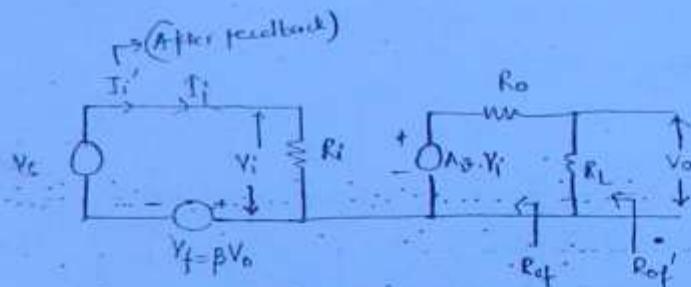
$$V_s = V_i + V_f \Rightarrow V_i = V_s - V_f$$

(There is -ve feedback).

$$\text{i/p resistance} \therefore R_{if} = \frac{V_s}{I_i'}$$

Applying KVL -

$$V_s = I_i' R_i + V_f$$



$$V_s = I_i' R_i + \beta V_o$$

$$\text{but } V_o = \frac{A_v \cdot V_i \cdot R_L}{R_o + R_L} = A_v \cdot R_o \cdot V_i$$

$$\therefore V_s = I_i' R_i + \beta \cdot A_v \cdot R_o \cdot V_i$$

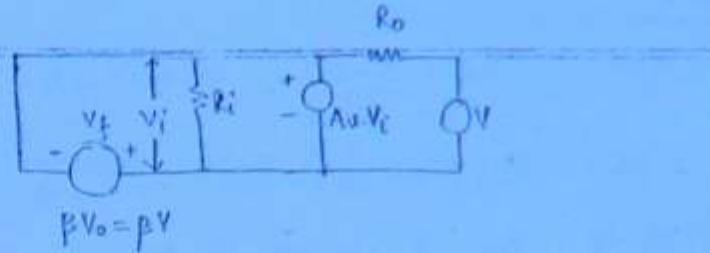
$$\Rightarrow V_s = I_i' R_i + \beta A_v R_i I_i'$$

$$\therefore \frac{V_s}{I_i'} = R_{if} = R_i + \beta A_v R_i$$

$$\Rightarrow [R_{if} = R_i (1 + \beta A_v)] \Rightarrow [R_{if} > R_i]$$

Calculation for  $R_{of}$ :

(203)



$$\rightarrow V = I R_o + A_v \cdot V_i$$

$$\rightarrow V_i + V_f = 0$$

$$\Rightarrow V_i + \beta V = 0$$

$$\Rightarrow V_i = -\beta V$$

$$\therefore V = I R_o - \beta A_v V$$

$$\Rightarrow V (1 + \beta A_v) = I R_o$$

$$\Rightarrow R_{of} = \frac{V}{I} = \frac{R_o}{1 + \beta A_v} \quad \{ R_{of} < R_o \}$$

$$R_{of}' = R_{of} \parallel R_L$$

	Voltage Series	Current Series	Voltage Shunt	Current Shunt
Output	V	I	V	I
Input	V	V	I	I
Basic Amplifier	Voltage Amp. $A_v = V_o/V_i$	Transconductance $G_m = I/V$	Transresistance $R_m = V/I$	Current Amp. $A_I = I_o/I_i$
Stabilised Gain	$A_{vf} = \frac{V_o}{V_s} = 1/\beta$	$G_{mf} = \frac{I_o}{V_s} = 1/\beta$	$R_{of} = \frac{V_o}{I_s} = \frac{1}{\beta}$	$A_{if} = \frac{I_o}{I_s} = \frac{1}{\beta}$
Unit of $\beta$	Unit less	ohm	mho	unit less
Another name ( $i/p-d/p$ )	Series-Shunt	series-series	shunt-shunt	shunt-series
Effect on $R_i$	Yes	Yes	No	Yes
" " $R_o$	Yes	Yes	Yes	Yes

	Voltage Series	Current Series	Voltage Shunt	Current Shunt
$\rightarrow R_i$	$V_i \xrightarrow{A_v} V_o$	$V \xrightarrow{G_m} I$	$I \xrightarrow{R_m} V$	$I \xrightarrow{A_I} I_o$
$\rightarrow R_i \uparrow$	$R_i \uparrow$	$R_i \uparrow$	$R_i \downarrow$	$R_i \downarrow$
$\rightarrow R_{if} = (1 + A_v \beta) R_i$	$\rightarrow R_{if} = (1 + \beta G_m) R_i$	$\rightarrow R_{if} = \frac{R_i}{1 + \beta G_m}$	$\rightarrow R_{if} = \frac{R_i}{1 + \beta A_I}$	
$\rightarrow R_{of} = \frac{R_o}{1 + \beta A_v}$	$\rightarrow R_{of} = (1 + \beta G_m) R_o$	$\rightarrow R_{of} = \frac{R_o}{1 + \beta G_m}$	$\rightarrow R_{of} = R_o (1 + \beta A_I)$	
$\rightarrow$ Normally, $A_v = A_{if}$ (i.e., $R_L \approx \infty$ )	$\rightarrow G_m \approx G_m$	$\rightarrow R_{if} \approx R_m$	$\rightarrow A_I \approx A_I$	
$\rightarrow R_{of}' = R_{of} \parallel R_L$				

### Workbook

Q.16.  $I_B = \frac{5 - 0.7}{10^3 \text{ k}\Omega} = 4.3 \times 10^{-3} \text{ mA}$

$I_C = \beta I_B = 100 \times 4.3 \times 10^{-3} = 0.43 \text{ mA}$

$$\frac{V_o - 12}{2\text{k}} + \frac{V_o}{4} + I_C = 0 \Rightarrow \frac{V_o}{2} + \frac{V_o}{4} - 6 + 0.43 = 0$$

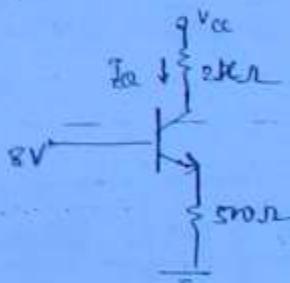
$$\Rightarrow V_o = 7.43 \text{ V}$$

Q.17 Since, the circuit is amplifier, then  $V_{CE} > 0.2$ .

$\Rightarrow V_E - V_E > 0.2 \text{ V}$

$V_E = 8 - 0.7 = 7.3 \text{ V}$

$V_E = V_{CE} - I_C \cdot 2\text{k}$



$\Rightarrow V_{CE} = 8 - 7.3 > 0.2 \text{ V}$

$\Rightarrow V_{CE} > 13.5 \text{ V}$

For pnp,  $V_{CE} < -13.5 \text{ V}$

10<sup>th</sup> Sep, 2012

### Voltage Series Feedback

Best practical examples are-

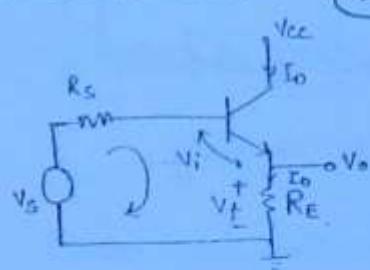
- emitter follower (CC configuration)
- source follower (CD<sub>rain</sub> ..)
- voltage follower (Non-inverting op-amp)

$\Rightarrow$  Basic Assumptions :-

- The basic amplifier is unilateral from i/p to o/p, i.e. it does not allow signal from o/p to i/p.
- Feedback n/w is unilateral, from o/p to i/p., i.e., it does not allow signal from i/p to o/p.
- $A_f$  is independent of source resistance  $R_s$  & load resistance  $R_L$ .

### Emitter follower :-

(20)



Let  $R_E$  is very small, hence drop across  $R_E$  can be neglected.

Without fb :- (or w/o RE)  $\Rightarrow V_i = V_s$ .

With fb :-  $V_i = V_s - V_f$

Since  $V_i$  is with feedback, hence -ve feedback.

& series inverting (since voltage is changing).

$\rightarrow$  Also,  $V_o = V_f$ .

$$\Rightarrow \beta = 1 \quad \{ \because V_f = \beta V_o \}$$

& there is voltage sampling.

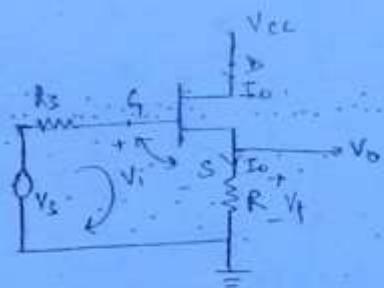
$$\rightarrow A_f = \frac{V_o}{V_s} = \frac{1}{\beta} = 1 ; \phi = 0$$

$\rightarrow$  If we assume current sampling, then

$$V_f = \beta I_o \Rightarrow \beta = \frac{V_f}{I_o} = \frac{I_o R_E}{I_o} = R_E$$

but  $\beta$  depends on  $R_E$ , hence not a current sampling.

### Source Follower:



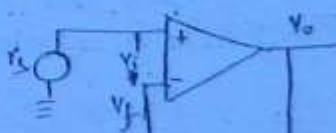
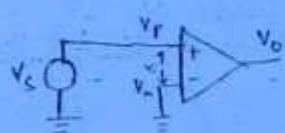
Results will be similar to Emitter follower.

For both circuits :-

- There is max. -ve feedback

- Gain is highly stable ( $\because \beta$  is independent)

### Voltage follower:



w/o feedback

$$V_{f0} = V_s - V_o = V_s$$

$$V_o = A_v \cdot V_i$$

with fb  $V_i = V_s - V_f \Rightarrow$  Vfb, -ve feedback & series inver.

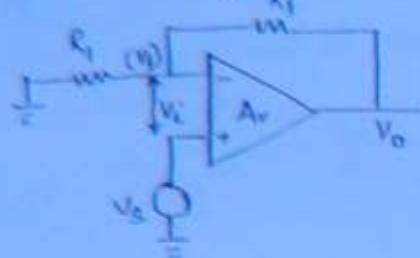
$$V_f = V_o \Rightarrow \beta = 1 \text{ & there is voltage sampling.}$$

$$A_{vf} = \frac{V_o}{V_s} = \frac{1}{\beta} = 1 ; R_{vf} = (1 + A\beta) \cdot R_f = (1 + 10^4 \cdot 1) \cdot 10^6 = 10^4 \text{ M}\Omega$$

$$B W_F (1 + A\beta) \cdot B W = 10^4 \cdot (B W)$$

$$R_{vf} = \frac{R_f}{1 + A\beta} = \frac{10^6}{10^4} \approx 0.01 \Omega = 0.01$$

### Non-Inverting Op-Amp



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(con't)

$V_i = V_s - V_f \Rightarrow$  -ve feedback, voltage inverting companion

$$V_f = \frac{R_f}{R_1 + R_f} V_o \Rightarrow V_f = \beta V_o \Rightarrow \text{voltage sampling.}$$

$\beta$  is constant ( $\because R_1, R_f$  are neither source resistance, nor load resistance).

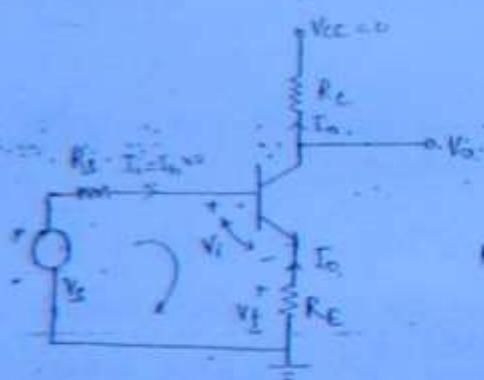
$$\rightarrow A_{Vf} = \beta = \left(1 + \frac{R_f}{R_1}\right) \rightarrow \text{(approximate)}$$

$$\rightarrow A_{Vf} = \frac{A_v}{1 + \beta \cdot A_v} \rightarrow \text{(exact),}$$

$$\rightarrow R_{if} = (1 + \beta A_v) \cdot R_1 ; \quad R_{of} = \frac{R_o}{1 + \beta A_v} ; \quad B_{Wf} = (1 + \beta A_v) B_W.$$

### Current Series Feedback :-

#### (i) CE with unbypassed $R_E$ :-



let drop across  $R_E \approx 0$ .

$$\text{w/o } R_E \approx V_i = V_s$$

$$\text{with } R_E \approx 0 \quad V_i = V_s - V_f$$

$\therefore$  There is series comparison

Now, let there is voltage sampling,

$$V_f = \beta V_o \quad \therefore \beta = \frac{V_f}{V_o} \Rightarrow \beta = -\frac{I_o R_E}{I_o R_C} \Rightarrow \beta = -\frac{R_E}{R_C}.$$

$$\rightarrow A_{Vf} = \frac{V_o}{V_i} = \beta = -\frac{R_E}{R_C}.$$

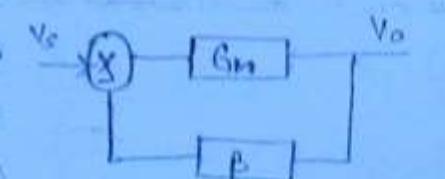
But,  $\because \beta$  is dependent on load  $R_L = R_C$ , hence our assumptions are wrong.

Hence, there is current sampling.

$$\therefore V_f = \beta I_o \Rightarrow \beta = \frac{R_E}{V_f} = -\frac{E_o R_E}{I_o R_E} \Rightarrow \beta \propto \frac{1}{R_E}.$$

$$\therefore \beta = \frac{V_f}{I_o} \Rightarrow \boxed{\beta = -R_E}$$

Hence gain is independent of  $R_C$

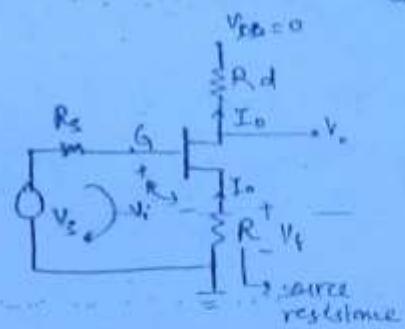


$$G_{Mf} = \frac{I_0}{V_s} = \frac{1}{R_B} = -\frac{1}{R_E}$$

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$$\Delta V_f = \frac{V_o}{V_s} = \frac{I_0 R_C}{V_s} \Rightarrow \Delta V_f = -\frac{R_C}{R_E}$$

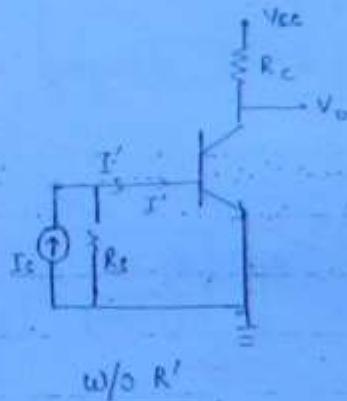
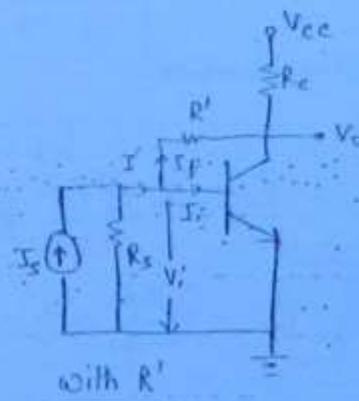
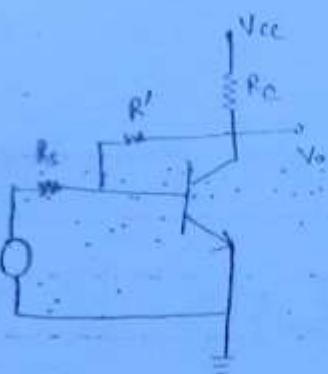
Common Source with unbypassed source resistance:-



$$\begin{aligned} \beta &= -R \\ G_{Mf} &= -\frac{1}{R} \\ \Delta V_f &= -\frac{R_D}{R} \end{aligned}$$

Voltage Shunt Feedback :-

(a) Collector  
eB Bias circuit :-

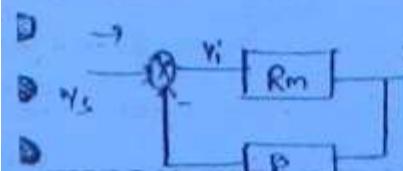


→ w/o feedback,  $I_f = I'$

→ with fb  $-I_f = I' - I_f \Rightarrow I_f \downarrow$ , hence shunt voltage compression

→ for A CE configuration,  $|\Delta V| \gg 1 \Rightarrow V_o \gg V_i$

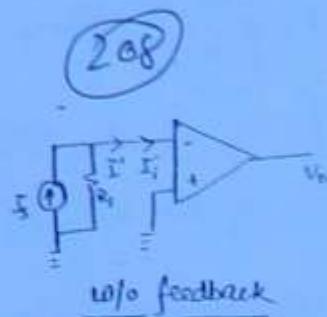
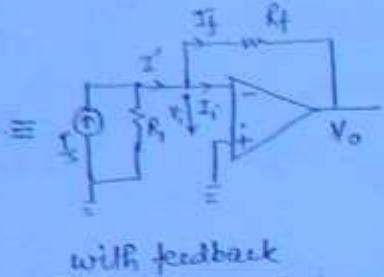
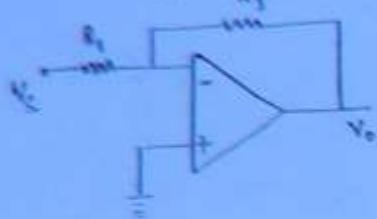
$$\rightarrow I_f = \frac{V_i - V_o}{R_f} \Rightarrow I_f = -\frac{V_o}{R_f} \Rightarrow \beta = -\frac{1}{R_f}$$



$$R_{Mf} = \frac{V_o}{I_s} \Rightarrow R_{Mf} = -R_f$$

$$\Delta V_f = \frac{V_o}{V_s} \Rightarrow \frac{V_o}{I_s R_s} \Rightarrow \Delta V_f = -\frac{R_f}{R_s} \quad \text{with } V_i \downarrow \Delta V_f$$

### Inverting Op-amp :-



w/o feedback:  $I_i = I'$

with feedback:  $I_i = I' - I_f \Rightarrow$  shunt mixing component

Now,  $V_i = V_o - V_p ; V_o = Av \cdot V_i ; Av < 0$

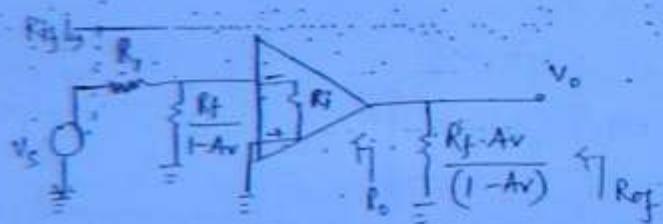
$\because |Av| \gg 1 \Rightarrow V_o \gg V_i$

$$\rightarrow I_f = \frac{V_i - V_o}{R_f} \Rightarrow I_f = -\frac{V_o}{R_f} \Rightarrow \boxed{\beta = \frac{1}{A} R_f} \quad (\text{Ans})$$

$$\rightarrow R_{of} = \frac{1}{\beta} = -\frac{R_f}{A} \Rightarrow A_{vf} = \frac{V_o}{V_s} = \frac{V_o}{I_s R_s} \Rightarrow \boxed{A_{vf} = -\frac{R_f}{R_s}}$$

$$\Rightarrow R_{of} = -R_f = \frac{V_o}{I_s}$$

To calculate  $R_{if}$  &  $R_{rf}$ , applying Miller's theorem -



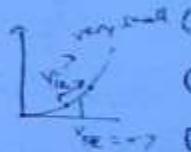
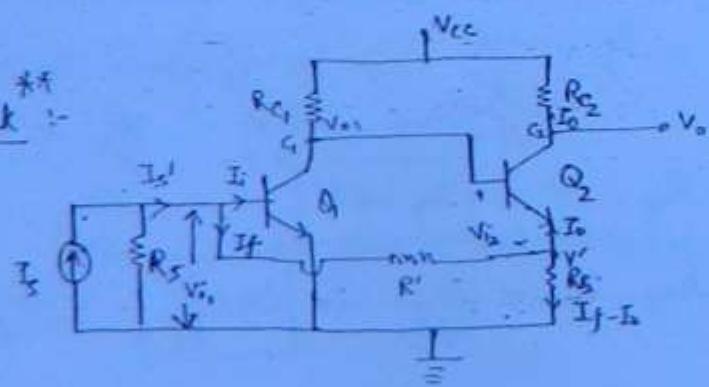
$$R_{if} = R_i + \left( \frac{R_f}{1-Av} \parallel R_i \right)$$

$$\because \frac{R_f}{1-Av} \approx 0 \quad \{ \because |Av| \gg 1 \}$$

$\Rightarrow R_{if} = R_i$  hence Reduced i/p resistance

$$\Rightarrow R_{of} = R_o \parallel \left( \frac{R_f \cdot Av}{1-Av} \right) \approx R_o \parallel R_f \quad \text{Hence, o/p resistance also les.}$$

### Current Shunt Feedback :-



w/o feedback  $\rightarrow I_i = I_s'$

with feedback  $\rightarrow I_i = I_s' - I_f \Rightarrow$  shunt mixing configuration

Now,

$$A_{Vi} = \frac{V_{o1}}{V_i} \gg 1 \Rightarrow V_{o1} \gg V_i \quad \left\{ \because \text{CE configuration?} \right\}$$

$$\text{Now, } V' = V_{o1} - V_{i2} \approx V_{o1} \quad \left\{ \because V_{i2} \ll V_{o1}, V_{i2} = \text{small signal} \right\}$$

$$\Rightarrow V = V_{o1} \gg V'$$

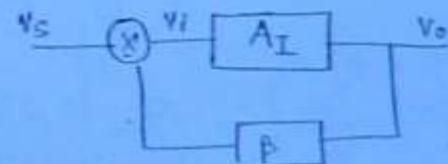
$$\rightarrow I_f = \frac{V_i - V'}{R'} \Rightarrow I_f = -\frac{V'}{R'}, \text{ but } V' = (I_f - I_o)R_E$$

$$\Rightarrow I_f = -\frac{(I_f - I_o)R_E}{R'}$$

$$\Rightarrow I_f = \frac{R_E}{R' + R_E} \cdot I_o \rightarrow \because I_f \text{ depends on } I_o, \text{ hence current sampling.}$$

$$\Rightarrow I_f = \beta I_o \Rightarrow$$

$$\boxed{B = \frac{R_E}{R' + R_E}}$$



$$\rightarrow A_{If} = \frac{V_o}{V_s} = \frac{1}{B} = \frac{1 + \frac{R'}{R_E}}{1 + \frac{R'}{R_E}}$$

$$\rightarrow A_{Vf} = \frac{V_o}{V_f} = \frac{I_o \cdot R_{G2}}{I_s R_s} \Rightarrow \boxed{A_{Vf} = \frac{1}{\beta} \cdot \frac{R_{G2}}{R_s}}$$

# FET

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$$\rightarrow i_d = f(V_{gs}, V_{ds})$$

$$\rightarrow i_d = g_m V_{gs} + \frac{V_{ds}}{r_d} \quad \text{--- (1)}$$

Change in  $i_d$  due to  $V_{gs}$  &  $V_{ds}$  -

$$di_d = g_m dV_{gs} + \frac{dV_{ds}}{r_d}$$

When  $V_{ds} = \text{constant}$ ;  $\boxed{g_m = \left. \frac{di_d}{dV_{gs}} \right|_{V_{ds}=\text{constant}}} = \text{Transconductance}$

When  $V_{gs} = \text{constant}$ ;  $\boxed{r_d = \left. \frac{dV_{ds}}{di_d} \right|_{V_{gs}=\text{constant}}} = \text{drain resistance}$

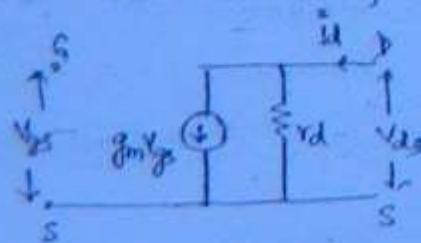
$$\rightarrow \mu = \text{amplification factor} \Rightarrow \boxed{\mu = g_m \times r_d}$$

$$\rightarrow g_m = g_{mo} \left( 1 - \frac{V_{gs}}{V_p} \right) ; \quad g_{mo} = \frac{2I_{DSS}}{|V_p|} = g_m \Big|_{V_{gs}=0}$$

$$\rightarrow I_{DSS} = T_{DSS} \left( 1 - \frac{V_{gs}}{V_p} \right)^2 ; \quad T_{DSS} = I_{DSS} \Big|_{V_{gs}=0} ; \quad I_{DSS} = \text{saturation drain current}$$

$$\rightarrow g_{mo} = \frac{2}{|V_p|} \sqrt{I_D \cdot T_{DSS}}$$

Small signal Model :- (at low frequency)



Workbook :-

Chap 7 :-

(21)

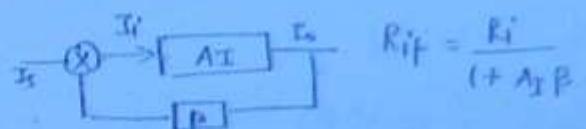
Q.1 b (All wrong answers)

Q.4 (c)

Q.5 (5)  $A_v = 50$ ;  $\beta = 0.2$

Q.2 (a)

Q.5 (b)



Q.3 (d)

Q.6 (a)

Q.8 (b)

Q.7 (b)

$$A_v = \frac{V_o}{V_i} = \frac{I_o R_o}{I_i R_i} = \frac{A_i R_o}{R_i}$$

Q.12 :-

$$G_{mI} = \frac{I_o}{V_i} = -1 \text{ mA/V}$$

Q.10 (c)

$$\therefore R_{if} = 1/5 \Omega$$

$$A_{vI} = -1$$

$$D = 1 + \beta \cdot G_m = 50$$

$$\beta = -R_E$$

$$G_{mI} \approx 1/B \approx -1/R_E$$

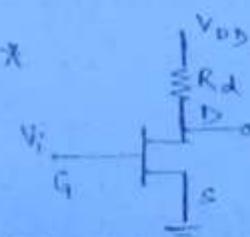
$$\Rightarrow -1 \approx -1/R_E$$

$$\therefore R_E \approx 1 \text{ k}\Omega$$

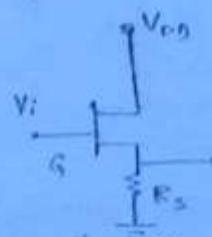
$$\therefore G_{mI} = \frac{G_m}{1 + \beta G_m} \Rightarrow -1 = \frac{G_m}{50} \Rightarrow G_m = -50$$

$$1 + \beta (-50) = 50 \Rightarrow \beta = -\frac{51}{50} = -R_E$$

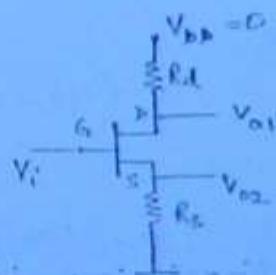
$$\Rightarrow R_E = 0.98 \text{ k}\Omega$$



Common Source



Common Drain



$V_o = V_{o1} = CS \text{ with output resistance } R_d$

$V_o = V_{o2} = CD \text{ with chain resistance } R_d$

Fig. 1

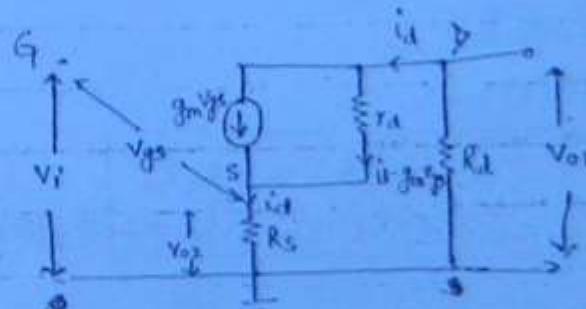
\* Small Signal Analysis :-

KVL at i/p :-

$$V_{o1} = -i_d R_d \quad \text{--- (1)}$$

$$V_{o2} = i_d R_s \quad \text{--- (2)}$$

$$V_{gs} = V_i - i_d R_s \quad \text{--- (3)}$$



(From fig. 1)

$$\therefore \mu = g_m \times r_d \text{ & eqn (3) ---}$$

$$i_d (R_d + r_d + R_s) - \mu (V_i - i_d R_s) = 0$$

KVL at o/p :-

$$-i_d R_d - r_d (i_d - g_m V_{gs}) - i_d R_s = 0$$

$$\Rightarrow i_d (R_d + r_d + R_s) - g_m r_d V_{gs} = 0$$

$$\Rightarrow i_d = \frac{\mu \cdot V_i}{R_d + r_d + (1+\mu)R_s} \quad \text{--- (4)}$$

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for CS with source resistance  $R_s$  —

$$V_{oi} = -i_d \cdot R_d$$

$$\Rightarrow V_{oi} = \frac{-\mu \cdot V_i \cdot R_d}{R_d + r_d + (1+\mu)R_s} \quad \text{--- (5)} \Rightarrow A_v = \left[ \frac{-\mu \cdot R_d}{R_d + r_d + (1+\mu)R_s} \right] ; \phi = 180^\circ \quad \text{--- (6)}$$

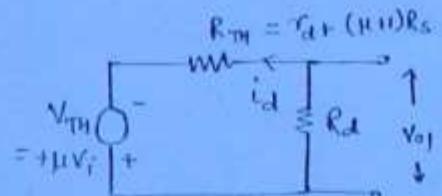
$\rightarrow$  if  $(\mu+1)R_s \gg (R_d + r_d)$  —

$$A_v = \left[ \frac{R_d}{R_s} \right] \quad \text{--- (7)}$$

→ Independent of any parameter for FET, hence gain is highly stable.

Thevenin's equivalent :-

$$V_{oi} = \frac{R_d}{R_d + R_{TH}} \cdot V_{TH} \quad \text{--- (8)}$$



from (5) & (8) —

$$\begin{aligned} R_{TH} &= R_o = r_d + (\mu+1)R_s \\ V_{TH} &= -\mu V_i \end{aligned} \quad \text{--- (9)}$$

⇒ o/p resistance increases due to current series feedback. (Effect on k/p resistance is neglected as it is already  $\approx 0$ )

for common source ( $\omega/\theta R_s$ ) :-

Put  $R_s=0$  in eqn (6) —

$$A_v = \left[ \frac{-g_m r_d \cdot R_d}{R_d + r_d} \right] = -g_m R_d' \quad ; \quad R_d' = R_d || r_d$$

From eqn (9) —

$$R_o = r_d = R_{TH}$$

use if  $r_d$  is not given, then take it  $\infty$ .

For Common Drain with drain resistance  $R_d$  :-

$$V_{o2} = i_d \cdot R_s$$

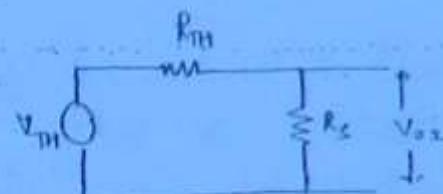
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$$\therefore V_{o2} = \frac{\mu V_i R_s}{R_d + r_d + (\mu+1) R_s} \quad \text{--- (12)}$$

$$\Rightarrow A_v = \frac{V_{o2}}{V_i} \Rightarrow \boxed{A_v = \frac{\mu \cdot R_s}{R_d + r_d + (\mu+1) R_s}} \quad \text{--- (13)}, \quad \boxed{\phi_{shift} = 0^\circ} \\ |\Delta v| < 1$$

Thevenin's Equivalent :-

$$\frac{V_{TH} + R_s}{R_s + R_{TH}} = V_{o2} \quad \text{--- (14)}$$



Dividing numerator & denominator by  $(\mu+1)$  in eqn (12) -

$$V_{o2} = \frac{\left(\frac{\mu}{\mu+1}\right) V_i \cdot R_s}{\frac{R_d + r_d}{\mu+1} + R_s} \quad \text{--- (15)}$$

Comparing (14) & (15) -

$$\boxed{V_{TH} = \left(\frac{\mu}{\mu+1}\right) \cdot V_i} ; \quad \boxed{R_{TH} = \frac{R_d + r_d}{\mu+1} = R_0} \quad \text{--- (16)}$$

For common drain (w/o  $R_d$ ) :-

Putting  $R_d=0$  in eqn. (13) -

$$A_v = \frac{\mu R_s}{r_d + (\mu+1) R_s}; \quad \text{if } (\mu+1) R_s \gg r_d \approx \mu \gg 1,$$

$\Rightarrow \boxed{A_v \approx 1}$   $\rightarrow$  Circuit is called source follower.

from eqn (16) -

$$\rightarrow R_0 = \frac{r_d}{\mu+1} \approx \frac{r_d}{\mu} \approx \frac{r_d}{g_m r_d} \Rightarrow \boxed{R_0 = \frac{1}{g_m}}$$

## Source Self Biasing

### DC analysis

$$V_{GS} = I_D R_S + V_{DS} + I_D R_S$$

$$\Rightarrow V_{GS} = V_{DS} + I_D R_S$$

$$\Rightarrow V_{GS} = V_{DS} - I_D R_S$$

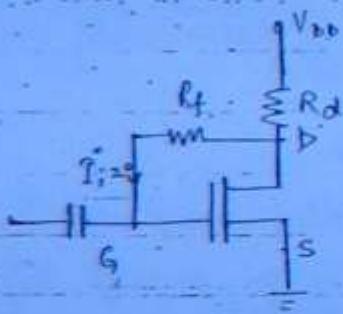
$$\text{if } V_{GS}=0; \quad V_{DS} = -I_D R_S.$$

if  $R_S=0$ ;  $V_{GS} = V_{DS}$ . — (fixed Biased ckt)

→ Self bias technique cannot be used to establish an operating point for enhancement-type MOSFET as voltage drop across  $R_S$  is in a direction to reverse bias the gate and forward gate bias is required for E-MOSFET.

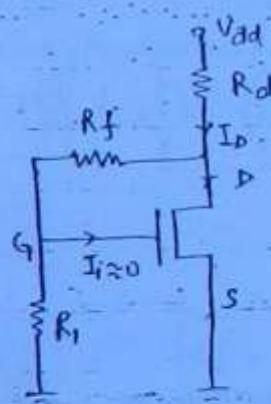
→ This is used for JFET or depletion-type MOSFET.

## Drain-Gate biasing for Enhancement Type MOSFET



$$V_{DS} = I_D R_f + V_{GS}$$

$$\Rightarrow V_{DS} = V_{GS}$$



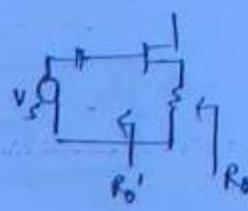
$$V_{GS} = \frac{R_I}{R_I + R_f} \cdot V_{DS}$$

## Workbook (Chap. 4)

$$(1)(b) \mu = \frac{dV_{GS}}{dV_{GE}}$$

$$(2)(c) R_o = R_o' \parallel R_S$$

$$R_o' = 1/g_m = \frac{1000}{3}$$



$$R_o = \frac{1000 \parallel 3000}{3} \\ = 300$$

$$\text{Ques. 3: } \frac{V_{0s}}{V_i} = -g_m R_d' , \quad R_d' = r_d \parallel R_d = \infty \parallel R_d = R_d$$

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$$\therefore V_{GS} = 0, \quad V_{GS} = -I_D \cdot R_S \\ = -2.5V$$

$$\therefore g_m = \frac{2I_{DSS}}{|V_P|} \left[ 1 - \frac{V_{GS}}{V_P} \right] = \frac{2 \times 10}{5} \left( 1 - \frac{2.5}{5} \right) = 2$$

$$\therefore A_V = -2 \times 3 = -6$$

$$\text{Ques. 4: } V_{GS} = V_{GDS} = -2V$$

Ques. 5 (a)

$$g_m = \frac{2 \times 10}{8} \left( 1 - \frac{2}{8} \right) = 2.5 \text{ mS}$$

$$A_V = -g_m R_d' ; \quad R_d' = 20k \parallel 2k$$

$$\therefore A_V = -3.41$$

$$\text{Ques. 5 (b)} \quad A_V = -g_m R_d' = -g_m (R_d)$$

When  $\rightarrow$  for ac analysis,  $V_{DD} = 0$ ,  $C \rightarrow \text{short}$ ,  $R_D = 5k \parallel 10k$ .

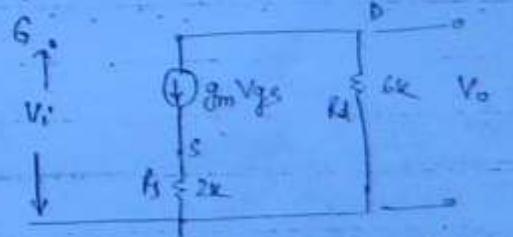
$$|A| = 2 \times (5 \parallel 10) = 5$$

$$\text{Ques. 8 (c)} \quad R'_i = 20 \parallel 100k \parallel 10 \omega$$

$$R'_i = 16.67 \text{ k}\Omega$$

$$\text{Ques. 9 (d)} \quad A_V \approx -\frac{R_D}{R_C} = -3 = -2.66 \quad (\text{slight})$$

by model



$$V_o = -g_m V_{ds} \cdot R_d. \quad (\because r_d \text{ not given})$$

$$V_i = V_{gs} + g_m V_{ds} \cdot R_s.$$

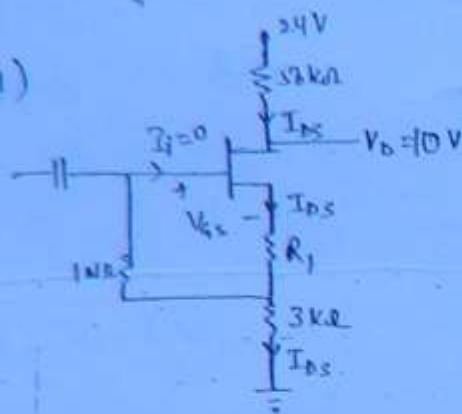
$$A_V = \frac{V_o}{V_i} = \frac{-g_m R_d}{1 + g_m R_s} = \frac{-4 \times 6}{1 + 4 \times 2} = -2.6$$

on solving,

$$I_D = 2.26 \text{ mA}$$

Conventional :

Soln (1)



Assuming FET is in saturation

$$V_{GS} + \frac{1}{2} k_F V_D = I_{DS} \cdot R_1 = 0$$

$$I_{DS} = \frac{24 - 10}{5k} = 1 \text{ mA}$$

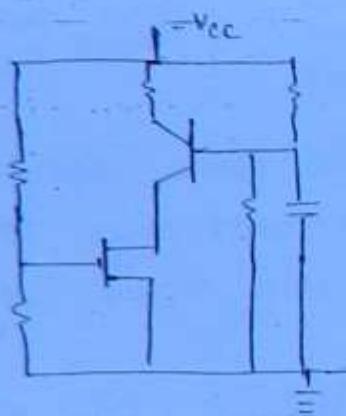
$$I_{DS} = \frac{200 \times 2}{1} \left( 1 - \frac{k_F}{(1)} \right)^2$$

$$\Rightarrow 0.25 = 2 \left( 1 + V_{GS} \right)^2 \Rightarrow V_{GS} = \frac{1}{2\sqrt{2}} - 1$$

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Chapter 3 :

Ques-2 :



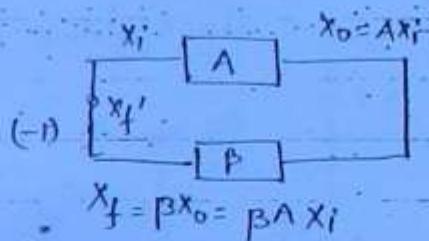
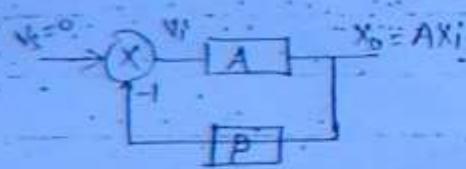
$$g_m = \frac{\beta}{1+\beta} \cdot g_{mF}$$

$$= \frac{99}{100} \times 2$$

$$= 1.98 \text{ mA/V}$$

1st Sem., 2012

Oscillators (Sinusoidal)



$$X_f' = -PA X_i$$

$$\rightarrow \text{loop gain} = \frac{X_f'}{X_i} = -AP$$

X<sub>f</sub>' = X<sub>i</sub> then there is

$\rightarrow$  If finite o/p w/o any i/p.; ckt acts as oscillator

$\therefore$  Loop gain = Y = (-AP) = 1  $\rightarrow$  Barkhausen Criterion

Phase shift  $\phi = 0, 360^\circ$  or  $2n\pi$ .

$\rightarrow | \text{loop gain} | = AP = 1$

Now,  $A_f = \frac{A}{1+Ap}$ . for system satisfying Barkhausen criteria - (2) 7

$$A_f = \frac{A}{1-1} = \infty.$$

Barkhausen Criterion :- It states that -

- 1) Total phase shift around a loop as signal proceeds from i/p through amplifier, feedback now and back to i/p again, completing a loop is multiple integral of  $2\pi$ , ie,

$$\boxed{\phi = 2n\pi} ; n=0, 1, 2, \dots$$

- 2) The magnitude of product of open loop gain of amplifier, A and feedback factor  $\beta$  is unity.

$$\boxed{|Ap| = 1.}$$

Practical Consideration :-

Practically magnitude of loop gain, ie,  $|Ap|$  should be kept slightly greater than unity. Then amplitude of oscillation is controlled by onset of non-linearity present in system, in other words, in a practical oscillator, loop gain is kept slightly greater than one to overcome the circuits internal losses.

Oscillators :-

→ Oscillator is basically a waveform generator, used in designing of signal generator and function generators.

→ It is also defined as an amplifier with  $\infty$  gain.

Amplifier

- 1) Gain is finite  
2) Negative feedback  
3) Excellent stability

Oscillator

- 1) Gain is  $\infty$ .  
2) Positive feedback.  
3) less stable.

→ External i/p signal is compulsory

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→ External i/p signal is not reqd.  
i/p signal will be noise.

Note:

→ An amplifier can be converted into an oscillator by applying +ve feedback & increasing the gain to  $\infty$ .

→

### Oscillators

#### AF oscillator

$f_0 \rightarrow 30\text{Hz}$  to  $20\text{kHz}$

RC phase shift ose.      Wein Bridge oscillator

- By using Op-Amp
- By using FET
- By using BJT

#### RF oscillator

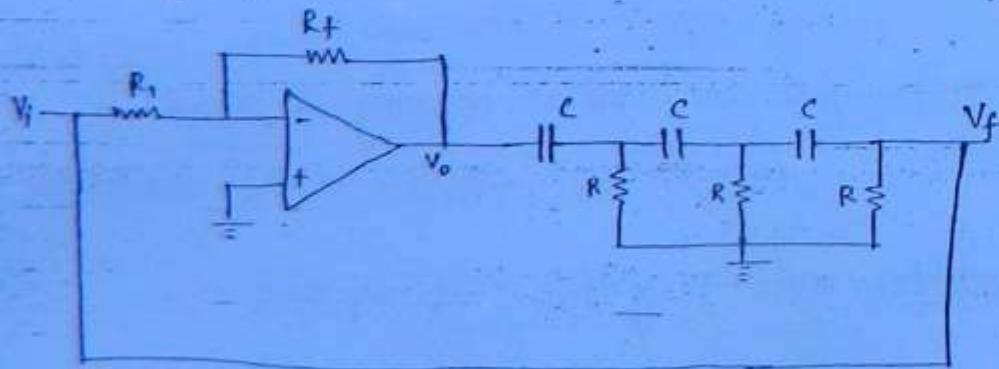
$f_0 > 20\text{kHz}$

Hartley  
Colpitt  
Clapp

Crystal oscillator

### RC Phase Shift Oscillator

→ By using Op-Amp



→ If  $V_f = V_i$ ; circuit acts as an oscillator.

→ feedback mechanism:— Voltage series

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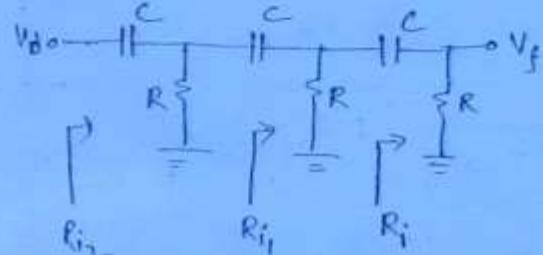
$$\phi = -\tan^{-1}(\omega RC)$$

$$\phi = \tan^{-1}(\frac{1}{\omega RC})$$

→ Preferable as lower values of  $R, C$  are required to maintain higher phase shift.

→ To get the overall gain equal to  $180^\circ$ , the phase shift is distributed among all the stages.

→ In this RC phase shifter, three stages are added but the individual phase shift of each stage is not  $60^\circ$ . This is due to loading effect.

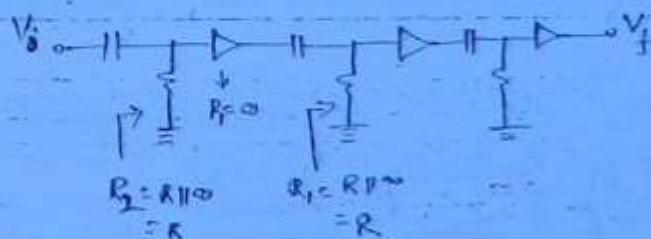


$$\rightarrow \beta = V_f / V_o$$

$$\begin{aligned} R_{i1} &= R_i || R < R \\ R_{i2} &= R || R_{i1} < R. \end{aligned} \quad \left. \begin{array}{l} \text{Hence, phaseshift of} \\ \text{individual stage will} \\ \text{be } > 60^\circ \text{ in this case} \\ \text{and hence, overall } \phi > 180^\circ. \end{array} \right\}$$

→ To calculate set the overall  $\phi = 180^\circ$ , calculate  $V_f$  and set imaginary part equal to 0 and set value of  $R, C$  for given  $\omega$  such that real part  $\alpha$  is -ve. In this way, total  $\phi = 180^\circ$  but individual  $\phi$  of stages is not known.

→ We can use buffer in b/w the stages to prevent loading effect but not used due to its complexity.



Voltage follower = Buffer.

→ freq. of oscillation:

$$P = \frac{V_f}{V_o} = X + jY \quad \text{On putting } Y = 0 -$$

$$\rightarrow \text{freq of oscillation} ; \quad f_0 = \frac{\omega_0}{2\pi} = \frac{1}{2\pi RC \sqrt{G}}$$

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Substituting  $f_0$  in  $\beta$  —

$$\boxed{\beta = X = -\frac{1}{29}} \quad \Rightarrow \text{-ve real part} \Rightarrow 180^\circ \text{ phase shift}$$

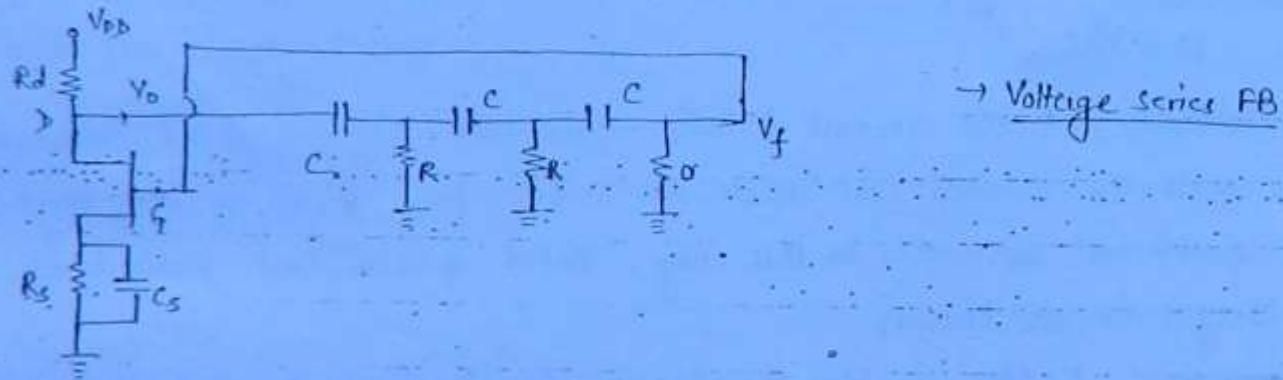
$$\rightarrow \text{Condition for oscillation} - \quad |A\beta| = 1 \\ \Rightarrow |A| = 29$$

$$\text{for inverting op-amp,} \quad A = -\frac{R_f}{R_i}$$

$$\Rightarrow \frac{R_f}{R_i} = 29 \Rightarrow \boxed{R_f = 29 R_i} \quad * \underset{\text{Imp.}}{\text{only slightly}} \geq 29 R_i$$

$$\text{Practically, } |A\beta| \geq 1 \Rightarrow \boxed{R_f \geq 29 R_i} \quad \left. \begin{array}{l} \text{only slightly} \geq 29 R_i \\ \end{array} \right\}$$

By Using FET :-



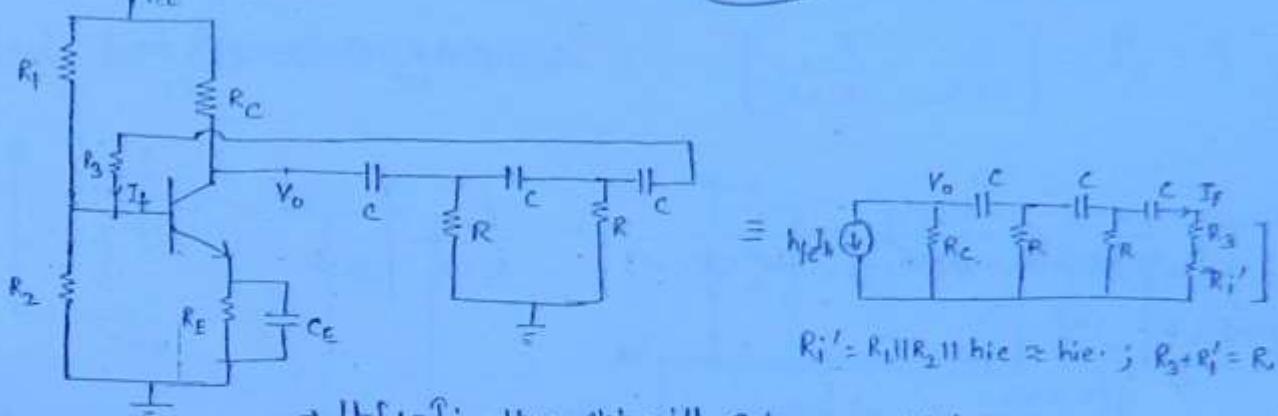
$$\rightarrow \text{Condition for oscillation} : \quad |A\beta| = 1$$

$$\rightarrow A_y = -\frac{V_{ds}}{V_{gs}} = \mu$$

$$\Rightarrow \boxed{\mu = 29} \rightarrow \text{Amplification factor; Practically, } \boxed{\mu \geq 29}$$

By using BJT :-

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$\rightarrow$  If  $I_f = I_i$ , then ckt will act as oscillator.

→ Type of feedback - Voltage Shunt

$$\rightarrow f_o = \frac{\omega_0}{2\pi} = \frac{1}{2\pi R C \sqrt{4K+6}} ; K = \frac{R_c}{R}$$

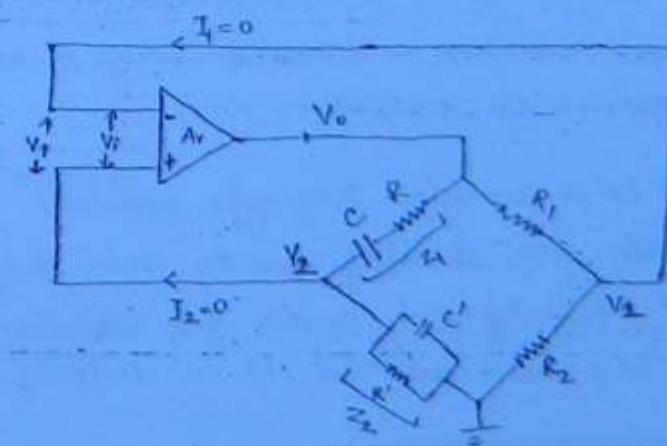
$$\rightarrow \text{Putting } |A_{pl}|=1 ; \quad h_{fe} = 4K + 23 + \frac{29}{K}$$

$$\rightarrow \text{Diff. w.r.t. } K, \quad \frac{dh_{fe}}{dK} = 0 \Rightarrow K \approx 2.7 ; \quad h_{femin} = 44.54$$

- Note
- An FET with  $\mu < 29$  cannot be used in RC phase shift oscillator.
  - An Tr with small signal CE short ckt current gain, i.e.,  $h_{fe}$  less than 44.54 cannot be used in this oscillator.
  - RC phase shift oscillator is considered as a fixed freq. oscillator since to change  $f_o$ , we have to change value of  $R$  &  $C$  of all three sections simultaneously, but this is practically very difficult.

Wein Bridge Oscillator :-

If  $V_i = V_f$ , then ckt acts as an oscillator.



$$\rightarrow V_f = V_2 - V_1 ; \Rightarrow V_f = \frac{Z_2}{Z_1 + Z_2} V_0 - \frac{R_2}{R_1 + R_2} V_0$$

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$$\therefore \beta = \frac{V_f}{V_0} = \left[ \frac{Z_2}{Z_1 + Z_2} - \frac{R_2}{R_1 + R_2} \right] ; Z_2 = R' \parallel \frac{1}{sC'} ; Z_1 = R + \frac{1}{sC}$$

$$\Rightarrow \beta = \left[ \frac{\omega R' C}{\omega (RC + R'C + R'C') - j(1 - \omega^2 RR'C'C')} - \frac{R_2}{R_1 + R_2} \right]$$

Imp. part = 0

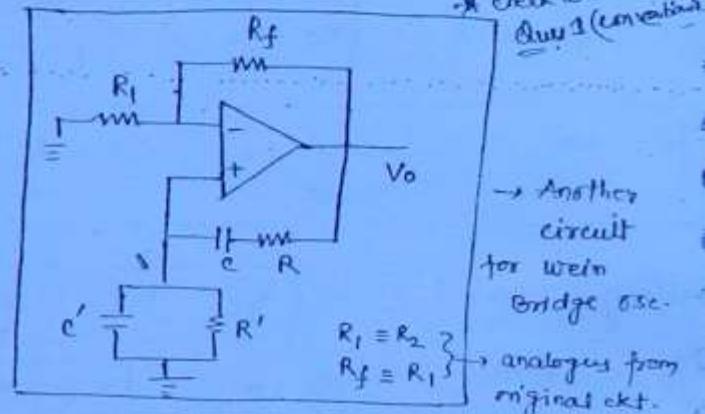
$$\Rightarrow f_0 = \frac{\omega_0}{2\pi} = \frac{1}{2\pi \sqrt{RR'C'C'}} \quad \text{--- Imp}$$

Putting  $\omega_0$  in  $\beta$  —

$$\beta = \left[ \frac{R'C}{RC + R'C + R'C'} - \frac{R_2}{R_1 + R_2} \right]$$

Condition for oscillation —  $|Av\beta| = 1$

$$\Rightarrow \beta = \frac{1}{|Av|} = \frac{1}{\infty} = 0$$



→ Another circuit for Wein Bridge osc.

→ analogous from original ckt.

$$\Rightarrow \left[ \frac{R'C}{RC + R'C + R'C'} - \frac{R_2}{R_1 + R_2} \right] = 0 \quad \text{--- Imp}$$

If  $R = R'$ ,  $C = C'$  —

$$f_0 = \frac{1}{2\pi RC} \quad \text{obj.}$$

$$\text{Condition: } \frac{1}{3} - \frac{R_2}{R_1 + R_2} = 0 \Rightarrow R_1 = 2R_2. \quad \text{obj.}$$

→ An oscillator circuit in which a balanced bridge is used as a feedback n/w is called Wein bridge oscillator.

→ Advantages: It is a variable freq. type oscillator

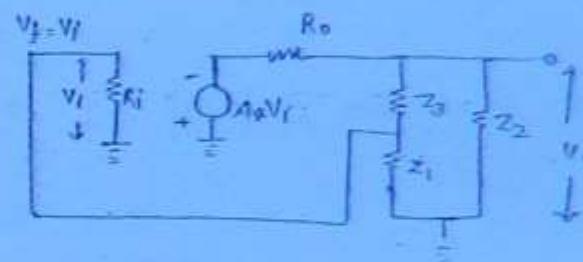
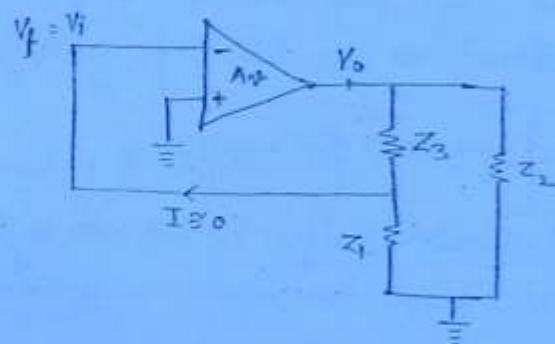
- Better freq. stability due to Wein bridge.

→ Application — 1) Popularly used audio freq. oscillator  
2) As a master oscillator ckt in designing of signal generator

## RF oscillator :-

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General form of oscillator circuit :-



$$\rightarrow z_L = (z_1 + z_3) \parallel z_2 \quad \left\{ \because T \approx 0 \right\} \quad \text{--- (1)}$$

$$\rightarrow z_1, z_2 \text{ & } z_3 \text{ are all reactive} ; \quad z_1 = jx_1 ; \quad z_2 = jx_2 ; \quad z_3 = jx_3 \quad \text{--- (2)}$$

$$\rightarrow V_f = \frac{z_1}{z_1 + z_3} \cdot V_0 \quad \Rightarrow \quad \beta = \frac{V_f}{V_0} = \frac{z_1}{z_1 + z_3} \quad \text{--- (3)}$$

$$\rightarrow \text{Overall gain, } Av = \frac{V_0}{V_i}$$

$$\text{From equivalent ckt, } \quad V_0 = - \frac{Av \cdot V_i \cdot z_L}{R_o + z_L}$$

$$\Rightarrow Av = - \frac{\beta \cdot z_L}{R_o + z_L}$$

$$\begin{aligned} \text{Now, } Av\beta &= - \frac{Av \cdot z_L}{R_o + z_L} \times \frac{z_1}{z_1 + z_3} \\ &= - \frac{Av \cdot z_1 z_2}{R_o(z_1 + z_2 + z_3) + z_2(z_1 + z_3)} \end{aligned}$$

$$\left. \begin{aligned} &\text{on putting } z_L = \frac{z_2(z_1 + z_3)}{z_1 + z_2 + z_3} \end{aligned} \right\}$$

On substituting from eqn (2). -

$$Av\beta = \frac{Av \cdot x_1 x_2}{j R_o (x_1 + x_2 + x_3) - x_2(x_1 + x_3)}$$

for freq. oscillation,  $\Im m = 0$

$$\Rightarrow \boxed{x_1 + x_2 + x_3 = 0}$$

Now, on substituting in  $\beta$  -

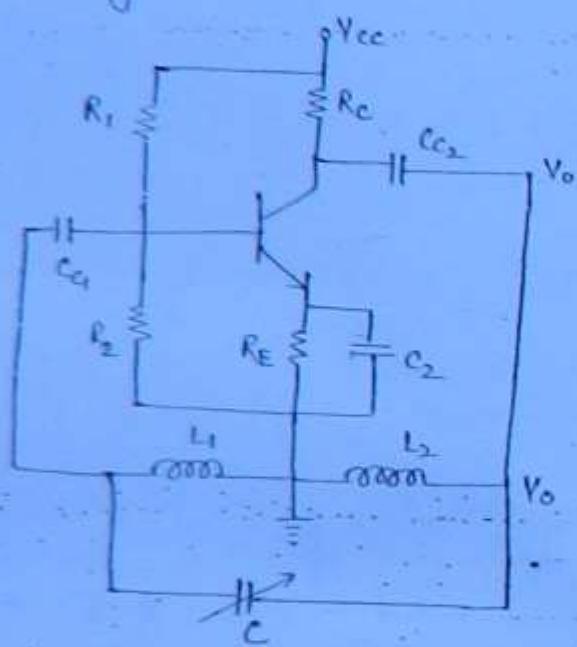
$$A_v \beta = \frac{A_v x_1 x_2}{-x_2(x_1+x_3)} = \frac{-A_v x_1}{(x_1+x_3)}$$

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$$\Rightarrow A_v \beta = \frac{A_v x_1}{x_2}$$

Now, for oscillation,  $|A_v \beta| > 1 \Rightarrow \boxed{A_v \geq \frac{x_2}{x_1}}$   $\rightarrow$  cond'n for oscillation

### Hartley Oscillator:



$$z_1 = j\omega L_1, z_2 = j\omega L_2, z_3 = -j\frac{1}{\omega C}$$

Freq. of oscillation :-

$$x_1 + x_2 + x_3 = 0$$

$$\Rightarrow \omega L_1 + \omega L_2 - \frac{1}{\omega C} = 0$$

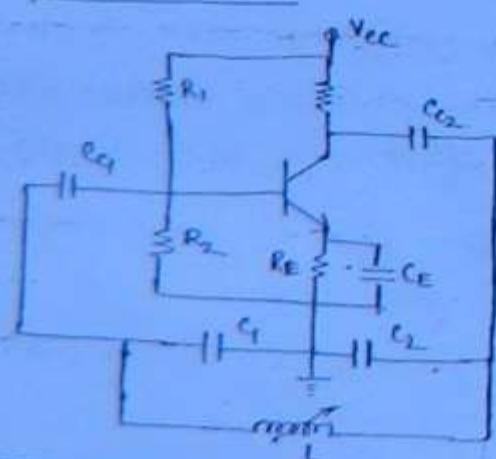
$$\Rightarrow \omega = 2\pi f = \frac{1}{\sqrt{(L_1+L_2)C}}$$

$$\Rightarrow f = \frac{1}{2\pi\sqrt{(L_1+L_2)C}}$$

Condition for oscillation :-

$$A_v \geq \frac{x_2}{x_1} \Rightarrow \boxed{A_v \geq \frac{L_2}{L_1}}$$

### Colpitt Oscillator:



$$z_1 = -j/\omega C_1, z_2 = -j/\omega C_2, z_3 = j\omega L$$

$$x_1 + x_2 + x_3 = 0$$

$$\Rightarrow \omega = \frac{1}{\sqrt{L \cdot \frac{C_1 C_2}{C_1 + C_2}}} ; f = \frac{1}{2\pi \sqrt{L \cdot \frac{C_1 C_2}{C_1 + C_2}}}$$

Condition:-  $A_V \geq \frac{X_1}{X_2} \Rightarrow A_{V2} \geq \frac{C_1}{C_2}$  — Imp 225

Common Points :-

- They are variable freq. type RF oscillator
- Working principle is for parallel resonance.

### Hartley Oscillator

→ It is also called Tapped inductor type oscillator

Advantage :- Capacitive tuning, ie., no wear & tear problem.

Disadvantage :- Bulky & expensive because of two inductors.

Applications :- i) In designing of local oscillator ckt in receiver.

### Colpitt Oscillator

→ It has better freq. stability and it is obtained by reducing net capacitance of modified tank ckt.

$$C_{eq} = \frac{C_1 C_2}{C_1 + C_2}; Q = \frac{1}{\omega R C}; \text{ as } C \downarrow, Q \uparrow \rightarrow \text{stability} \uparrow$$

Advantage :-

- It is smaller in size and economical.

Disadvantage :- Inductive Tuning, ie., wear & tear problem.

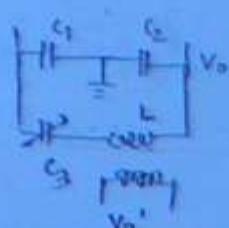
Application :- i) As a local oscillator in receiver.

### Clapp Oscillator

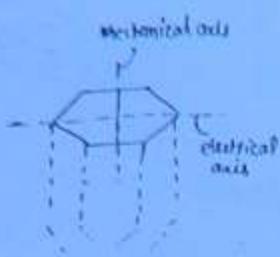
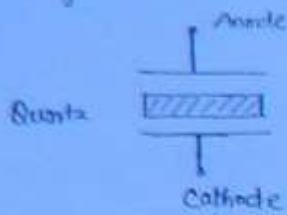
It is a modification of colpitt osc. where variable inductor is replaced by a variable capacitor  $C_3$  in series with an inductor  $L$  and  $\omega_p$  is inductively obtained.

→ Working principle is series resonance, therefore

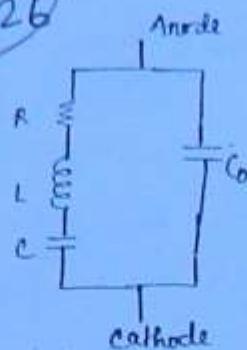
$$\omega_0 = \frac{1}{2\pi\sqrt{LC_3}}$$



## Crystal oscillator :-



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AC equivalent circuit

f → internal losses or viscous damping

L → Mass of crystal

C → Stiffness =  $\frac{1}{\text{spring constant}}$ ; C<sub>0</sub> = capacitance b/w anode & cathode plate.

Series resonance :- Due to RLC in series ,

→ Impedance  $\Rightarrow$  minimum.

$$f_s = \frac{1}{2\pi\sqrt{LC}}$$

Parallel Resonance :-

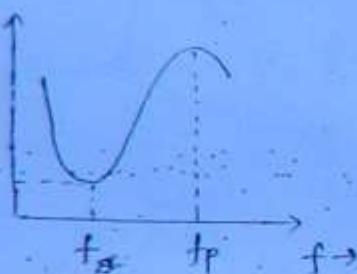
→ Impedance maximum

$$f_p = \frac{1}{2\pi\sqrt{LC_{eq}}}; C_{eq} = \frac{C C_0}{C + C_0}$$

→  $f_p > f_s$ , and freq. of oscillator varies b/w f<sub>s</sub> and f<sub>p</sub>.

frequency of oscillation

→ Hz



→ f<sub>0</sub> depends of l, b, t → Physical dimensions

$$f_0 \propto \frac{1}{\text{thickness}}$$

→ on higher frequencies, crystal becomes weak.

→ It is a fixed frequency type RF oscillator.

→ It works on principle of piezoelectric effect.

→ It has two resonating freq., i.e., f<sub>s</sub> & f<sub>p</sub>. Oscillating frequency lies b/w f<sub>s</sub> & f<sub>p</sub>.

→ Due to high quality factor Q of a resonance ckt, it provides very good freq. stability.

→ freq. of oscillation, generated by crystal depends on its physical dimensions but mainly on thickness.

→ On high freq., t should be small but it makes crystal mechanically weak.

① Advantage:-

- ② - Excellent freq. stability
- ③ - Simplest RF oscillator ckt.

④ Disadvantage:-

- ⑤ - Fixed freq. type oscillator.

⑥ Application :- i) To generate carrier in AM & FM transmission.

⑦ ii) In designing of timer circuit.

⑧ Frequency Stability -

⑨ - freq. stability of an oscillator is measure of its ability to maintain as nearly a fixed freq. as possible over as long a time interval as possible.

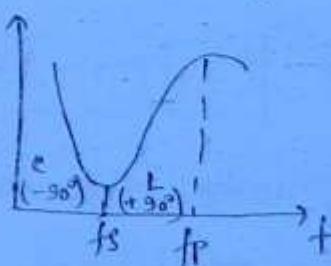
⑩ - If  $d\theta$  is small change in phase angle and corresponding freq. change is  $df$ , then

$$\frac{d\theta}{df} = \text{figure of merit} \quad \text{and its value should be high.}$$

⑪ - Ideally,  $\frac{d\theta}{df} = \infty$ .

(resistive  $\beta_{HW}$ )

⑫ - Inverting op-amp is preferred as compared to non-inverting as it has  $\beta_{HW}$  which is adaptable due to freq. change, ie, when  $\phi$  changes due to temp. variations,  $\beta_{HW}$  will adjust ~~the~~ its phase so that overall change in freq. is very small.



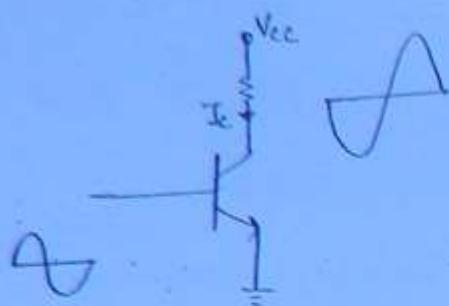
$$df = f_s^+ - (f_s^-)$$

$$d\theta = 90^\circ - (-90^\circ) = 180^\circ$$

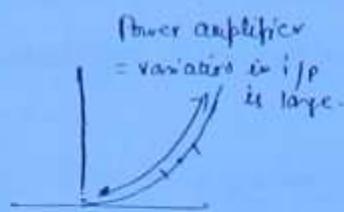
$$\frac{d\theta}{df} = \frac{180^\circ}{f_s^+ - f_s^-} = \frac{180^\circ}{0} = \infty \quad (\text{ideal}) \quad \text{for crystal oscillator}$$

## Power Amplifiers :-

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$$P_{dc} = V_{dc} \cdot I_{dc} = V_{dc} \cdot I_{ca}$$



- 1) It is last stage in multistage amplifier.
- 2) Power amplification is defined as ability of amplifier to convert available o/p dc power into ac signal power with the application of i/p signal.

Small signal Amp.

→ i/p signal amplitudes are very small ( $\mu V$  or  $mV$ )

→ operated only in linear region

→ Important specifications are-

$A_f, A_v, R_f, R_o, \phi$

→ Analysis of amp. will be done by using graphical as well as mathematical analysis

→ Transistors used in power amp. are called power tri.

→ Power amplifiers are designed mostly by BJT & they are generally in CE mode

Harmonic Distortion :-

→ In a power amp., signal amplitudes are very large, hence signal is

Large signal Amp.

→ i/p signal amplitudes are very large, ( $\geq 1V$ )

→ operated both in linear & nonlinear region of i/p charc. curve.

→ Important specifications are -

- power conversion efficiency,
- dc power available at o/p
- ac " "
- distortions at o/p

- by only graphical analysis.

operated in linear & non-linear portion of i/p charac. curve, so we get harmonics in o/p & harmonic distortion is present at o/p.  
— Harmonic distortion is a non-linear distortion.

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fourier series expan of collector current of power transistor :-

$$\rightarrow i_C = \underbrace{I_{C0}}_{DC} + \underbrace{B_1 \cos \omega t}_{\text{fundamental}} + \underbrace{B_2 \cos 2\omega t + \dots}_{\text{Harmonics}} \rightarrow \omega \uparrow, \text{Amplitude} \downarrow.$$

$$\rightarrow 2^{\text{nd}} \text{ Harmonic distortion} \rightarrow D_2 = \left| \frac{B_2}{B_1} \right|$$

$$\rightarrow 3^{\text{rd}} \text{ } \dots \dots \rightarrow D_3 = \left| \frac{B_3}{B_1} \right|$$

$\rightarrow$  AC power o/p due to fundamental component

$$P_{AC} = I_{m0}^2 \cdot R_o = \left( \frac{B_1}{2} \right)^2 \cdot R_o \quad \left\{ = P_1 \right\}$$

$\rightarrow$  Total Harmonic Power (THP) -

$$P_T = \frac{B_1^2}{2} \cdot R_o + \frac{B_2^2}{2} \cdot R_o + \dots$$

$$\Rightarrow P_T = \frac{B_1^2}{2} \cdot R_o \left[ 1 + \left( \frac{B_2}{B_1} \right)^2 + \left( \frac{B_3}{B_1} \right)^2 + \dots \right]$$

$$\Rightarrow P_T = P_1 \left[ 1 + D_2^2 + D_3^2 + \dots \right]$$

Total Harmonic Distortion (THD) -

$$D = \sqrt{D_2^2 + D_3^2 + \dots}$$

$$\therefore P_T = P_1 \left( 1 + D^2 \right)^{\frac{1}{2}} \quad (\text{mp})$$

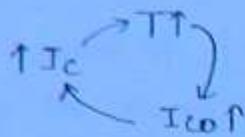
for THD = 10%,  $D = 0.1$

$$\therefore P_T = P_1 (1+0.01) = 1.01 P_1$$

$\Rightarrow P_T \approx P_1$ ; ie, if THD is kept  $\leq 10\%$ , then THP is almost equal to fundamental power.

$\frac{dT_j}{dt} \rightarrow$  rate of heat dissipation

### Thermal Runaway :-



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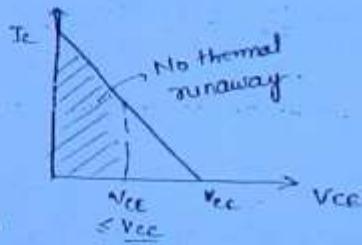
- The process where a transistor is subjected to self destruction due to excess heat produced in CB junction
- It is due to  $I_{CO}$ .
- BJT suffers from thermal runaway. In FET there is no thermal runaway.

### Condition to eliminate thermal runaway:-

$$\text{At } T_j \text{ is so selected that } V_{CE} \leq \frac{V_{CC}}{2}$$

$$\frac{dP_c}{dT_j} \leq \frac{1}{\theta}$$

$P_c = \text{max. collector power dissipation in W.}$   
 $T_j = \text{junct. temp. at collector junc.}$   
 $\theta = \text{Thermal resistivity in } {}^\circ\text{C/watts.}$   
 $\rightarrow T_j - T_A \propto P_D$  (θ should be small)  
 $\rightarrow T_j - T_A = \theta P_D$        $T_A = \text{ambient temp.}$   
(1)



→ Area of collector  $\uparrow$ ,  $\theta \downarrow$

→ Diff. (1) w.r.t.  $T_j$  —

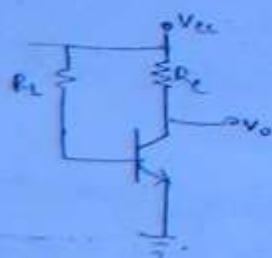
$$1 = \theta = \theta \frac{dP_D}{dT_j}$$

Rate of heat dissipation in atmosphere

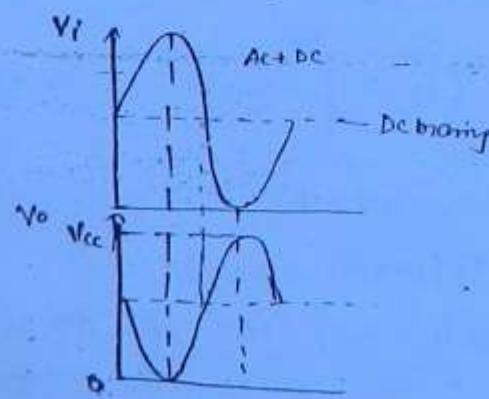
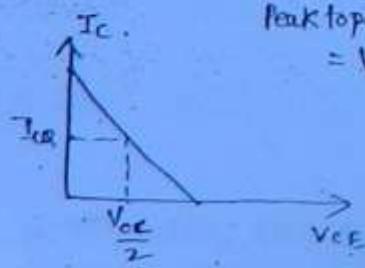
$$\frac{dP_D}{dT_j} = \frac{1}{\theta}$$

### Classifications of Amplifiers :-

#### Class A operation-



Cond'ng angle =  $2\pi$   
Peak-to-peak  
 $= V_{CC}$



$$\eta = \frac{\text{AC Power}}{\text{DC Power}}$$

- Collector current flows for entire  $360^\circ$  of i/p signal; conduct angle =  $2\pi$   
 → Q point is located at centre of dc load line

(23)

Advantage :- → Minimum distortion

→ Excellent thermal stability, ie, no thermal runaway problem

Disadvantage :- - Small power conversion efficiency.

- Reduced power gain, - Introduces power drain -

When signal is not applied, transistor is consuming max. power & it is called power drain. When signal is applied, tr. is using less power.

Application :- designing of audio pre-amp.

Note - Class A amplifier is always designed with a single amplifier, ie, single ended, ie, one Tr. per stage.

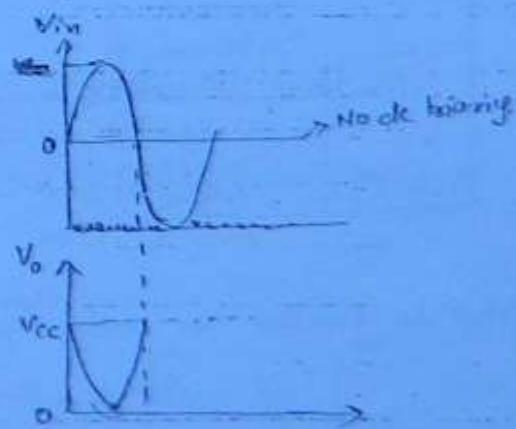
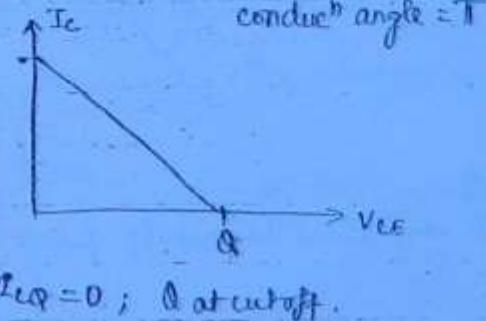
→ Power rating of transformer  $P_D(\text{max})$  → maximum allowable heat dissipation, is defined at room temp, ie,  $25^\circ\text{C}$ .

→ In class A operation, power dissipated by Tr is equal to max. signal power o/p.

→ For class A,  $P_D = P_{D(\text{max})}$ ; ie, max. power o/p.

e.g. To design a class A amp. with  $20\text{W}$  o/p signal power, Tr must dissipate  $20\text{W}$  of power.

Class B operation :-



Date \_\_\_\_\_

Page No. \_\_\_\_\_

amplifier  
— for class B amplifier  
—  $\Rightarrow 2V_{cc}$

- collector current flows exactly for  $180^\circ$  of i/p signal
- Q point is located at cutoff
- It is a double ended amplifier, i.e. two transistor in one stage.

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Advantage :- Higher efficiency. ( $78.5\%$ )

- Power drain is eliminated.

Disadvantage :- Higher distortion

| - Thermal stability is less.

- Introduces crossover distortion (CD)  $\rightarrow$  major disadvantage.

Application :- Used in designing of Power amp., for ex., push-pull power amp., complementary symmetry push pull power amp.

When signal is applied, Tr is consuming power & when signal is absent, Tr will not consume any power, therefore no power drain.

Power dissipated by single Tr. in ckt,

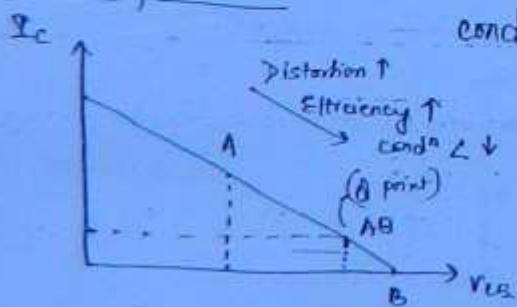
$$P_D = 0.2 P_{omax} \quad P_{omax} = \text{max. o/p signal power.}$$

Power dissipated by circuit. i.e. by two tr.

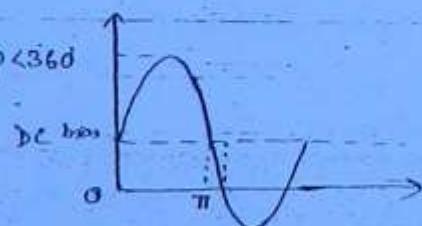
$$P_D = 0.4 P_{omax}.$$

for eg; to design a class B amplifier, with  $20W$  o/p signal power, power dissipated by single transistor should be  $4W$ .

Class AB operation :-



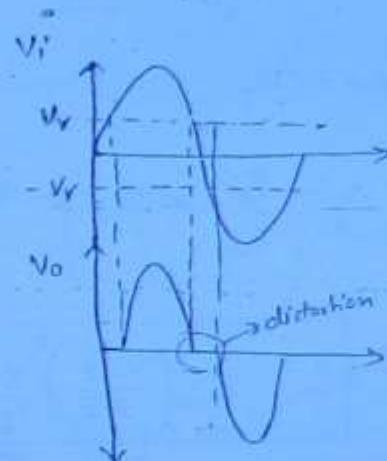
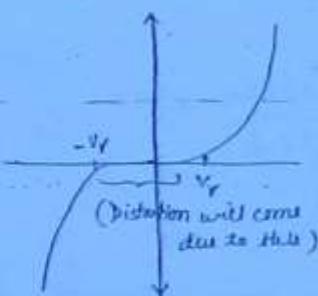
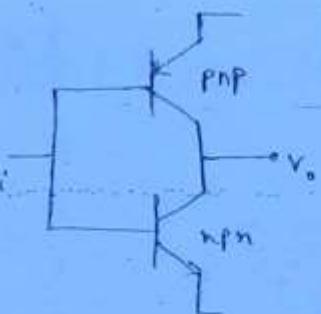
condn angle -  $180^\circ < \phi < 360^\circ$



→ Q point is located in active region but very close to cutoff point.

- Distortion & noise interferences is more as compared to class A & B when compared to class B.
- It is used in power amp for ex. push pull power amp. (23)
- The main advantage of class AB operation is it eliminates CDS.
- Max. efficiency is approx. 60%.

### Crossover Distortion :-

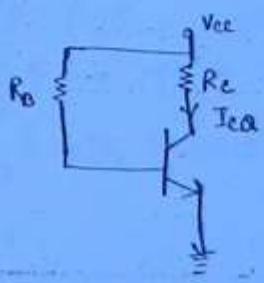


- It is a distortion arising when conduction transfer from one tr. to other.
- It is a non-linear distortion.
- It is due to operating the signal over non-linear characteristic curve.
- Class B introduce CDS. and class AB eliminates CDS.
- The most suitable remedy to minimise CDS is to use Ge Tr. in place of Si Tr. - But this will reduce power handling capability of circuit.

12/07/2012

### Class A amplifier :-

### Direct Coupled Amplifier :-



$$I_{CQ} = \frac{I_{max}}{2} = \frac{V_{cc}}{2R_c} \quad (\because Q \text{ point is in centre})$$

$$\underline{\text{DC Power}} \dots P_{dc} = V_{dc} \cdot I_{dc}$$

$$\Rightarrow P_{dc} = V_{cc} \cdot I_{CQ} \Rightarrow P_{dc} = \frac{V_{cc}^2}{2R_c}$$

AC power  
RMS

$$P_{AC} = V_{rms} \cdot I_{rms} = \left[ \frac{V_{rms}^2}{R_L} \right]$$

Peak       $P_{AC} = \frac{V_P}{\sqrt{2}} \cdot \frac{I_P}{\sqrt{2}} = \frac{V_P I_P}{2} = \left[ \frac{V_P^2}{2R_L} \right]$

Peak-Peak       $P_{AC} = \frac{V_{P-P}}{2\sqrt{2}} \times \frac{I_{P-P}}{2\sqrt{2}} = \frac{V_{P-P} I_{P-P}}{8} = P_P \left[ \frac{V_{P-P}^2}{8R_L} \right]$

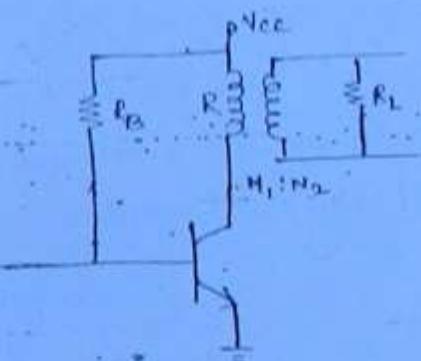
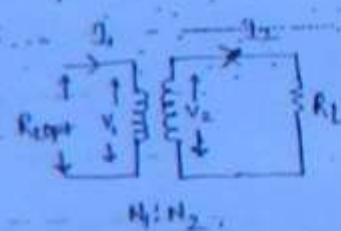
\* Ideally,  $V_{P-P} = V_{cc}$

Efficiency :  $\eta = \frac{P_{AC}}{P_{DC}} \times 100 \Rightarrow \eta = \frac{V_{P-P}^2 / 2R_L}{V_{cc}^2 / 2R_L} \times 100$

$$\Rightarrow \boxed{\eta = \frac{1}{4} \left( \frac{V_{P-P}}{V_{cc}} \right)^2 \times 100 \%}$$

$$\rightarrow \eta_{max} = \frac{1}{4} \times 100 \times \left( \frac{V_{cc}}{V_{cc}} \right)^2 \quad \boxed{\eta_{max} = 25 \% ; \text{ Practically } \Rightarrow 10-15 \%}$$

### Transformer Coupled Amplifier



→ It is used when  
 $R_L$  is very small

$$\frac{V_1}{V_2} = \frac{N_1}{N_2} ; \quad \frac{I_2}{I_1} = \frac{N_2}{N_1} ; \quad R_{opt} = \text{optimum resistance or reflected resistance}$$

$$R_{opt} = \frac{V_1}{I_1} \Rightarrow R_L = \frac{V_2}{I_2} \Rightarrow \frac{R_{opt}}{R_L} = \left( \frac{N_1}{N_2} \right)^2$$

$$\Rightarrow \boxed{R_{opt} = \left( \frac{N_1}{N_2} \right)^2 \times R_L} \quad (\text{Imp})$$

Transformer provides -  
- DC isolation  
-  $R_L$  adjustment

from Graphical analysis :

$$\eta = 50 \times \left( \frac{V_{CEmax} - V_{CEmin}}{V_{CEmax} + V_{CEmin}} \right)^2 \%$$

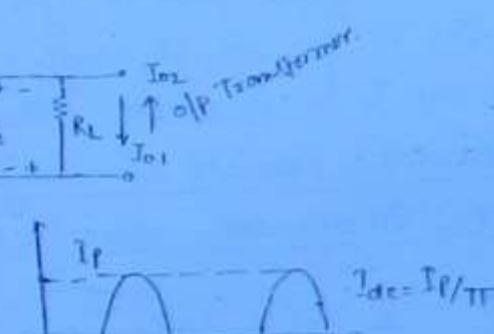
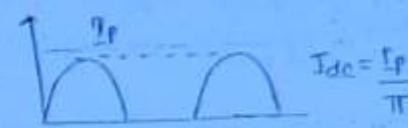
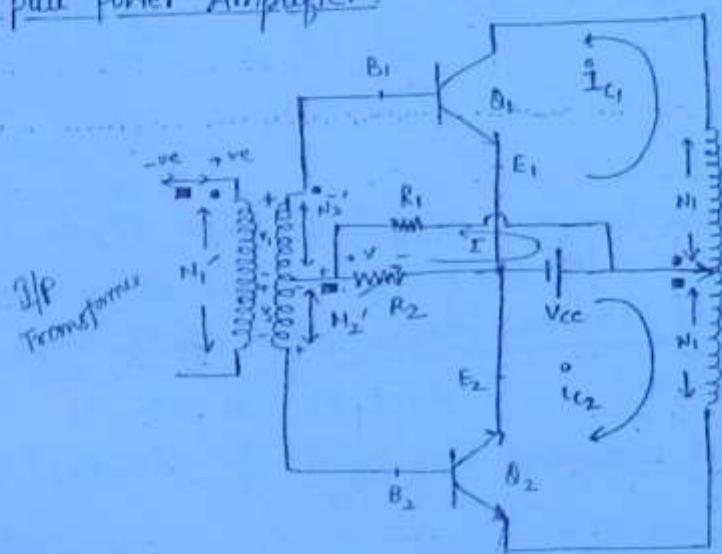
→ ideally,  $V_{CEmin} = 0$

$$\Rightarrow \eta_{max} = 50\%$$

Practically,  $\eta \geq 30-35\%$

(235)

Push-pull power Amplifier:

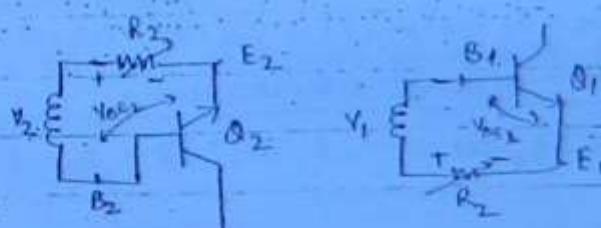


During +ve half,  $Q_1(\text{ON}), Q_2(\text{OFF})$

$$I_{dc} \propto i_{c1}$$

During -ve half,  $Q_1(\text{OFF}), Q_2(\text{ON})$

$$I_{dc} \propto i_{c2}$$



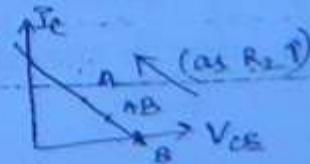
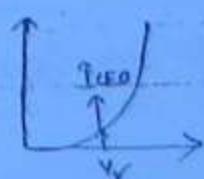
$$\therefore I_{dc} \propto (i_{c1} - i_{c2}) \Rightarrow I_{dc} = K(i_{c1} - i_{c2})$$

When signal is absent -  $V_1 = V_2 = 0$

→ let  $R_2 = 0$ ,  $\Rightarrow V_{BE1} = V_{BE2} = 0 \Rightarrow$  both  $Q_1$  &  $Q_2$  in cutoff  $\rightarrow$  class B operation

→  $R_2 \uparrow \Rightarrow IR_2 \uparrow$  and when  $IR_2 = V_T \therefore V_{BE1} = V_{BE2} = V_T \rightarrow$  class AB operation

→  $R_2 \uparrow, IR_2 \uparrow$  and  $V_{BE} \uparrow$  and  $\theta$  will move towards saturation  $\rightarrow$  class C operation



→  $I_{CEO}$  is the standby current during class AB operation.

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→ It is double ended amp.

→ It can be class B or class AB operated.

→ Designed with identical transformers Tr.

→ The CKT operates in class B when  $R_2 = 0$ .

→ for class AB operation, voltage drop across  $R_2$  is adjusted to be opposite equal to  $V_T$ , where a small standby current flows at zero excitation.

→ The funcn of centre tapped secondary coil of ipf transformer is to provide two equal & opposite voltages  $V_1$  &  $V_2$ .

→  $V_1$  &  $V_2$  are push pull voltages

→ Both the Tr. are in CE mode.

→ When one Tr is in active, other is in cutoff.

→ o/p current consists of only odd harmonic terms since in o/p I, even harmonic terms are cancelled out.

~~Proof:~~  $\Rightarrow \because I_o = K(i_{C1} - i_{C2})^{(6 \text{ marks})}$

$$i_{C1} = B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t + \dots$$

$$i_{C2} = B_0 + B_1 (\cos(\omega t + \pi)) + B_2 \cos 2(\omega t + \pi) + \dots$$

$$= B_0 - B_1 \cos \omega t + B_2 \cos 2\omega t + \dots$$

$$\therefore I_o = 2K(B_1 \cos \omega t + B_3 \cos 3\omega t + B_5 \cos 5\omega t + \dots)$$

→ First available harmonic distortion  $D_3 = \left| \frac{B_3}{B_1} \right|$  (very small)

Note: If  $B_1, B_2, B_3$  are not identical, then even harmonics will be present in o/p & distortion will be large.

Advantage: 1) Higher o/p power due to double ended.

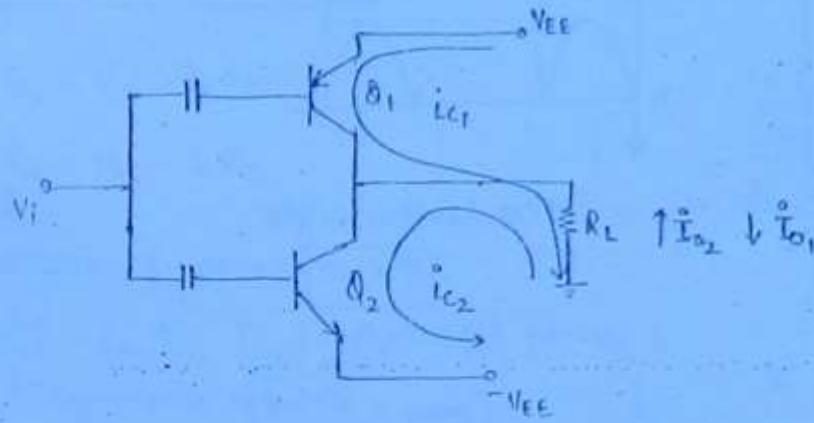
2) " efficiency if class B operated

3) less distortion due to cancellation of even harmonics.

Disadvantage :- Very bulky & highly expensive due to requirement of bulky transformer.

(23)

### Complementary - Symmetry Push Pull Power Amplifier :-



For  $V_i > 0$  -

$$Q_1 = \text{OFF}, Q_2 = \text{ON}, i_{Q_2} = i_{C_2}$$

for  $V_i < 0$  -

$$Q_1 = \text{ON}, Q_2 = \text{OFF}, i_{Q_1} = i_{C_1}$$

- It is double ended amplifier designed with matched pairs of Tr.
- Popularly used Power amp ckt.
- Always class B operated.
- Both Tr. are in CE mode.
- o/p T consists of only odd harmonic terms.

Advantage :- - same as push-pull B amplifier.

- circuit is smaller in size & economical due to elimination of bulky transformer.

Disadvantage :- - Requires two power supply  
- introduces CDP

Efficiency :-  $\eta = \frac{P_{ac}}{P_{dc}} \times 100\%$ .

→ ideally,  $V_p = V_{cc}$ .

$$\therefore P_{ac} = \frac{V_p^2}{2R_L}$$

$$\therefore \eta = \frac{\pi}{4} \times \left( \frac{V_p}{V_{cc}} \right) \times 100\%.$$

when  $Q_1$  on  
↑

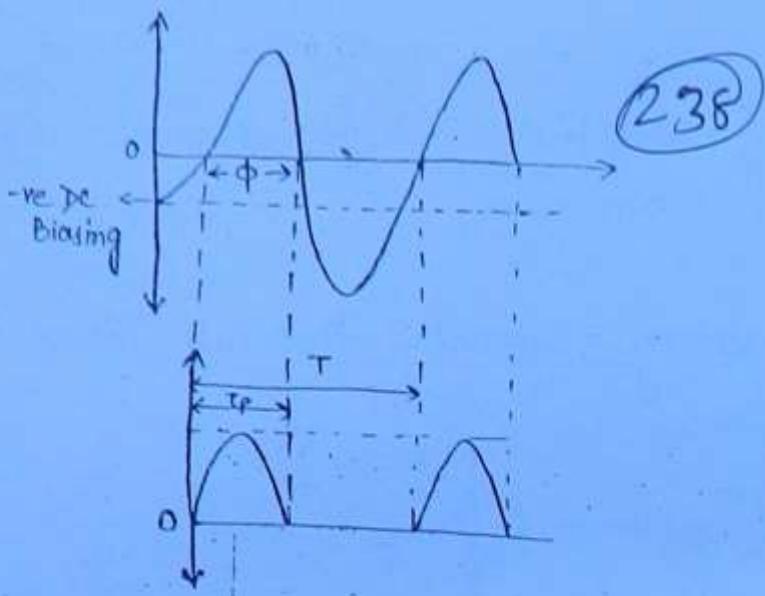
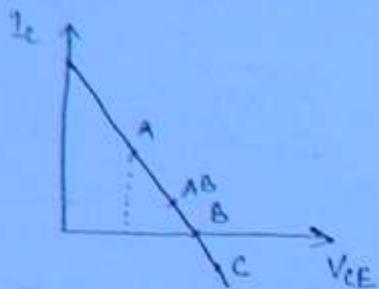
when  $Q_2$  on  
↑

$$P_{dc} = V_{cc} \times \frac{I_p}{\pi} + V_{cc} \times \frac{I_p}{\pi}$$

$$\therefore P_{dc} = \frac{2V_{cc} \cdot I_p}{\pi} = \frac{2V_{cc} \cdot V_p}{\pi R_L} \quad (\text{imp})$$

$$\therefore \eta_{max} = \frac{\pi}{4} \times 100\% \Rightarrow \eta_{max} = 78.5\%$$

### Class C amplifier :-



→ Duty cycle :

$$\frac{\tau_p}{T} \times 100\% = D$$

→ Power dissipation across Tr. during  $\tau_p$  -

$$P_D = V_{CE} \cdot I_c$$

→ conduction angle

$$[\phi < 180^\circ]$$

→ Energy dissipation across Tr during  $\tau_p$  -

$$E_D = P_D \cdot \tau_p$$

→ Efficiency -

$$\eta_{max} = 87.5\%$$

→ Avg. power dissipation during one cycle :-

$$P_{Davg} = \frac{E_D}{T} = \frac{P_D \cdot \tau_p}{T} \Rightarrow P_{Davg} = P_D \cdot D$$

→ Distortion is very high.

### Class D Amplifier:

- They are special amplifier designed to operate with digital pulse signal.
- Efficiency of class D is above 90%.
- It is not a power amplifier.
- Widely used in commercial application.

## Multivibrator by using Transistors :-

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### Bistable Multivibrator -

#### a) Fixed Bias Binary -

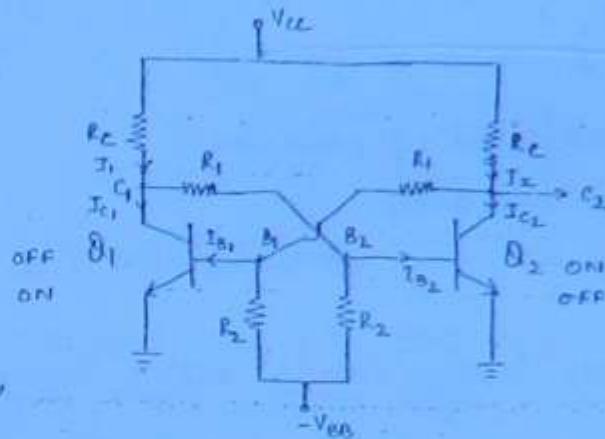
$$V_{C_2} = V_{CC} - I_2 R_C$$

$$V_{C_1} = V_{CC} - I_1 R_C$$

→ Because of noise -

$$\rightarrow V_{B_2} \uparrow, I_{B_2} \uparrow, I_{C_2} \uparrow, I_2 \uparrow, V_{C_2} \downarrow, V_{B_1} \downarrow$$

"Regenerative action"



$$V_{C_1} \uparrow, I_1 \uparrow, I_C \downarrow, I_{B_1} \downarrow$$

→ Finally  $Q_2$  in saturation and  $Q_1$  in cutoff.

$$Q = V_{C_2} = V_{CEsat} = 0 ; \bar{Q} = V_{C_1} \approx V_{CC} = 1$$

When a -ve pulse is applied -

$\left[ \begin{array}{l} \text{at } R_2 - \\ \rightarrow V_{B_2} \downarrow, I_{B_2} \downarrow, I_{C_2}, I_2 \downarrow, V_{C_2} \uparrow, V_{B_1} \uparrow, I_{B_1} \uparrow, I_{C_1}, I_1 \uparrow, V_{C_1} \downarrow \end{array} \right]$

"Regenerative action"

finally  $Q_2$  in cutoff &  $Q_1$  in saturation -

$$Q = V_{C_2} \approx V_{CC} = 1 ; \bar{Q} = V_{C_1} = V_{CEsat} = 0$$

When  $Q_2 = \text{on}$ ;  $Q_1 = \text{off}$  →  $Q_2$  should be well in saturation &  $Q_1$  should be well in cutoff.

$$V_{C_2} = V_{CEsat}, V_{C_1} = \frac{V_{CC} R_1}{R_1 + R_C} + \frac{V_{BESat} R_C}{R_1 + R_C} \approx V_{CC}$$

$$\rightarrow I_0 = I_{B_1} = 0$$

$$\rightarrow V_{B_1} = \frac{V_{CEsat} R_2}{R_1 + R_2} = \frac{V_{BB} \cdot R_1}{R_1 + R_2} ; V_{B_1} \approx 0 \text{ when } V_{BB} = 0.0 \rightarrow \text{Noise Margin}$$

$$V_{B_1} = \text{well in cutoff when } \frac{V_{BB} \text{ is present}}{(Regd)} = \frac{\text{Noise margin}}{\text{margin}} \Rightarrow \frac{0.5}{0.5} = 1.25$$

$$\rightarrow 1.25 (\text{max})$$

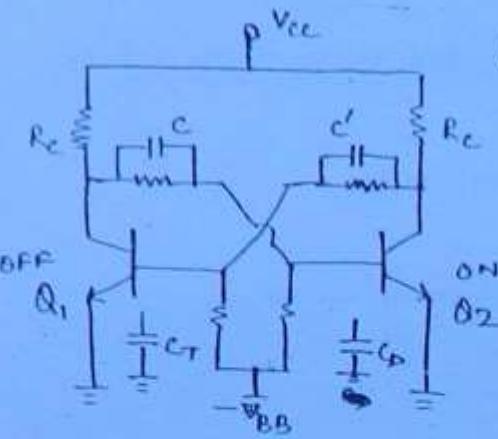
## Commutating capacitors - (c & c')

→  $C_s$  &  $C_T$  are transition & diffusion capacitances of ON & OFF Tr. respectively.

→  $C_s$  &  $C'$  are speed up capacitors.

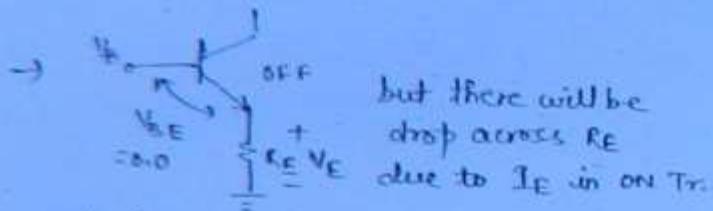
→  $C_s$  &  $C'$  → very small

→ Due to  $C_s$  &  $C'$  → ↓ in transition time or ↓ in propagation delay.



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## Self Biased Binary / Emitter Coupled Binary

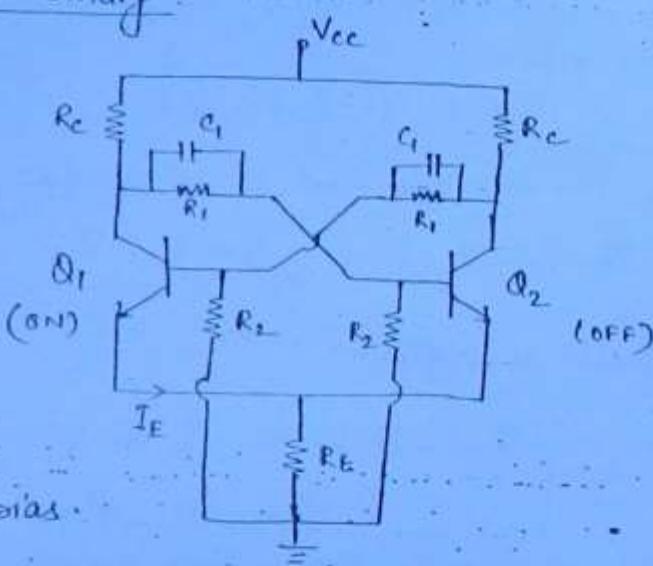


and it min. voltage reqd. to ON

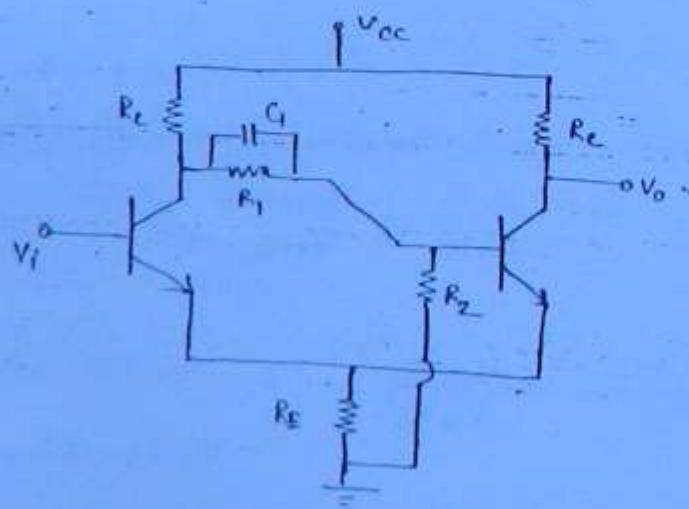
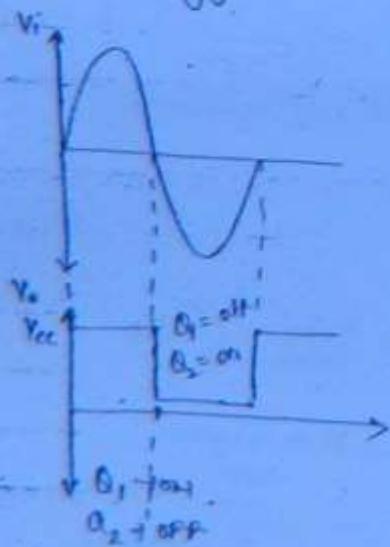
$Q_2$  is atleast  $(V_E + 0.5)$  and hence

$V_{BB}$  is not required in this ckt.

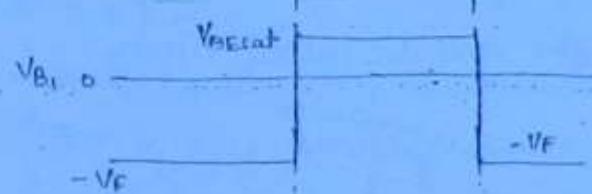
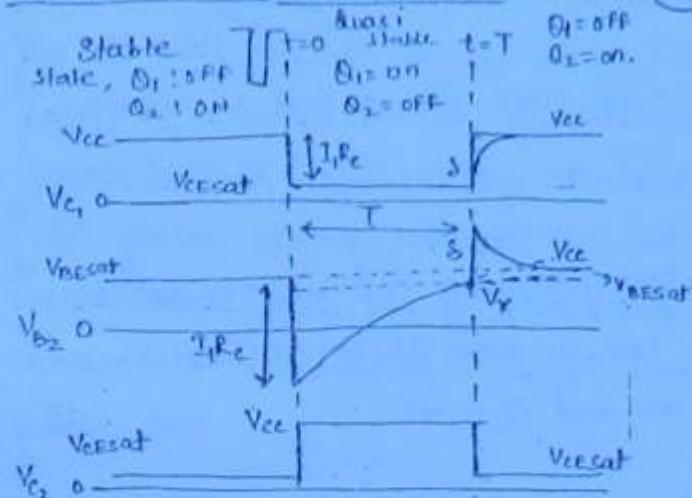
⇒ Other operation is same as fixed bias.



## Schmitt Trigger



## Monostable Multivibrator :-



$$\text{Hence } V_{cc} = V_{c1} = V_{cc}$$

$$\text{and } V_{B1} = \frac{V_{cesat} \cdot R_2}{R_1 + R_2} - \frac{V_{BB} \cdot R_1}{R_1 + R_2} = -V_F$$

→ At  $t \leq 0$ , voltage across  $C$  -

$$V_C = V_{cc} - V_{cesat}$$

$$\frac{V_{c1}}{V_{cc}} = \frac{V_{B2} - V_{cesat}}{V_{cc}}$$

→ For  $t > 0$ . A trigger is applied and  $Q_2: OFF$  &  $Q_1: ON$ .

When  $Q_2 = OFF$ ,  $V_{c2} = V_{cc}$  and it will be transferred to  $B_1$  by commutating capacitor.

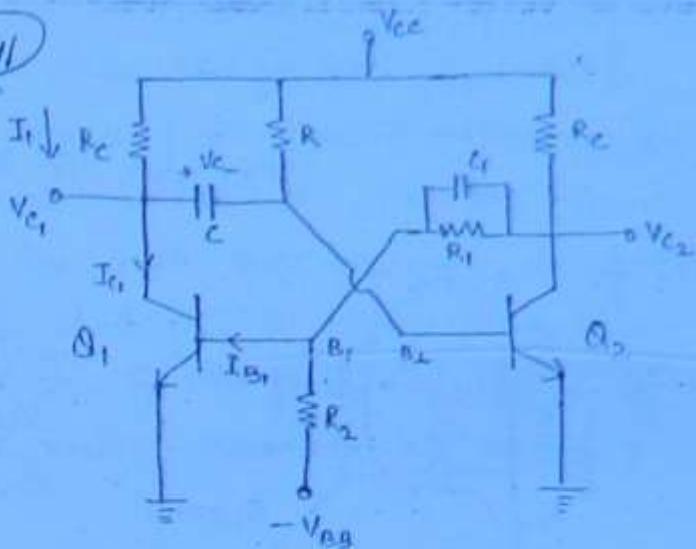
& Now,  $Q_1$  will be on due to this. &  $V_{c1} = V_{cesat}$ .

→ Capacitor  $C$  is called timing element (capacitance) and its value is very large and it does not allow sudden change.

→ A current  $I_1$  will flow in  $Q_1$ -

$$I_1 = \frac{V_{cc} - V_{cesat}}{R_c}$$

(24)



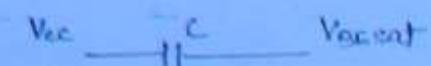
For  $t < 0$  ckt is in stable state

$$Q_1: OFF, Q_2: ON$$

$$V_{c2} = V_{cesat}; V_{B2} = V_{cesat}$$

∴ capacitor will act as open circuit.

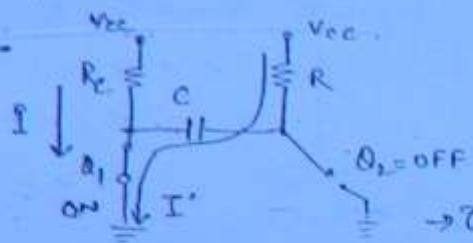
$$I_{c1} = I_{B1} = 0, \therefore \text{current through } R_c = 0$$



(242)

$V_{cesat} = I_s R_1 \Rightarrow V_{ce} < 0$  and  $Q_2$  is well in cutoff.

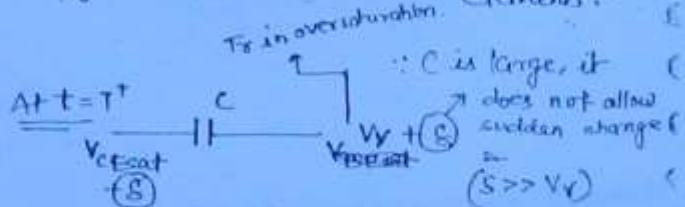
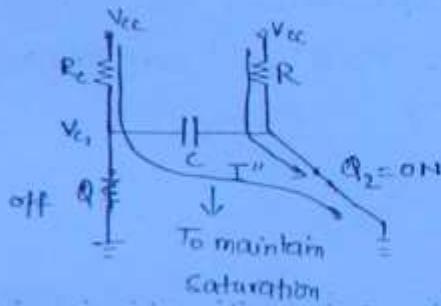
Now, for  $0 \leq t \leq T$



$I'$  will start charging capacitor towards  $V_{cc}$ , but as soon as it reaches  $V_T$ ,  $Q_2 = ON$  and  $Q_1 = OFF$ .  
 $R \& C \rightarrow$  very high. and are called timing elements.

for  $t > T$

At  $t = T^+$ ,



$\Delta t = T^+$   $\therefore C$  is large, it does not allow sudden change ( $S \gg V_T$ )  
Now,  $I''$  will start charging  $C$  and  $V_C$  will start  $\uparrow$  towards  $V_{cc}$  and  $V_T + S$  will start discharging and will settle at  $V_{cesat}$ . (see graph)

### Important Points:

#### → Waveform:-

for  $t < 0$  :- The circuit is in stable state with  $Q_2(ON)$  &  $Q_1(OFF)$ . Capacitor  $C$  will act as open ckt.

for  $0 \leq t \leq T$  + Quasi Stable state.

- On application of -ve trigger, at  $t=0$  to base  $B_2$ , a regenerative action takes place driving  $Q_2$  below cutoff. Now voltage at  $C_2$  rises to  $V_{cc}(app)$  and because of cross coupling b/w  $C_1$  &  $B_2$ ,  $Q_1$  comes into saturation.

- Now current  $I_1$  exist in  $R_c$  of  $Q_1$  and  $V_C$  drops abruptly by an amount  $I_s R_c$  upto  $V_{cesat}$ . The voltage at  $B_2$  drops by same amount  $I_s R_c$  since  $C_1$  &  $B_2$  are capacitively coupled.

- Now the multivibrator is in Quasi stable state with  $Q_1$  (on),  $Q_2$  (off).
- The off will remain in QC state for only a finite time  $T$  because (241)  
base  $B_2$  is connected to  $V_{cc}$  through a resistance  $R$ , therefore  $V_{B_2}$   
starts to rise exponentially towards  $V_{cc}$  with time constant  $RC$  & when  
it passes cutin voltage  $V_F$  of  $Q_2$  at  $t=T$ , a regenerative action  
will take place as a result of which  $Q_1$  will go into cutoff &  $Q_2$   
comes into conduction and multivibrator returns to its initial  
stable state.

For  $t \geq T$  -

- At  $t=T^+$ ,  $Q_1$  = off,  $Q_2$  = conducted.  $V_{C_2}$  drops to  $V_{cesat}$ .  $V_{B_1}$  returns to  $-V_F$ .  
Now  $V_{C_1}$  rises abruptly since  $Q_1$  is off. This  $\nabla$  in  $V_{C_1}$  transmitted to  
base of  $Q_2$  and  $Q_2$  goes into oversaturation. Hence an overshoot  $\nabla$   
develops in  $V_{B_2}$  at  $t=T^+$  which decays as  $\nabla$  the capacitor-recharge

Derivation of  $T$  :

C null charge

$$V_{B_2} = V_F - (V_F - V_i) e^{-t/RC}$$

$$\rightarrow V_F = V_{cc}; V_i = V_{cesat} - I_1 R_C \quad \text{where } I_1 R_C = V_{cc} - V_{cesat}$$

$$\rightarrow T = RC \quad \Rightarrow \quad V_i = V_{cesat} - V_{cc} + V_{cesat}$$

$$\therefore V_{cc} - (V_{cc} - V_{cesat} + V_{cc} - V_{cesat}) e^{-T/RC} = V_{B_2}$$

$$\Rightarrow V_{B_2} = V_{cc} - [2V_{cc} - (V_{cesat} + V_{cesat})] e^{-T/RC}$$

$$\text{at } t=T, V_{B_2} = V_F$$

$$V_F = V_{cc} - [2V_{cc} - (V_{cesat} + V_{cesat})] e^{-T/RC}$$

$$\Rightarrow T = RC \ln \left( \frac{2V_{cc} - (V_{cesat} + V_{cesat})}{V_{cc} - V_F} \right)$$

$$\Rightarrow T = RC \ln 2 + RC \ln \left( \frac{V_{cc} - \frac{V_{BEsat} + V_{cesat}}{2}}{V_{cc} - V_f} \right)$$

(244)

For Si -

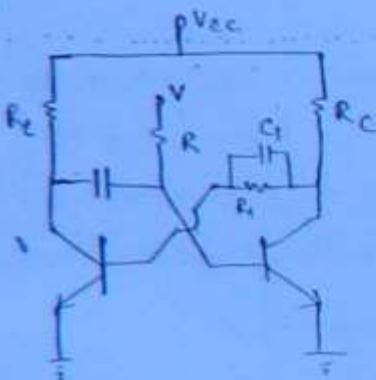
$$\frac{V_{cesat} + V_{BEsat}}{2} = \frac{0.8 + 0.2}{2} = 0.5 = V_f.$$

$$T = RC \ln 2 + RC \ln \left( \frac{V_{cc} - V_f}{V_{cc} - V_r} \right)$$

$$\Rightarrow T = RC \ln 2 = 0.69 RC$$

(Workbook)

\* Chap 11  
Conv. 1



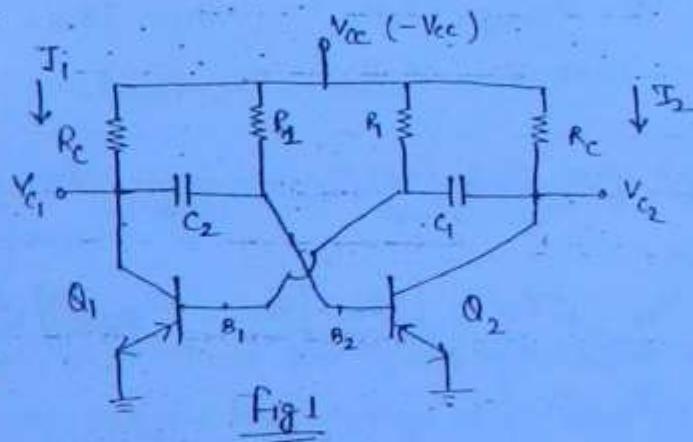
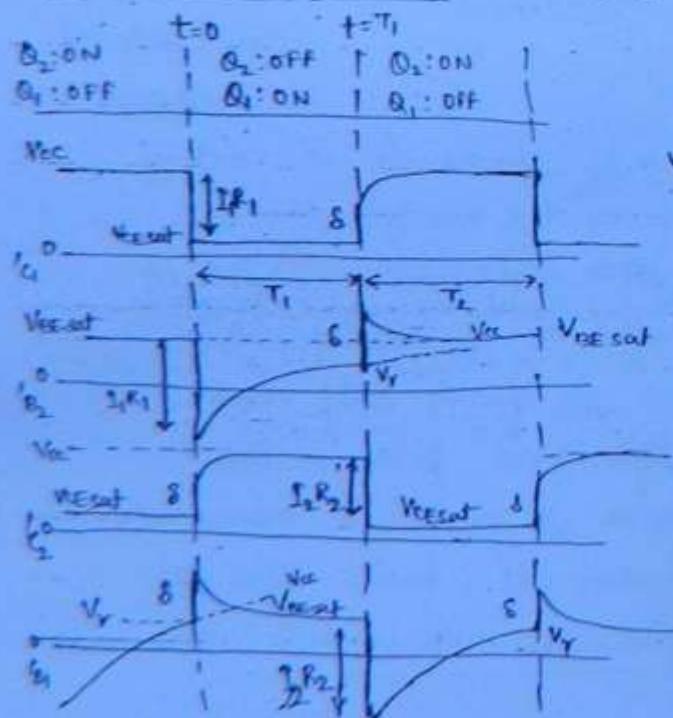
In the above derivation change

$V_f = V$  & assume  $V_{BEsat} + V_{cesat} \ll V_{cc}$   
 $\& V_f \ll V$ .

$$T = RC \ln \left( 1 + \frac{V_{cc}}{V} \right)$$

Diagram for Voltage controlled f (or T)

### Astable Multivibrator

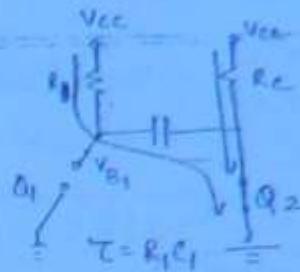


for  $t < 0$   $Q_1: OFF, Q_2: ON$

$$V_{C_2} = V_{CEsat}; V_{B_2} = V_{BEsat}$$

$$I_{C_1} = I_{B_1} = 0; V_{C_1} = V_{CC} \& V_{B_1} < 0.$$

(245)



$\rightarrow C_2$  will charge &  $V_{B_2} \uparrow$  till  $V_r$  & then

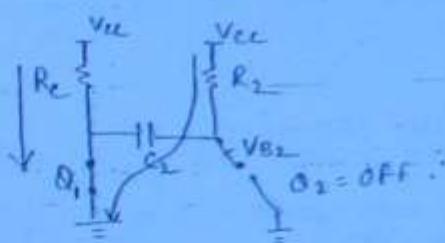
states will change with sudden change in  $V_{C_2}$  &  $V_{B_2}$ .

$\rightarrow t < T_1$  For  $t \leq 0$

for  $0 < t \leq T_1$  —  $Q_2: OFF; Q_1: ON$

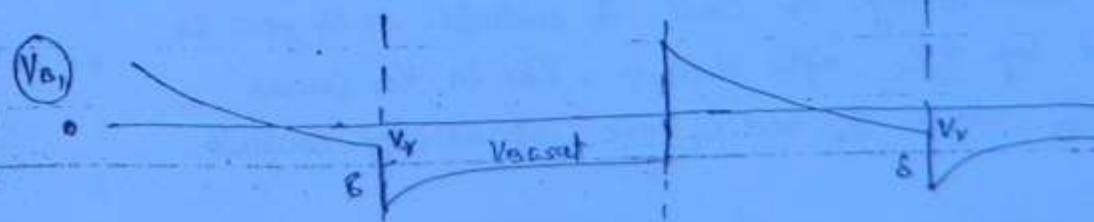
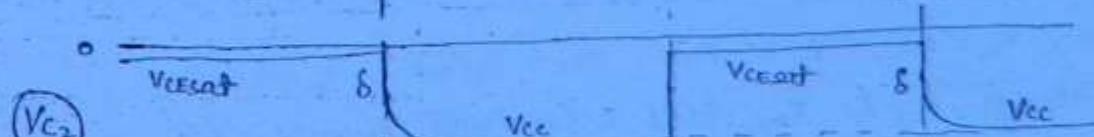
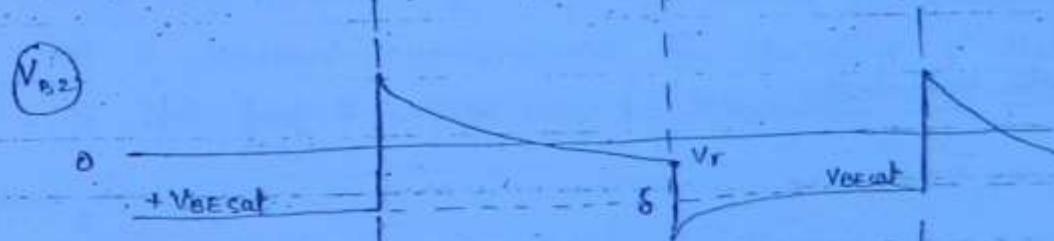
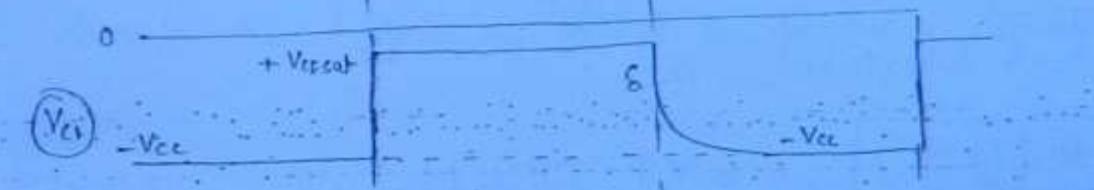
Now  $C_2$  will start charging

& similar process as above will be repeated



$\rightarrow$  for p-n-p Tr

$t=0$	$t=T_1$	$t=T_1+T_2$
$Q_2: ON$	$Q_2: OFF$	$Q_2: ON$
$Q_1: OFF$	$Q_1: ON$	$Q_1: OFF$



$$\rightarrow T_1 = 0.69 R_2 C_2 \quad ; \quad T_2 = 0.69 R_1 C_1 \quad \left\{ \because T_1 \neq T_2 ; D \neq 50\% \Rightarrow \text{Asymmetrical square wave} \right.$$

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$$\rightarrow T = T_1 + T_2 = 0.69 (R_1 C_1 + R_2 C_2)$$

$$\rightarrow f = \frac{1}{T} = \frac{1.44}{R_1 C_1 + R_2 C_2} ; \text{ if } R_1 = R_2 = R \text{ & } C_1 = C_2 = C$$

↓  
for asymmetrical sq. wave

$$\text{then } T = 1.38 R C$$

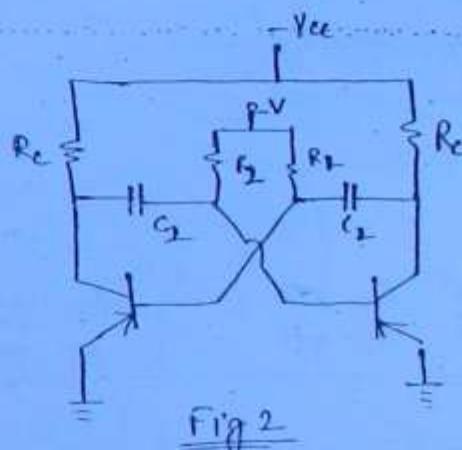
↓  
for symmetrical sq. wave

$\rightarrow$  Voltage to freq. converter :-

$$T_1 = R_2 C_2 \ln \left( 1 + \frac{V_{cc}}{V} \right)$$

$$T_2 = R_1 C_1 \ln \left( 1 + \frac{V_{cc}}{V} \right)$$

$$T = (R_1 C_1 + R_2 C_2) \ln \left( 1 + \frac{V_{cc}}{V} \right)$$



$\rightarrow f = 1/T$  & if  $R_1 = R_2 = R$  &  $C_1 = C_2 = C$ , then

$$T = 2RC \ln \left( 1 + \frac{V_{cc}}{V} \right) \quad \therefore \text{as } V \uparrow, T \downarrow \text{ & } f \uparrow$$

Important Points for Astable Multivibrator :-

Waveform (Pmp)

for  $t < 0$   $Q_1 : \text{OFF}$ ,  $Q_2 : \text{ON}$

Hence, for  $t < 0$ ,  $V_{B1} = +V$ ,  $V_{C1} = -V_{cc}$ ,  $V_{B2} = V_{BEsat}$ ,  $V_{C2} = V_{CEsat}$

$\rightarrow$  Capacitor  $C_1$  charges through  $R_2$  &  $V_{B1}$  falls exponentially towards  $-V_{cc}$ .

$\rightarrow$  At  $t=0$ ,  $V_{B1}$  reaches cutin voltage  $V_r$  and  $Q_1$  conducts. As  $Q_1$  goes to saturation,  $V_{C1}$  rises by  $I_1 R_c$  upto  $V_{cesat}$ . Rise in  $V_{C1}$  causes equal rise  $I_1 R_c$  in  $V_{B2}$  since  $B_2$  and  $C_1$  are capacitively coupled.

- Rise in  $V_{B_2}$  cuts off  $Q_2$  and its collector falls towards  $-V_{cc}$ . This fall in  $V_{C_2}$  is coupled through capacitor  $C_1$  to base  $B_1$ , causing undershoot  $\delta$ . in  $V_{B_1}$  and abrupt amount drop by same amount  $\delta$  in  $V_{C_2}$ .
- The voltage  $V_{B_2}$  is  $V_{BECAT} + I_1 R_C$  at  $t=0^+$  and  $\downarrow$  exponentially with time constant  $R_2 C_2$  towards  $-V_{cc}$ .  
At  $t=T_f$ ; base  $B_2$  reaches cutin level  $V_T$  and reverse transition takes place

(247)

In fig(1), the frequency of oscillation may be varied over the range from Hz to MHz by adjusting  $R$  or  $C$ . It is also possible to change  $T$  electrically by connecting  $R_1, R_2$  to an auxiliary voltage  $-V$  (fig.2) (The collector supply remains  $-V_{cc}$ ). Then,

$$T = 2RC \ln \left( 1 + \frac{V_{cc}}{V} \right).$$

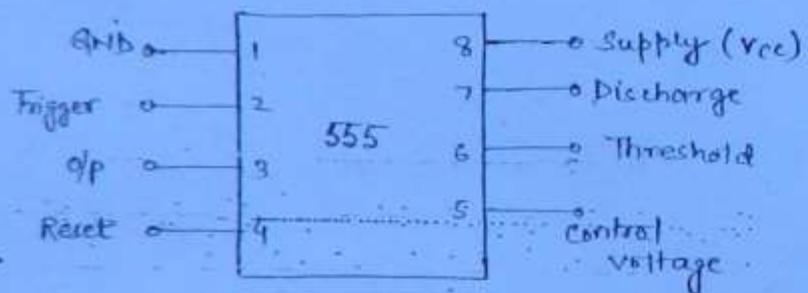
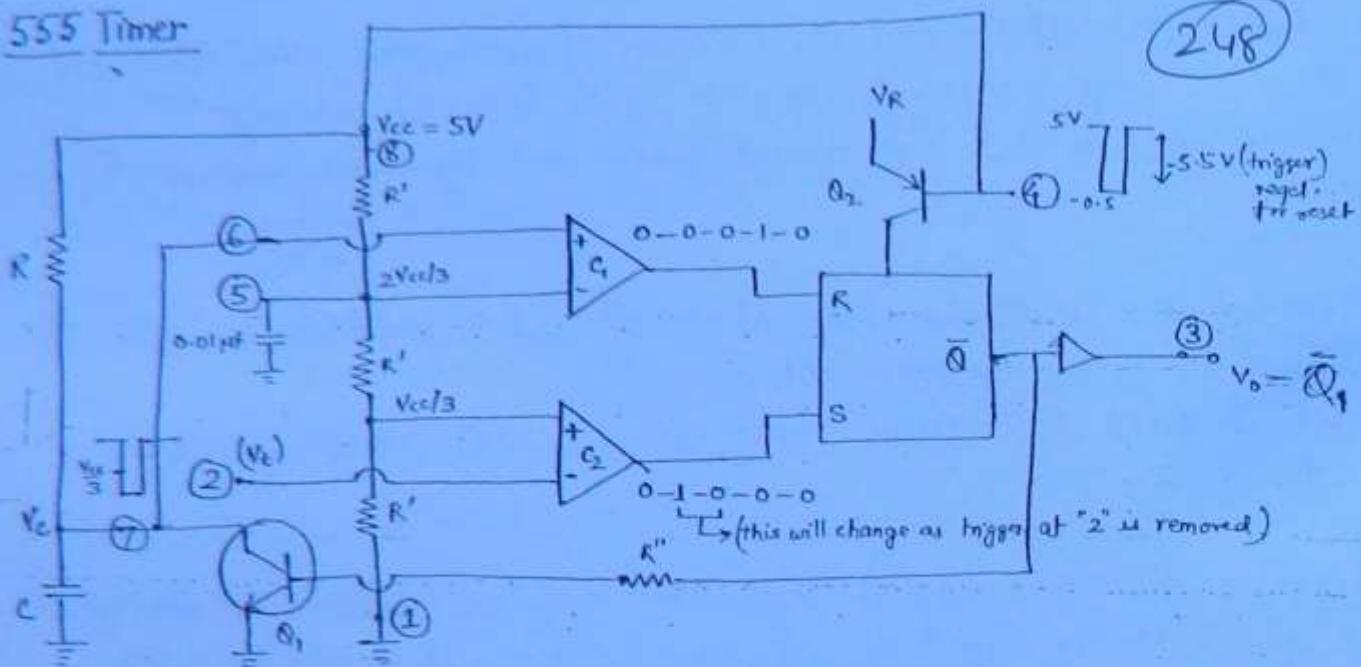
Such a ckt (fig.2) is voltage to frequency converter.

Note

- If each resistor  $R$  ( $R_1, R_2$ ) is replaced by a Transistor which acts as a constant current source for charging  $C$  then excellent linearity b/w freq & voltage may be attained.

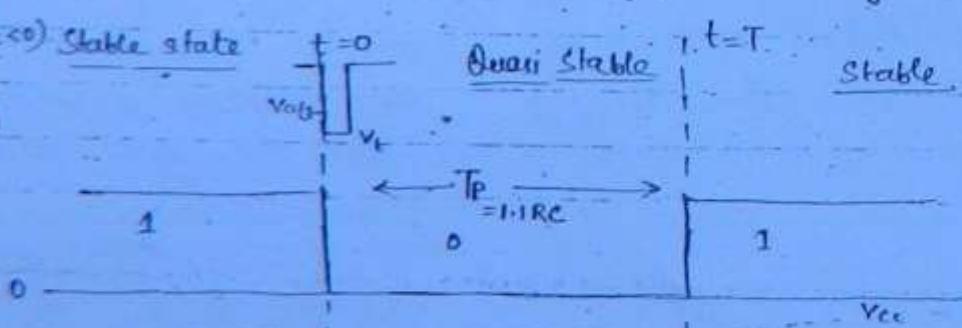
## 555 Timer

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( $t < 0$ ) Stable state

(V<sub>d</sub>)

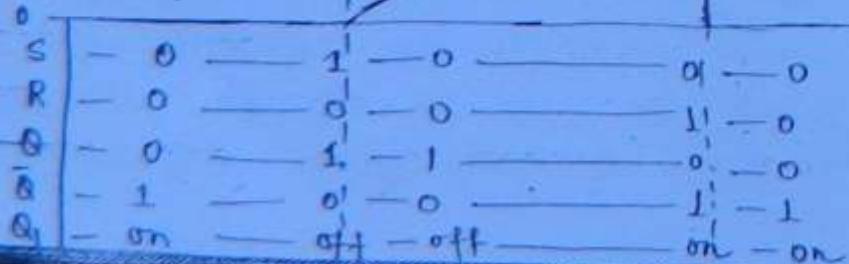


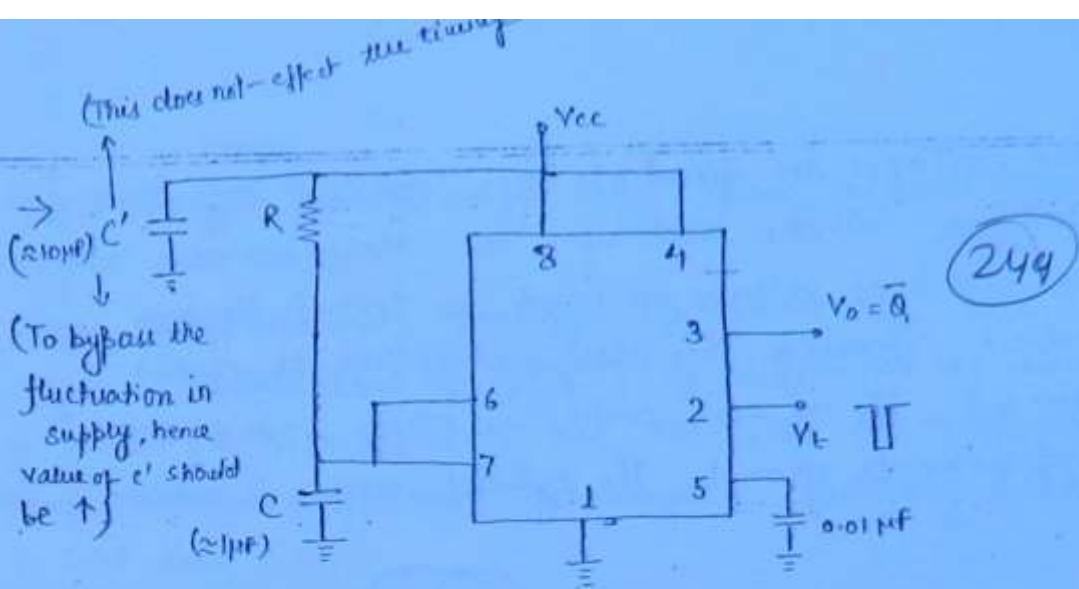
$T_p$  = pulse width

(V<sub>c</sub>)

$2V_{cc}/3$

$\tau = RC$





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### 555 in Monostable Mode

Derivation of  $T_p$  -

$\rightarrow V_C \rightarrow \text{change from } 0 \text{ to } \frac{2V_{cc}}{3}$

$$\rightarrow V_C = V_f - (V_f - V_i)e^{-t/\tau} ; \quad V_f = V_{cc}, \quad V_i = 0, \quad \tau = RC$$

$$\therefore V_C = V_{cc} (1 - e^{-t/RC})$$

$$\text{At } t = T_p - \quad \frac{2V_{cc}}{3} = V_{cc} (1 - e^{-T_p/RC})$$

$$\Rightarrow T_p = RC \ln 3$$

$$\Rightarrow \boxed{T_p = 1.1 RC}^{**} - \text{Imp}$$

$\rightarrow$  The device 555 is a monolithic timing ckt that can produce accurate & highly stable time delays or oscillations.

Constructional Details :-

- The device consists of two comparators ( $C_1$  &  $C_2$ ) that drive set & reset terminals of a flip flop which in turn controls on & off cycles of discharge tr  $Q_1$ .

- comparator reference voltages are fixed at  $\frac{2V_{cc}}{3}$  for  $C_1$  &  $\frac{V_{cc}}{3}$  for  $C_2$  by means of a voltage divider made up of three series resistors  $R$ . These reference voltages are reqd. to control timing.
- Timing can be controlled externally by applying voltage to control voltage terminal (pin 5). If no such control is reqd., pin 5 can be bypassed by a capacitor to ground. The typical value is about 0.01μF.

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### function:-

- When ~~voltage~~ voltage is applied at trigger terminal goes grows -ve & passes through reference level  $\frac{V_{cc}}{3}$ , the o/p of  $C_2$  changes its state. This change of state ( $S=1, R=0$ ) will set the flip flop with  $Q=0$  & Tr.  $Q_1 = \text{off}$ .
- When voltage applied at threshold terminal (pin 6) grows +ve & passes through  $\frac{2V_{cc}}{3}$ , o/p of  $C_1$  changes its state ( $S=0, R=1$ ). This change of state will reset the flip flop with  $Q=1$  and Tr.  $Q_1 = \text{on}$ .

### PIN-4 (Reset Pin)

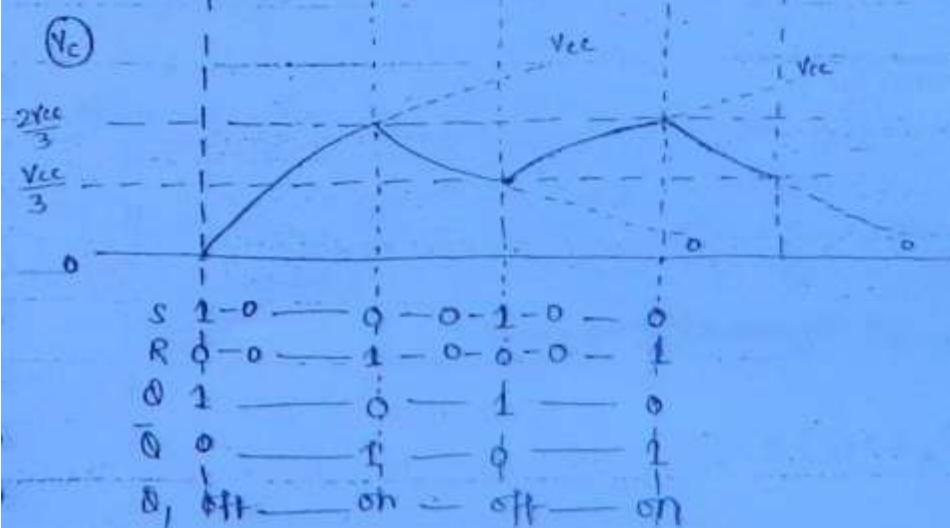
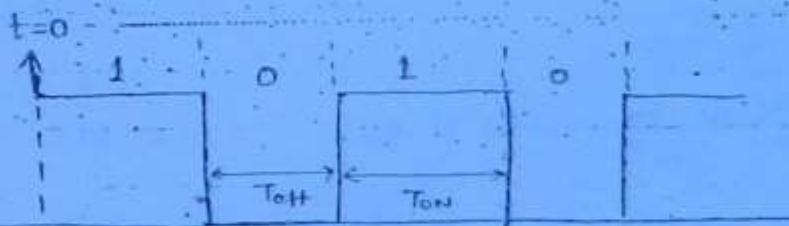
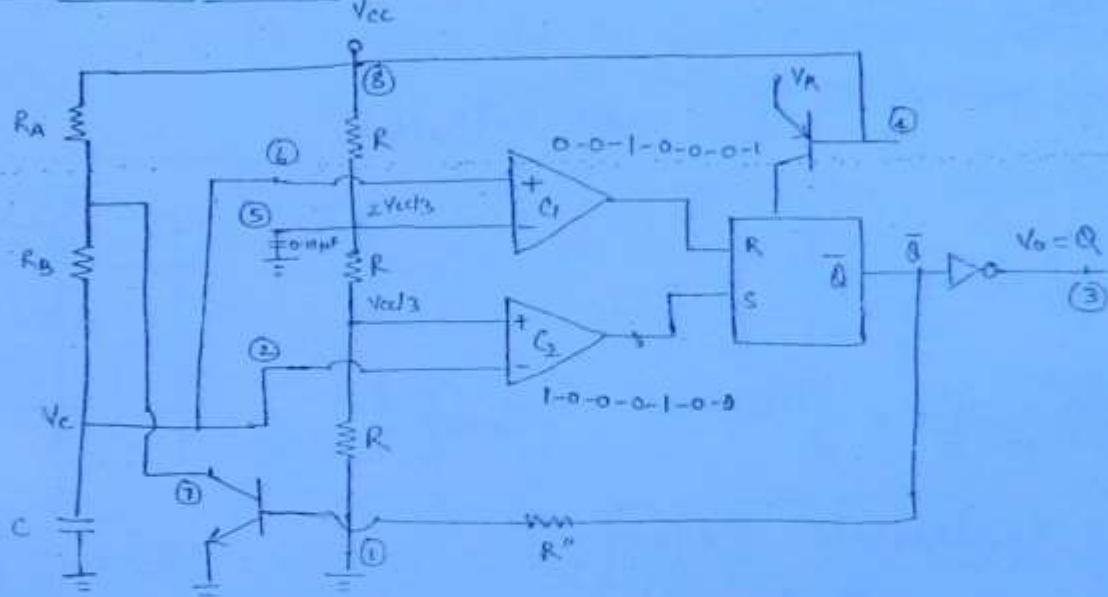
- A separate reset terminal is provided which is used to reset the ff externally. Normally when Pin 4 is not used, it should be connected to the supply  $V_{cc}$  to avoid any false triggering. Transistor  $Q_2$  acts as a buffer, isolating the ckt from false to reset.

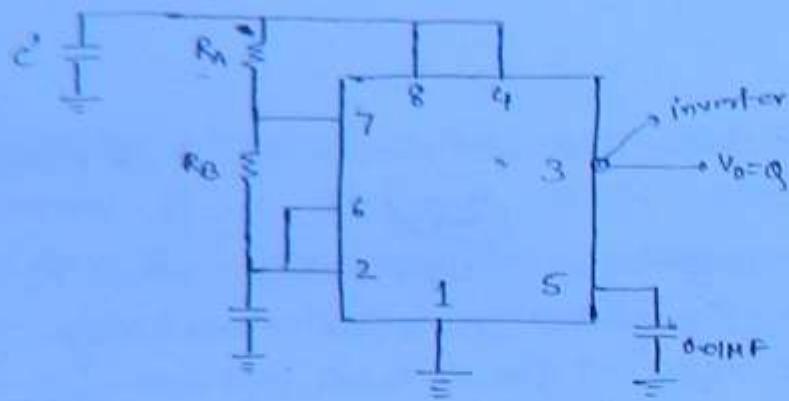
### 555 timer in Monostable state :-

- for  $t < 0$ , ckt is in stable state.  $V_t$  (trigger voltage) =  $V_{cc}$ ,  $V_o = Q = 1$  &  $V_c = 0$ , and  $S=R=0$ .
- At  $t=0$ , on application of -ve trigger less than  $V_{cc}$  causes o/p of  $C_2$  to be high. This will set ff with  $Q=V_o=0$  and  $Q_1 = \text{off}$ .

- Note that after termination of trigger pulse, FF will remain in  $\bar{Q}=0$  state. (Since  $S=0, R=0$ ). 257
- Now, timing capacitor C charges up towards  $V_{cc}$  with  $\tau = RC$ . When  $V_c$  reaches threshold level of  $\frac{2V_{cc}}{3}$ , C<sub>t</sub> will switch its state. This change of state ( $R=1, S=0$ ) resets the FF. with  $\bar{Q}=V_o=1$  and  $Q_f=0\text{mA}$ . Then the saturation resistance of Q<sub>f</sub> discharges C suddenly & ckt reach to its initial state.

### 555 timer in Astable Mode :-





(252)

### Astable Mode

#### Derivation of T<sub>on</sub>:

Capacitor charges from  $\frac{V_{cc}}{3}$  to  $\frac{2V_{cc}}{3}$  with  $C = (R_A + R_B)C$ .

$$V_f = V_{cc}, \quad V_i = \frac{V_{cc}}{3}$$

$$\therefore V_C = V_{cc} - \left[ V_{cc} - \frac{V_{cc}}{3} \right] e^{-t/C}$$

At  $t = t_{on}$

$$\Rightarrow \frac{2V_{cc}}{3} = V_{cc} - \frac{2V_{cc}}{3} e^{-t_{on}/(R_A + R_B)C}$$

$$\Rightarrow \boxed{t_{on} = 0.69 (R_A + R_B)C = C \ln 2}$$

#### Derivation of T<sub>off</sub>:

Capacitor discharges from  $\frac{2V_{cc}}{3}$  to  $\frac{V_{cc}}{3}$  with  $C' = R_B C$ .

$$V_i = \frac{2V_{cc}}{3}, \quad V_f = 0.$$

At  $t = T_{off}$

$$\frac{V_{cc}}{3} = 0 - \left( 0 - \frac{2V_{cc}}{3} \right) e^{-T_{off}/C'}$$

$$\Rightarrow \boxed{T_{off} = 0.69 R_B C}$$

$\rightarrow \boxed{T_{on} > T_{off}} \Leftrightarrow > 50\% \Rightarrow$  Asymmetrical sq wave.

$$\therefore T = T_{on} + T_{off} = 0.69 (R_A + 2R_B) C$$

$$\therefore f = \frac{1}{T} = \frac{1.44}{(R_A + 2R_B)C}$$

(253)

Duty Cycle

$$D = \frac{R_A + R_B}{R_A + 2R_B} \times 100\%$$

When  $V_O = Q$ ,  $D > 50\%$ .

When  $V_O = \bar{Q}$ ,  $D < 50\%$ .

→ For  $R_A = 0\Omega$ ,

$$D = 50\%$$

- but  $R_A$  cannot be 0 since pin 7 will be directly connected with  $V_{CC}$  and  $T_{on}$  will turn.

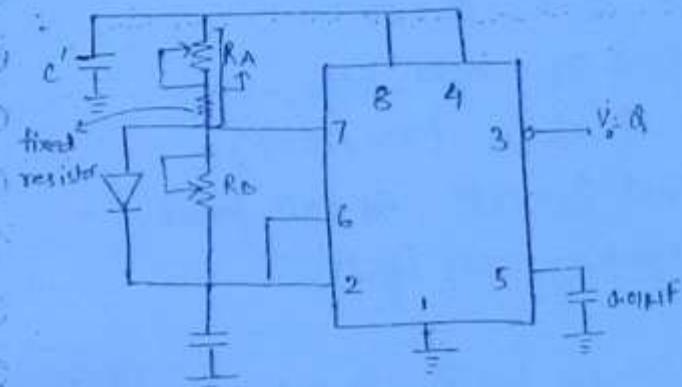


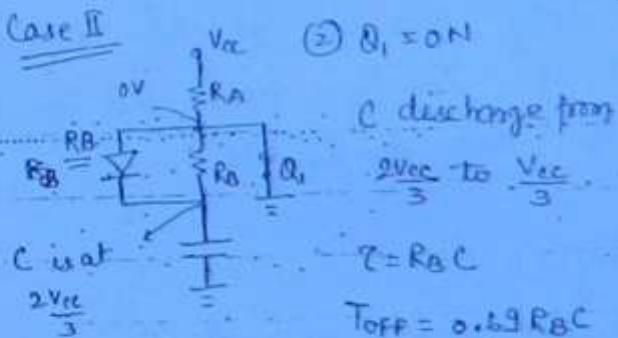
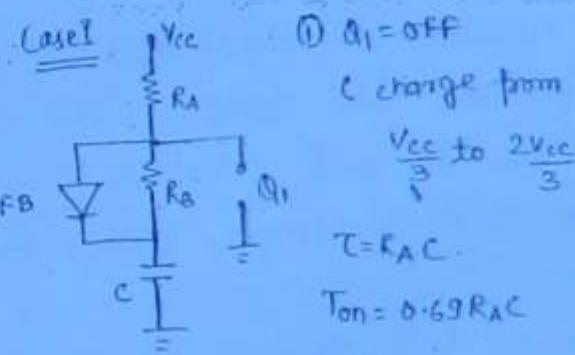
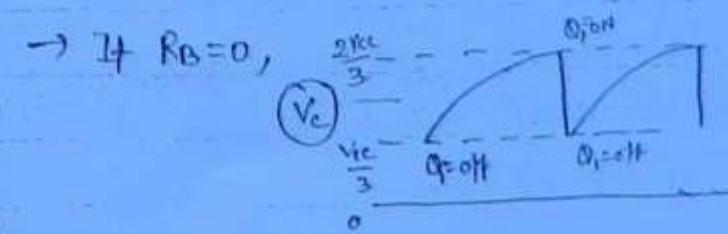
Fig 2.

$$\rightarrow T = 0.69(R_A + R_B)C$$

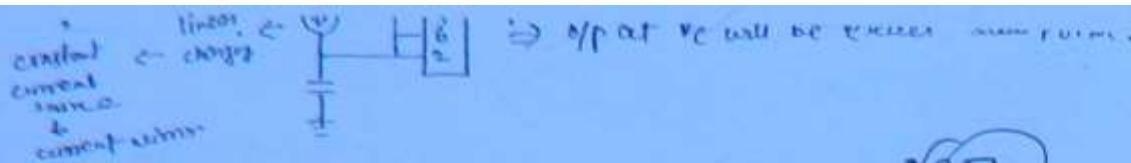
$$f = \frac{1}{T} = \frac{1.44}{(R_A + R_B)C}$$

$$D = \frac{T_{on}}{T} \Rightarrow D = \frac{R_A}{R_A + R_B} \times 100\%$$

Now if  $R_A = R_B$ ,  $D = 50\%$ .



This sawtooth pulse is achieved across capacitor and not on o/p.



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- Potentiometer is provided in o/p as duty cycle can be adjusted by changing  $R_A$  &  $R_B$ . (also all diodes & resistor are not ideal and  $R_A$  &  $R_B$  can be set accordingly to get "symm" o/p).
- A fixed resistor is added to prevent pin 7 to directly connected to Vcc even if potentiometer at  $R_A$  is set at 0.

### Important Points:-

For Fig ①

- In this mode, timing capacitor C charges up towards Vcc through  $R_A + R_B$  upto  $\frac{2V_{cc}}{3}$  then  $C_1$  switches its state. This change of state ( $s=0, R=1$ ) reset the FF with  $V_O = Q = 0, \bar{Q} = 1, Q_1 = \text{on}$ . Then capacitor C discharges through  $R_B$  &  $Q_1$  upto  $\frac{V_{cc}}{3}$ . Then  $C_2$  switches its state. This change of state ( $s=1, R=0$ ) set the FF with  $V_O = Q_0 = 1, \bar{Q} = 0$  and  $Q_1 = \text{off}$ . At this point capacitor starts to charge again, thus completing the cycle.
- Duty cycle will always be  $<$  or  $>$  50% for fig ①. To achieve 50% duty cycle we should make  $R_A = 0$ , however with  $R_A = 0$  pin 7 is directly connected to +Vcc and this may damage tr.  $Q_1$  when  $Q_1$  is ON.

→ In Fig ② -

- Capacitor charges through  $R_A$  and diode D upto  $\frac{2V_{cc}}{3}$  and discharge through  $R_B$  and  $Q_1$  upto  $\frac{V_{cc}}{3}$ . Then cycle repeats.

- To obtain a square wave o/p  $R_A$  must be a combination of a fixed resistor & potentiometer so that potentiometer can be adjusted for exact sq. wave. Fixed resistor will avoid direct connection of pin 7 to Vcc when potentiometer is set at 0%.

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Note:- Guys Be Cool Dude I am here for help You ☺

(255)

Application

directly connecting voltage will change  
referent  
using Ptn 5). - Ve Variation will from  
 $2V_{CC}$  or  $\frac{V_{CC}}{2}$ .

- 1) It is used as Freq. modulator, i.e. voltage to freq. converter.
- 2) It is used as missing pulse detector.