

# OpenJIT Compiler System Verification Using ISM + HOL-Isabelle Platform w.r.t JikesRVM – Research Virtual Machine → Probing Next Generation Java/Scala/JI Prolog Based IoT Informatics Frameworks Using JADE – A Simple Suggestion.

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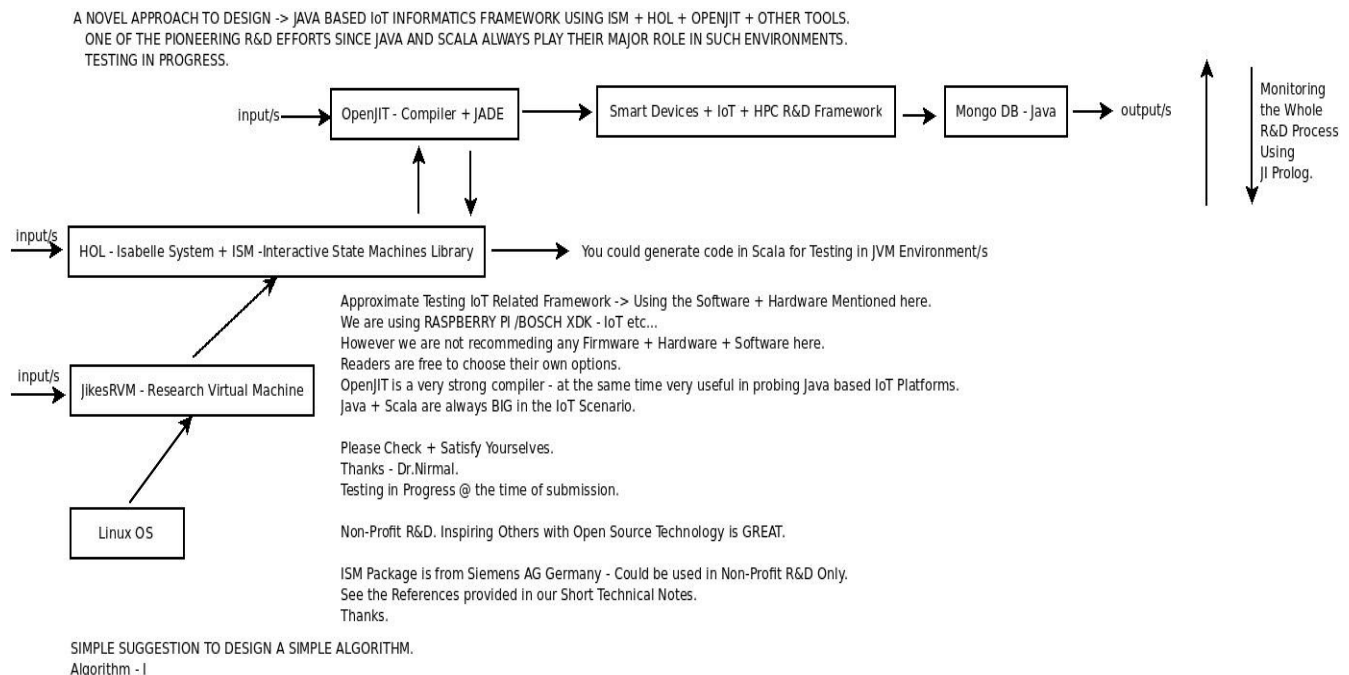
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[ No matter how you spin it....even a top can't stay upright forever ]

## [I] Idea + Inspiration + Introduction :

Exploring OpenJIT Compiler System Using HOL – Isabelle w.r.t IoT Informatics R&D.

## [II] Informatics Framework :



[ Figure I – Algorithm I – Java/Scala based Testing of IoT Informatics Framework ]

### **[III] Important References :**

- [a] <https://github.com/tejdkn-2019-ShortNotes>
- [b] <https://www.openjit.org/>
- [c] <http://www.jiprolog.com/>
- [d] <https://jade.tilab.com/>
- [e] <https://www.jikesrvm.org/>
- [f] <https://github.com/tejdkn-2019-ShortNotes/2021-Nir-Informatics/blob/main/Scala-Nir-Kub-2021-ISM.pdf>

**[IV] Acknowledgment/s :** Non-Profit R&D. Thanks to all. Inspiring Others is GOOD.

**[ THE END ]**