CAKEML + RACKET/macros/Machine Learning

→ To TEST [U96 + Zynq MPSoC Board] w.r.t

Space + Medicine + Telecoms + IoT/HPC

Heterogeneous Systems → A Novel Suggestion on

Designing & Testing an Interesting Video DSL
Domain Specific Language based Algorithm/s.

Nirmal – Informatics R&D – USA/UK/Israel/BRICS Group of Nations.

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[I] Main Idea + Inspiration + Introduction :

- [a] https://github.com/tejdnk-2019-ShortNotes Very much useful notes.
- [b] https://github.com/tejdnk-2019-ShortNotes/2021-Nir-Informatics/blob/main/AVNET-U96-Ruby-Nir-21.pdf
- $[d] \ \underline{https://github.com/tejdnk-2019-ShortNotes/2021-Nir-Informatics/blob/main/Racket-Java-Img-Nir-2021.pdf}$
- [e] https://github.com/tejdnk-2019-ShortNotes/2021-Nir-Informatics/blob/main/CAKEML-AVNET-U96-Xilinx-Nir-21.pdf
- **[II] Acknowledgment/s**: Non-Profit R&D. Thanks to all. Inspire Others Always.
- [III] Conclusion: Please derive your own R&D Informatics Frameworks. Keep Hacking
- **[IV] Our Status :** We are testing with CAKE ML + RACKET. Thank You Dr.Nirmal.

[V] Important Reference/s:

https://files.devnetwork.cloud/DeveloperWeekNewYork/presentations/2019/open-talks/Fred_Kellerman.pdf

https://cakeml.org/ && https://www.racket-lang.org/ && https://pkgs.racket-lang.org/

[THE END]