Very Long Instruction Word (VLIW) Architecture Exploration Using LLVM + Ruby + mRuby + QRNG -Services/mruby-qrng + SVM/Machine Learning Algorithms → An interesting Short Technical Notes.**[Probing Space + Medicine + Telecoms + HPC Domains]**

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[I] Main Idea + Inspiration + Introduction :

TITLE is enough → Just do it. VLIW is good subject for Theoretical investigations. Based on our Technical Notes our readers could derive their own theoretical framework/s easily. Thanks.

"ρ-VEX: A Reconfigurable and Extensible VLIW Processor " → r-vex on github.
https://www.geeksforgeeks.org/very-long-instruction-word-vliw-architecture/
https://doi.org/10.1109/ASPDAC.1995.486374
https://link.springer.com/chapter/10.1007/978-1-4471-3544-9_16

[II] Probing VLIW Architecture w.r.t Ruby + LLVM + Other Related Tools :

ruby Tools → Probe → VLIW Architecture → Theoretical investigations.

[III] Important References/ :

https://researcher.watson.ibm.com/researcher/view_group_subpage.php?id=2833

"ρ-VEX: A Reconfigurable and Extensible VLIW Processor " -

https://github.com/tvanas/r-vex – Very much useful + interesting to explore hardware designs.

euler.mat.uson.mx/~havillam/ca/CS323/0708.cs-323012.html

Others you could just find online. We leave it to you.

https://github.com/tejdnk-2019-ShortNotes

Non – Profit R&D.
Testing in Progress.
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