

Testing Vedic Mathematics based on UT Sutra Algorithm/s w.r.t Advanced Digital Signal Processing Framework → To Probe Space + Medicine + Telecoms + AI/ML/DL + HPC Heterogeneous Complex Systems.

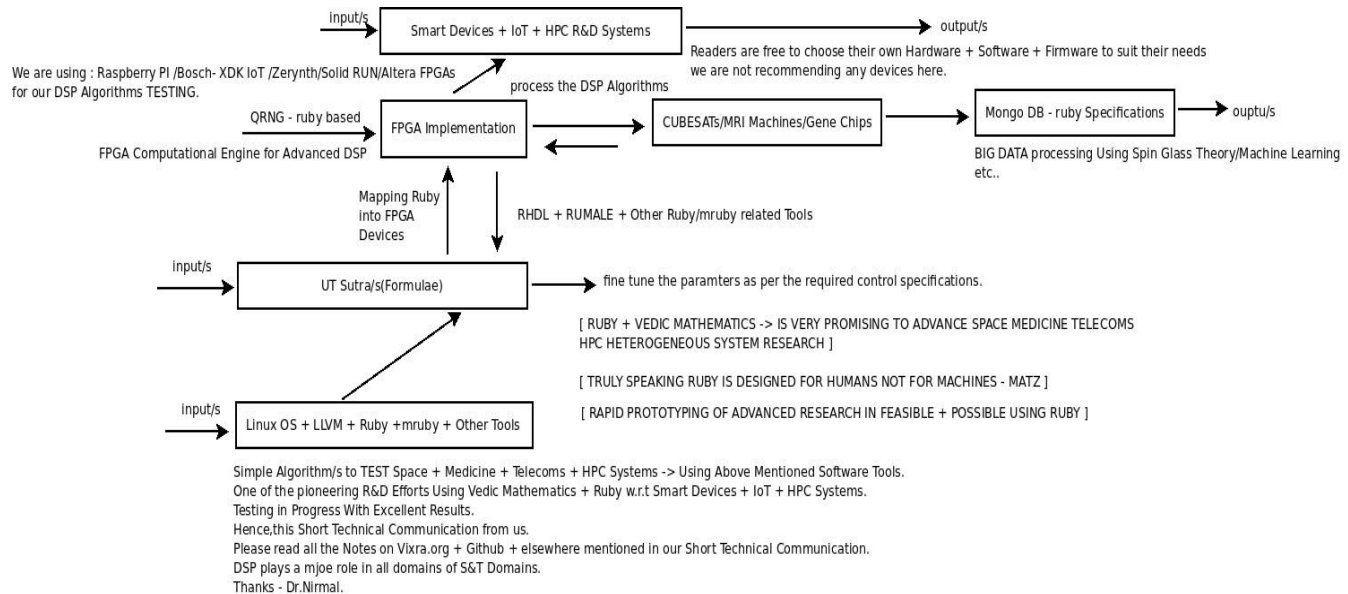
[Exploring Ruby/mruby/QRNG/Machine Learning/Spin Glass Theory → Hybrid DSP Techniques]

Nirmal – Informatics R&D – USA/UK/Japan/Israel/BRICS Group of Nations.

Current Member – ante Inst UTD Dallas TX USA.

Contact_info – hmfg2014@gmail.com

[I] Main Idea + Inspiration + Introduction :



[ALGORITHM I]

[Figure I – Simple Algorithm I – Vedic Mathematics → Space + Medicine + Other S&T Domains]

** Making use of Urdhava Tiryakbhyam [UT]Sutra or Formula of Vedic mathematics. Multiplier is one of the key hardware blocks in most Digital Signal Processing (DSP) Systems. Please See the References.

[II] Very Useful + Important References :

[a] <https://github.com/tejdnk-2019-ShortNotes>

[b] <https://www.electronicsforu.com/technology-trends/vedic-mathematics-microcontrollers>

[III] Acknowledgment/s : Non-Profit R&D. Inspire Others Always.

[THE END]