HOList + DeepHOL + LLVM + AD - Algorithmic Differentiation w.r.t -> Testing with OCaml - FPL- & Python -> A Simple Suggestion as a Short Technical Communication on Testing FPGAs for IoT Computing Environments.

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[I] Main Idea + Inspiration + Introduction :

https://github.com/tejdnk-2019-ShortNotes -> Lot of examples for your use : Please TRY & come up with Novel Algorithms.

https://arxiv.org/pdf/1904.03241.pdf

https://blog.janestreet.com/using-python-and-ocaml-in-the-same-jupyter-notebook/

https://github.com/zshipko/ocaml-py && https://github.com/thierry-martinez/pyml

https://ocaml.xyz/book/algodiff.html -> Algorithmic Differentiation & Scientific Computing with OCaml.

Please make a note : Not a Straight Forward Method.Needs some tricky approaches.Please Try.

We are generating different test-beds to generate FPGA Testing.

With Thanks for reading our Short Technical Communication.
Dr.Nirmal - 16/Jan/2022.

[THE END]