

{ Multi-core Parallelization of Abstracted Abstract Machines [AAM] -> Can we apply to refs [a] & [b] ? - An Interesting Suggestion }

[A Simple Idea by Dr.Nirmal - Informatics R&D -USA/UK/Israel/Brazil - I am still working on this idea]

Our main idea & inspiration is derived from :

"It is straightforward to derive well-known higher-order flow analyses as abstract interpretations of well-known abstract machines. In this paper, we explore multi-core parallel evaluation of one such abstract machine, the CES machine. The CES machine is a variant of CESK machines that runs Continuation Passing Style (CPS) λ -calculus. Using k-CFA, the concrete semantics for a CES machine can be turned into abstract semantics. Analyzing a program for this machine is a state graph walk, which can be run in parallel to increase performance “ -> [Authors : Leif Andersen & Matthew Might - University of Utah USA]

[Source - <https://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.453.7805>]

[a] "Racket, the Programming Language + JikesRVM -Research Virtual Machine [RVM] / Java / Prolog /Imaging Mathematics/QRNG Concepts To Probe Electron Microscopy [EM] Images + Medical Images [MRI Scans] w.r.t Smart Devices + IoT + HPC Heterogeneous Systems.” ->

[Source - <https://github.com/tejdkn-2019-ShortNotes>]

[b] “Racket, the Programming Language + JikesRVM - Research Virtual Machine [RVM] / Java / Prolog /Clifford Algebras [Geometric Algebra \rightarrow GA] +Wavelets/QRNG Concepts To Probe Electron Microscopy [EM] Images + Medical Images [MRI Scans] w.r.t Smart Devices + IoT + HPC Heterogeneous Systems.” ->

[Source - <https://github.com/tejdkn-2019-ShortNotes>]

Non-Profit R&D - Keep Hacking

With Thanks - Dr.Nirmal - [THE END]