Considering Ising Models w.r.t FPGA Testing towards ::-> Neuromorphic Computing - A Simple Suggestion + Short Technical Communication Using Python + Dr.Racket Programming Languages with Tools.

[Exploring FPGA as an Ising Device w.r.t IoT Informatics]

Dr.Nirmal - Informatics R&D - ante Inst UTD Dallas TX USA - hmfg2014@gmail.com

[I] Main Idea + Inspiration + Introduction :

Ref[a] -> is our main inspiration & introduction.

[II] R&D Information Framework :

Please generate your own Algorithms.

[III] Important References :

[a] https://par.nsf.gov/servlets/purl/10216396 ; [b] https://github.com/bdhammel/ising-model

[c] https://unlcms.unl.edu/cas/physics/tsymbal/reference/spin-dependent_tunneling/magnetic_tunnel_junction.shtml#

[d] https://github.com/tejdnk-2019-ShortNotes -> Lot of examples for your information with interesting Algorithms.

[e] https://www.intel.com/content/www/us/en/products/programmable.html

[IV] Acknowledgment/s : *Non-Profit R&D - Inspire Others - Sincere Thanks.*

[V] Conclusions with Future Perspectives : *One of the RARE pioneering R&D Efforts.Rigorous Testing in Progress @ the TIME of Submission.*

[THE END]