To Explore & Test [ JI Prolog+ JLang + [jxos/mjvm] ] Usage w.r.t Next Generation Intelligent Embedded Systems [ES] Co-Design involving: [ Hardware + Software + Firmware ] based on [ Smart Devices + IoT + HPC Systems ] → To run Space + Medicine + Telecoms Heterogeneous Systems.

## [ Exploring Java based TEST BED Architecture for Embedded Systems ]

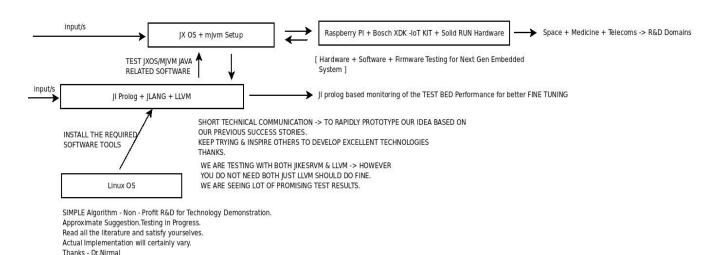
Nirmal – Informatics R&D – USA/UK/Israel/BRICS Group of Nations. Current Member – ante Inst UTD Dallas TX USA. Contact\_info – <a href="mailto:hmfg2014@gmail.com">hmfg2014@gmail.com</a>

## [I] Main Idea + Inspiration + Introduction :

SIMPLE BUT INTERESTING JAVA BASED AVANCED INFORMATICS FRAMEOWRK TO PROBE SPACE + MEDICINE + TELECOM USING SMART DEVICES + HOT + HPC SYSTEMS FOR DEMANDING APPLICATIONS.

ONE OF THE PIONEERING R&D EFFORTS USING JX OS + MJVM TOOLS.

JAVA NEVER DISAPPOINTS YOU.



[ Figure I – Algorithm I – Our Simple Informatics TEST BED ]

[II] References: https://github.com/tejdnk-2019-ShortNotes https://www.cs.cornell.edu/~dzag/other

**[III] Acknowledgment/s**: Thanks to all. Non-Profit R&D. Inspiring Others Always is GOOD.

[THE END]