

# Understanding RUST + CAKEML Interfacing → Testing Smart Devices + IoT + HPC Systems w.r.t Machine Learning.

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## [I] Main Idea + Inspiration + Introduction :

RUST and CAKEML interfacing Using Miro-kernel/Minsky Machines/Machine Learning Aspects w.r.t Developing & Testing Next Generation Intelligent Embedded Systems (ES) – A Novel Feasible Architecture & ES Informatics as a Simple Suggestion.

## [II] Rust + CAKEML → R&D Informatics Framework :

Derive your own R&D Informatics Framework Using our Short Notes on Vixra.org/github.  
We are TESTING the concepts with promising results. It is easy. Thanks.

[ <https://github.com/tejdnk-2019-ShortNotes/tejdnk-Space-Medicine-Informatics-github.io/blob/master/Rust-C-GeneChip-Nir-2021.pdf> ]

[ <https://github.com/tejdnk-2019-ShortNotes/tejdnk-Space-Medicine-Informatics-github.io/blob/master/Test-Kernels-Nirmal-Ruby-21.pdf> ]

[a] <https://AtheMathmo.github.io/rusty-machine>

[b] <https://source.that.world/source/rux/>

[c] <http://goodmath.blogspot.com/2006/05/minsky-machine.html>

[d] <https://cakeml.org/itp18-short.pdf>

[e] <https://github.com/CakeML/cakeml>

[f] <http://hol-theorem-prover.org>

[g] <http://www.polymml.org>

[h] <https://github.com/SEL4PROJ/cakeml-bake>

**[II] Acknowledgment/s:**

Sincere Thanks to all WHO made this happen. Non-Profit R&D.  
Inspiring Others is GOOD Always.

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