Electronic Circuits Verification using Grobner Bases in the Context of Firmware -> LLVM/Clang/Grobner Bases in C++/Ruby/mruby/QRNG -> Novel R&D Approach -> Firmware Testing Challenges in Embedded Systems.

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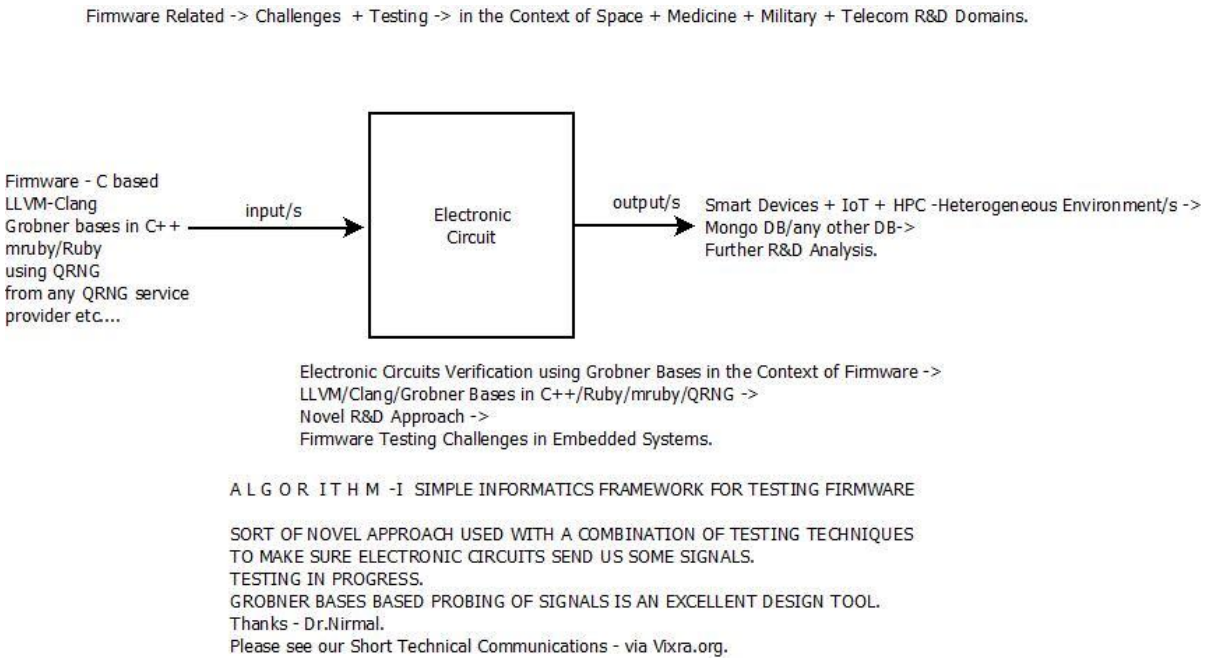
[I] Abstract + Main Idea + Inspiration :

Better Firmware with LLVM/Clang - https://interrupt.memfault.com/blog/arm-cortexm-with-llvm-clang#using-gcc-and-clang

[II] Informatics Framework Using Above Mentioned Software Tools + Grobner Bases :



[Figure I – A Simple Test Bed for Testing Firmware for Mission Critical Applications]

[C/C++/Ruby/mruby -> Excellent Combination]

[III] Important References :

[a] https://vixra.org/pdf/1907.0306v1.pdf

[b] https://arxiv.org/pdf/2007.15876.pdf

[c] https://developers.redhat.com/blog/2020/01/20/mir-a-lightweight-jit-compiler-project/

[d] https://github.com/ruby-llvm/ruby-llvm

[e] https://llvm.org/pubs/2004-Spring-AlexanderssonMSThesis.html

[f] https://www.photonics.com/Articles/Related_Quantum_Random_Number_Generator_Service/ar45147

[g] https://qrng.physik.hu-berlin.de

[h] https://github.com/k0kubun/llrb

[i] https://github.com/tejdnk-2019-ShortNotes

[IV] Acknowledgment/s :

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[THE END]