An insight into Firmware Generation [relocatable code] & Testing w.r.t ARM Processors Using : : -> e prover + llvm + MLGO + AIfES based Options on Smart Devices + IoT + HPC Heterogeneous Systems - A Simple Suggestion/Short Technical Communication.

by

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[I] Main Idea + Inspiration + Introduction :

Generating relocatable code for ARM processors

By Pavel Loktev ; Oct 1, 2021 ; #compiler ; 17 minute read.

https://blog.llvm.org/posts/2021-10-01-generating-relocatable-code-for-arm-processors/

https://arxiv.org/pdf/2101.04808.pdf -> MLGO: a Machine Learning Guided Compiler Optimizations Framework.

https://github.com/tejdnk-2019-ShortNotes/tejdnk-Space-Medicine-Informatics-github.io/blob/master/AVNET-U96-Ruby-Nir-21.pdf

https://wwwlehre.dhbw-stuttgart.de/~sschulz/E/E.html -> e theorem prover.

Check Machine Learning w.r.t LLVM/MLGO Algorithms.

Check AIfES -> Micro controllers - ARM etc..

We are writing -> Short Notes.

We are using e prover + Other Tools.

Details are not shown here.We have some interest/s.

Rigorous Testing & Understanding in Progress @ the TIME of Submission.

[THE END]