Stencil Vectors + Dr.Racket + AIfES to TEST/VERIFY Novel Algorithms w.r.t AI based Embedded Systems -> involving : Smart Devices [SD] + IoT + HPC Heterogeneous Systems.

Exploring : Hash Array Mapped Tries (HAMTs) + Testing of AI based Embedded Systems in IoT Computing Environments.

Just Fine Tune our Algorithms from our ref[c] -> Could be very much useful in your R&D Works.

Dr.Nirmal - Informatics R&D - Current Member - antE Inst UTD Dallas TX USA - email id : hmfg2014@gmail.com

[I] Idea + Implementation :

Please generate your own Framework based on our Algorithms.

[II] References :

[a] https://www.cs.utah.edu/plt/publications/dls21-tzf.pdf

[b] https://docs.racket-lang.org/reference/stencil_vectors.html#%28def._%28%28quote._~23~25kernel%29._stencil-vector-set%21%29%29

[c] https://github.com/tejdnk-2019-ShortNotes/tejdnk-Space-Medicine-Informatics-github.io/blob/master/AVNET-U96-Ruby-Nir-21.pdf

[III] Conclusion/s + Future Perspectives :

Very active R&D Topic -> Could be useful to TEST Firmware on Smart Devices + IoT + HPC Hetero Systems.

[IV] Acknowledgment/s :

Non-Profit R&D - Inspire others always - Sicnere Thanks to all WHO made this happen in my LIFE.

[THE END]