Final Project "Bid" Sheet

Instructions:

The purpose of this form is to get you to think in advance about your project, plan some parts ahead, and think deeply about the scope and focus of your project. This isn't a contract - but I want you to focus on aspects that you've committed to on this form before adding new features. This will also give me a chance to confirm the points you are seeking, and provide feedback about scope and challenge.

1. **Make a copy of this form**
2. **Start by filling out the gradesheet below, adding rows as necessary to describe added features of your processor. (NOTE: if you are doing the ARM version, you'll need to create a new score sheet.)**
3. **In the subsequent page, explain how you'll TEST each component. How will you know it works? How will you prove that it works?**
4. **submit the complete sheet as a PDF**

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Processor Name: Sarojni vich kampyooter x16

Processor Description: (i.e. 16-bit single CPU with branch support)

Score Sheet

Feature	Expected Value	Expected Completion
Basic: ALU	10	10
Basic: R and I type	10	10
Basic: LW and SW	10	10
Basic: Assembler	20	20
Basic: Writeup	20	20
A la Carte (add rows below as necessary)		
16-bit CPU	5	5
beq	10	10
jump	10	10
slt/sgt	5	5
Fancy-Assembler Linker	25	12
Total	125	112

Testing: For each feature listed above, explain HOW you'll test it, how you'll know it works, and how you'll prove (to yourself and to me) that it works.

For each of the features up till slt/sgt, run the corresponding code provided in the Project Test File, and see if we get the expected output.

As for the fancy-assembler linker testing, we will modify the test files given to include labels instead of explicit offsets, and see if we get the expected results still, and then also add some blt, bgt, bge, ble instructions, making sure we test for edge cases (where both operands are equal).