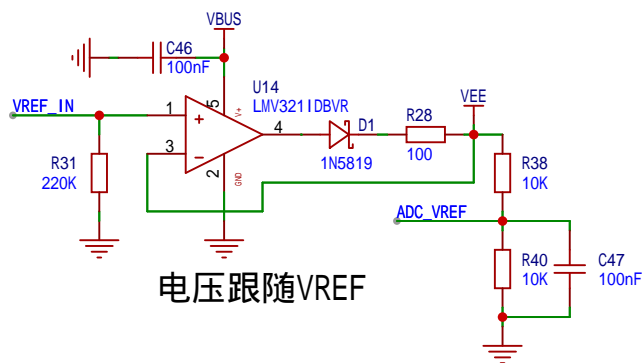
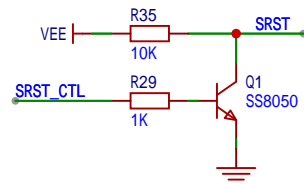
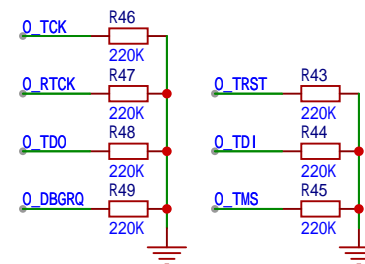
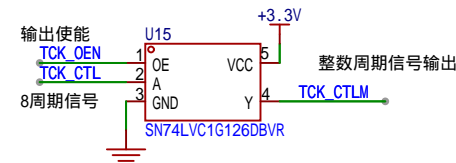
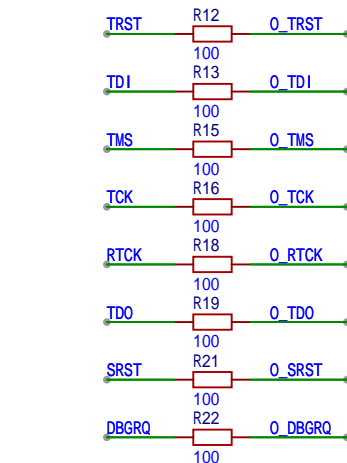
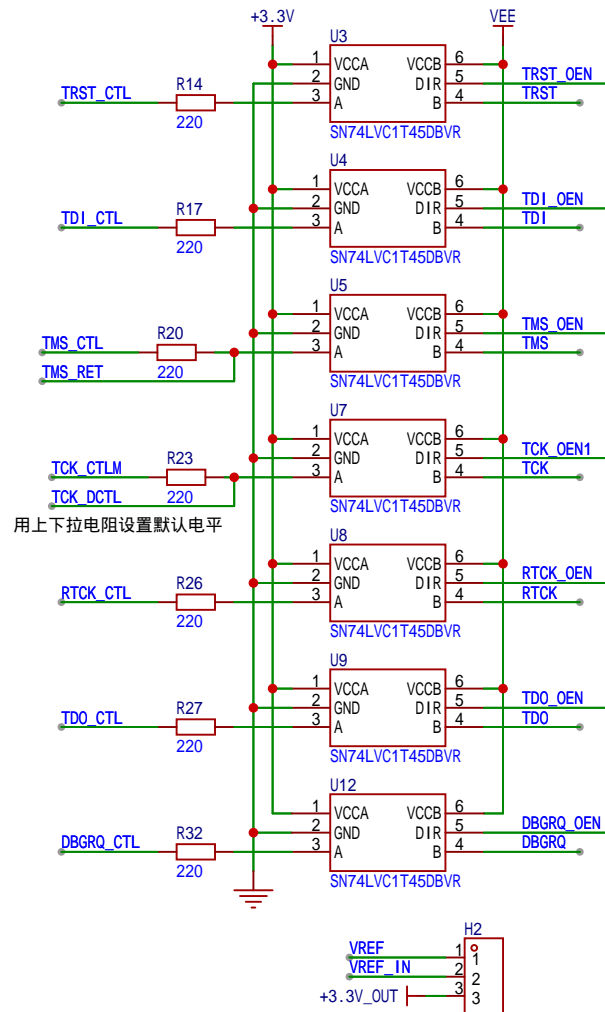


VBUS输出限流



电压跟随VREF



原理图	Schematic1_2			更新日期	2024-11-22
图页	P2			创建日期	2024-11-14
绘制	HDAP_v5			物料编码	
审阅					
嘉立创EDA		版本	尺寸	页 2 共 2	
		V1.0	A4	嘉立创EDA	