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Function Description

Before reading this document, you need to know what Telink primary link layer(TPLL) is.

This document is used to tell users about the use of TPLL PRX mode and there is a simple example in this document. Through this document, you can learn how to receive data in TPLL PRX mode.

This document needs to be used with the TPLL PRX mode demo (tpll_prx demo). The function of the TPLL PRX mode demo is to receive data from the transmitter which in the TPLL PTXmode.

Platform

if you want to receive data in TPLL PRX mode, you need to configure the TPLL PRX mode environment as follows.

Hardware Platform

- Telink TLSR8258 EVK(C1T139A30_V1.2)
- Telink Burning EVK(V1.0.0.0)

Note: You need an external antenna to work normally.



Figure 1 Telink burning EVK



Figure 2 Telink TLSR8258 EVK

Software Platform

- Telink Burning and Debugging Tool
- TPLL PTX software (TPLL_PRX.bin)

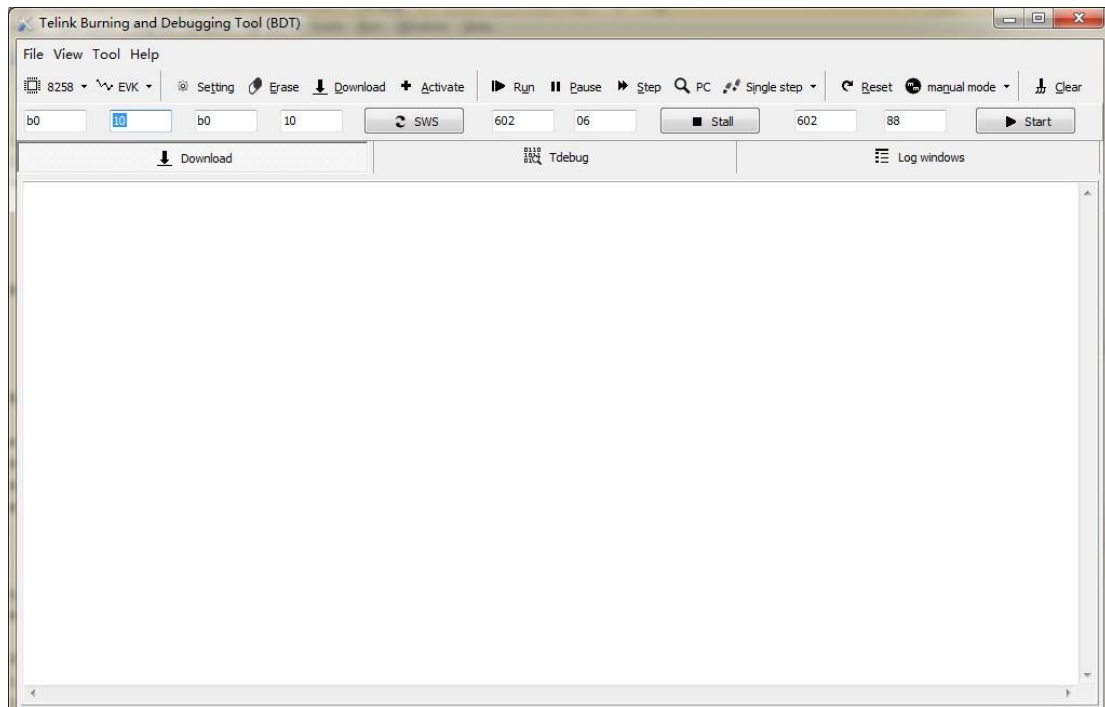


Figure 3 Telink Burning and Debugging Tool

Burning Step

Step1

Connect Telink Burning EVK and Telink TLSR8258 EVK with an usb cable, then connect with PC.
 Note: you also need connect Telink Burning EVK SWM pin and Telink TLSR8258 EVK SWS pin with a wire.

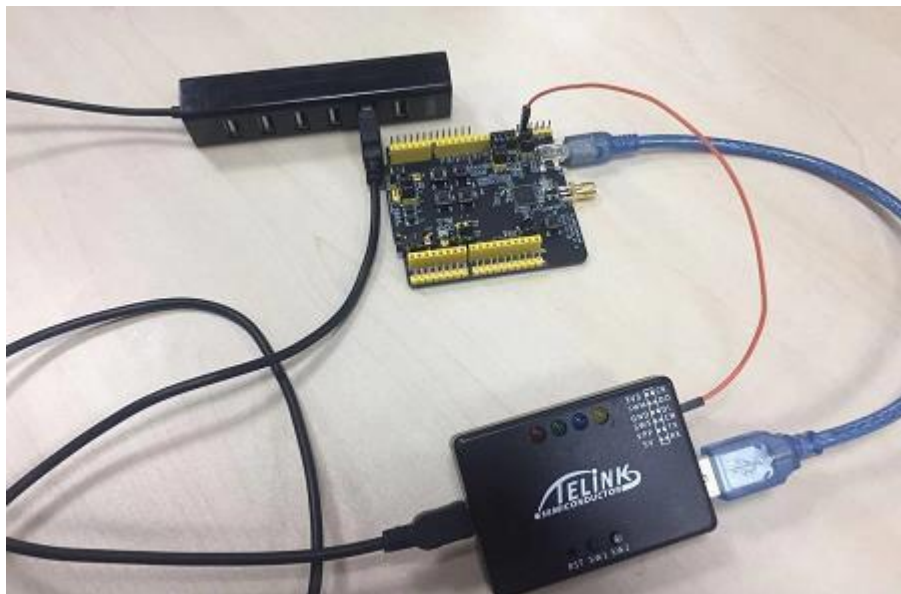


Figure 4 Connect way

Step2

Open Telink Burning and Debugging Tool (BDT) and select 8258 chip, then click on SWS. If BDT shows “no evk device!”, you should check whether the hardware connection is correct. If BDT shows “Swire ok!” that means hardware is ok.

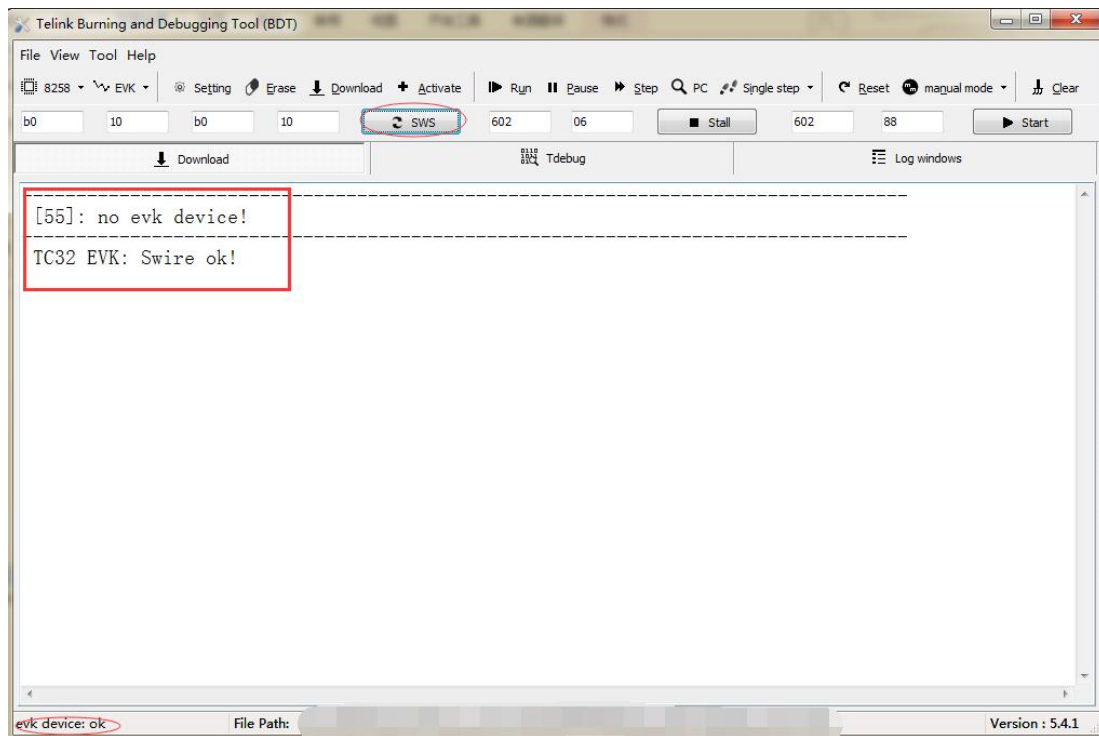


Figure 5 BDT information

step3

Burning the program to the 8258 TLSR8258 EVK. You should click on "file" button to select your target bin file, then click on “Download” button, it will show download information correctly. Whereas, if the BDT shows “Swire err! ”, you need check your hardware or click on "Active" button ,it may solves your problem!

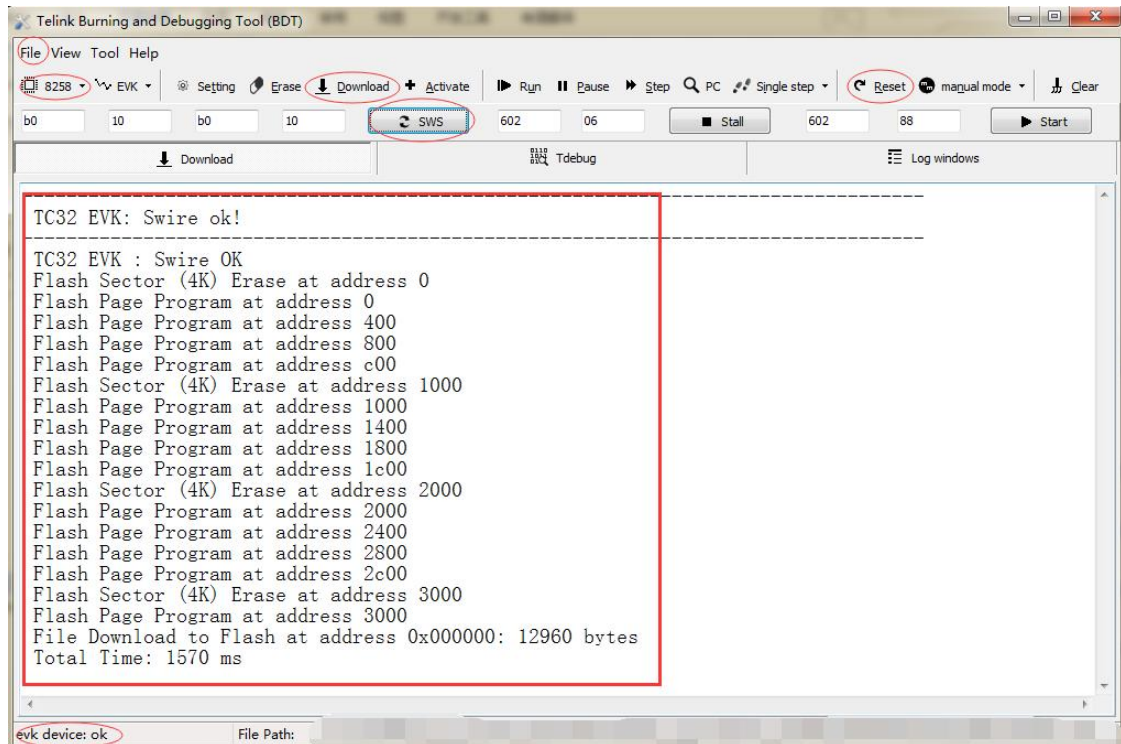


Figure 6 BDT information

Code Structure

System initialization

Include cpu initialization, read calibration value, clock initialization and user initialization.

```
cpu_wakeup_init(LDO_MODE, EXTERNAL_XTAL_24M);

user_read_flash_value_calib();

clock_init(SYS_CLK_24M_Crystal);

User_Init(4);
```

RF configuration

We need to set the basic parameter of RF before we receive the data in TPLL PRX mode

```
//rf configuration
TPLL_Init(TPLL_BITRATE_2MBPS);
TPLL_SetOutputPower(TPLL_RF_POWER_0DBM);
TPLL_SetAddressWidth(ADDRESS_WIDTH_5BYTES);
TPLL_ClosePipe(TPLL_PIPE_ALL);
#if PTX_CHANNEL == 0
    unsigned char rx_address[5] = {0xe7,0xe7,0xe7,0xe7,0xe7}; // {0xe7,0xe7,0xe7,0xe7,0xe7};
    TPLL_SetAddress(TPLL_PIPE0, rx_address);
    TPLL_OpenPipe(TPLL_PIPE0);
#endif
```

```
TPLL_ModeSet(TPLL_MODE_PRX);
TPLL_SetRFChannel(chn);
TPLL_TxSettleSet(149);
TPLL_RxSettleSet(80);
```

API *TPLL_Init* is used for setting RF bitrate, we provide 4 bitrate options, 2Mbps, 1Mbps, 500kMbps, 250kMbps. API *TPLL_SetOutputPower* is used for setting RF emission power.

API *TPLL_SetAddressWidth* is used for setting address width, you can select 3-5 bytes address width to set.

API *TPLL_SetAddress* is used for setting address and pipe. You need API *TPLL_OpenPipe* to open pipe after setting address and pipe.

API *TPLL_SetTXPipe* is used for launching pipe.

API *TPLL_ModeSet* is used to set TPLL mode, we have two mode to choose, PTX mode and PRX mode.

API *TPLL_SetRFChannel* is used for setting RF channel, notice that the unit of channel is 500kHz.

Other APIs are used to set up the RF basic configuration. Note: This version of 8258 only has pipe0.

TPLL packet setting

Use API *TPLL_PRXTrig* to start receiving data when rf configuration is complete.

```
TPLL_PRXTrig();
```

API *TPLL_ReadRxPayload* is used for receiving data. API *TPLL_GetTimestamp* is used for getting instantaneous value of the system tick counter when the sync word is successfully correlated. If you want to send data to PTX, you need to set pipe, data and data length immediately after receiving the data using the API *TPLL_WriteAckPayload*.

```
if(1 == rx_flag)
{
    rx_flag = 0;
    length_pip_ret = TPLL_ReadRxPayload(&rx_data);
    Rx_interval_us = (TPLL_GetTimestamp() - Timestamp_value) >> 4;
    Timestamp_value = TPLL_GetTimestamp();
    Rssi_value = TPLL_GetRxRssiValue();
    while(!TPLL_TxFifoEmpty(0));
    TPLL_WriteAckPayload(TPLL_PIPE0, ack_payload, ack_payload_length);
}
```

Result analysis

We can debug with BDT.

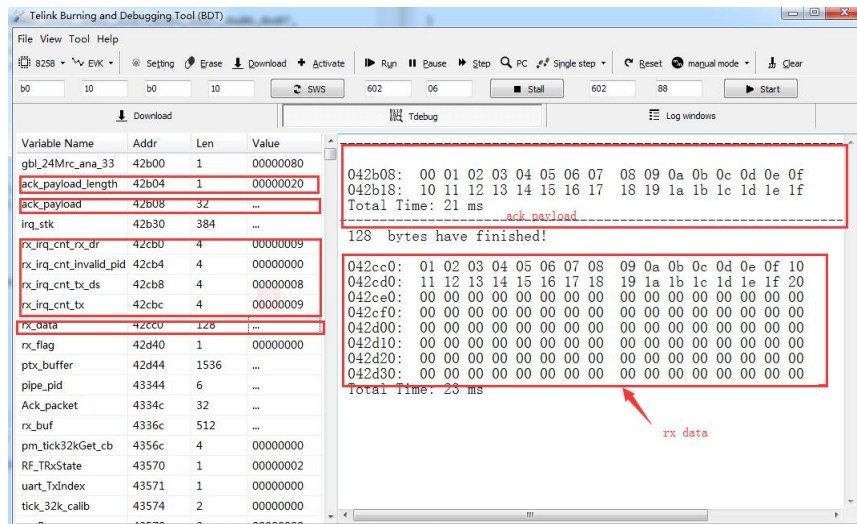


Figure 7 PRX debug information

From Figure 7, you can know what data you have received, how many ack payload times you have sent, how many data times you have received and so on.