core

Rev0:
1. initial rev

Rev1:
1. change the footprint of U4 to MODULE_LINUX3
2. change the thickness of the board to 1mm
3. adjust GPIO

Rev2:
  1. Add EXT_DATA_WR# FOR   NC

Rev3:
    Done: RTC power checked, same as linux, looks good
    Done: Delete NCBOT 74HC02 , move EXT_DATA_WR#&LE1 to the board's 74HC02 .
    Done: connected WAKEUP/INT0 to USB chip PIN1

Rev4:

    Done: change   25MHZ crystal   footprint   from SMT to DIP .
    Done: add GPIOs to CONN 144

Rev5:

     TBD: keep I/O used same with NC REV21, software the same as NCREV21
     TBD: Delete   DEBUG connector , no need
     TBD: change USB+ from Gold fingere A16 to B49,
           USB- from Gold fingere A17 to B48, keep the same with Linux board .
     TBD: change GP22 (P2_6) from Gold fingere B61 to B43, because of B61 is RESET .
     TBD: change DALI_READ from Gold fingere A22 to A26, because of A22 is Linux board's LDATA14.
     TBD: change DALI_WRITE from Gold fingere A23 to A27, because of A23 is Linux board's LDATA15.