











CM5

REV 0

Done: Copy Tstat5 Base Board

REV 1

```
Done: connect Rx2,TXEN1,TXEN2
done: change Footprint for RAM chip
done: change Footprint for U15,U17
done: KYFARNHIGH and WP_MEMORY are unconnected, connect with GND
done: add COM2 for ISP
done: modify the power of U12 and U13, from "-5v" to "5v"
done: put U12 to bottom board, so inverse the connector
done: connect 485- and NET_MINUS
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REV 2

```
Done: connect TXEN1
done: modify the pin of RAM chip( pin 11 and pin12)
done: add KEYFAD_LATCH, change LOW_VOLTAGE pin
done: KEYFADRIGH and WF_MEMORY are unconnected, connect with GND
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REV 3

Done: add 4 test resistors done: add keaypad_latch, TX_EN1

REV 4

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Done: modify output( Relay9 & Relay10)
Done: add backlight_enable pin
Done: change 4 test resistors to jumper
Done: modify J30
DONE: put thermostats in correct order on the bottom board
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REV 5

DONE: put thermostats in correct order on the bottom board DONE: put thermostats in correct order on the top board DONE: make sure J130 re485 jumper is in up/down orientation, same as thermostats TBD: add an on/off slider switch for power done: modify the relay latch done: change the relay9 and relay 10 DONE: change the relay9 and relay 10 DONE: change the LCD TBD: make the PCB outline top,bottom, left and right to inside about 0.5mm DONE: modify J6, don need 6pin, 4 pin is enough DONE: add socket (ms5940 or maix)

REV 5

```
done: modify the relay latch
DONE: change the LCD
TBD: add an on/off slider switch for power
TBD: make the PCB outline top,bottom, left and right to inside about 0.5mm
DONE: add socket (mm5964 or asix)
```



