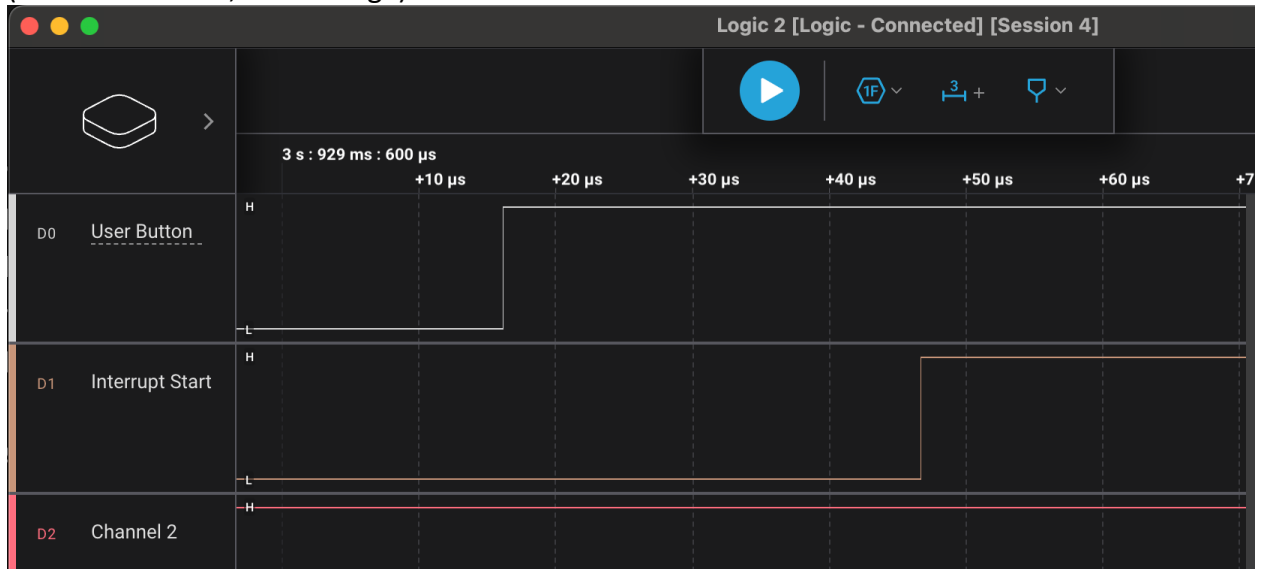Lab 2 Postlab

1. Because PC0 and PA0 are both mapped to the same external interrupt/event line, EXTI0. And only one interrupt request can be sent through an EXTIx line at a time.

2. There are four configurable levels, from 0-3. 0 is the highest priority and 3 is the lowest priority.

3. 8 bits are reserved for each interrupt, and two bits are implemented to determine its priority.

4. 30 microseconds between PA0 high (user button press, D0 in image) and green LED high (LED switched on, D1 in image).

5. If you don't then the interrupt will never know to return.