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ECE5780

<center>Lab 6 Postlab</center>

1. 1.25 Khz = 1/800 microsecond period
2. 3V/4096 = .0007324V a step. If the voltage was 1.75V then that would equate to an integer of 1.75/.0007324 = 2389.4 which rounded to nearest integer is 2389.

For the logic analyzer portion of the lab handout, I will have to get an image using an oscilloscope and pass it off with Bailey in lab tomorrow. Here is a picture of the digital output on channel one (output of PA4 for the DAC portion) in the interim which at least shows we are getting some highs and lows on pin PA4 with an expected frequency. I will upload a picture to the comments of the submission tomorrow during Wednesday lab.