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Education

Apr. 2015 – present	Ph.D. student in Engineering, Kyushu University, Fukuoka Advisor: Prof. Koji Inoue
Apr. 2010 - Mar. 2012	Master of Information Science and Technology, The University of Tokyo, Tokyo Advisor: Prof. Hiroshi Nakamura
Apr. 2006 - Mar. 2010	Bachelor of Engineering, The University of Tokyo, Tokyo Advisor: Prof. Hiroshi Nakamura

Work Experience

Apr. 2012 – Mar. 2015	Researcher at Fujitsu Laboratories Limited, Kawasaki, Japan I designed and prototyped both hardware and software for inter-node communication system for UNIX server products.

Referred Publications

1. Hiroshi Sasaki, Fang-Hsiang Su, Teruo Tanimoto, and Simha Sethumadhavan, "Heavy Tails in Program Structure," IEEE Computer Architecture Letters , vol.PP, no.99, pp.1-1.

2. Takatsugu Ono, Yotaro Konishi, Teruo Tanimoto, Noboru Iwamatsu, Takashi Miyoshi and Jun Tanaka, "A Flexible Direct Attached Storage for a Data Intensive Application", IEICE Transaction on Information and Systems, Vol.E98-D, No.12, pp.2168-2177.

3. Takatsugu Ono, Yotaro Konishi, Teruo Tanimoto, Noboru Iwamatsu, Takashi Miyoshi and Jun Tanaka, "FlexDAS: A Flexible Direct Attached Storage for I/O Intensive Applications", In Proceedings of IEEE International Conference on Big Data (IEEE BigData '14), pp.147-152, Oct. 2014.

4. Teruo Tanimoto, Takatsugu Ono, Kohta Nakashima and Takashi Miyoshi, "Hardware-assisted Scalable Flow Control of Shared Receive Queue", In Proceedings of the 28th ACM International Conference on Supercomputing (ICS '14), pp.175-175, Jun. 2014. (poster session)

5. Hiroshi Sasaki, Teruo Tanimoto, Koji Inoue, and Hiroshi Nakamura, "Scalability-based Manycore Partitioning", In Proceedings of the 21st International Conference on Parallel Architectures and Compilation Techniques (PACT '12), pp.107-116, Sep. 2012.

Research Interests

Computer architecture, hardware/software co-design, interconnects, operating systems