

# Teruo Tanimoto

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## Education

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| Apr. 2015 – present   | Ph.D. student in Engineering,<br>Kyushu University, Fukuoka<br>Advisor: Prof. Koji Inoue                           |
| Apr. 2010 - Mar. 2012 | Master of Information Science and Technology,<br>The University of Tokyo, Tokyo<br>Advisor: Prof. Hiroshi Nakamura |
| Apr. 2006 - Mar. 2010 | Bachelor of Engineering,<br>The University of Tokyo, Tokyo<br>Advisor: Prof. Hiroshi Nakamura                      |

## Work Experience

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| Apr. 2012 – Mar. 2015 | Researcher at Fujitsu Laboratories Limited, Kawasaki, Japan<br>I designed and prototyped both hardware and software for an inter-node communication system for UNIX server products. |
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## Refereed Publications

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1. Teruo Tanimoto, Takatsugu Ono, Koji, Inoue, Hiroshi Sasaki, “Enhanced Dependence Graph Model for Critical Path Analysis on Modern Out-of-Order Processors,” IEEE Computer Architecture Letters , vol.PP, no.99, pp.1-1, Mar. 2017.
2. Hiroshi Sasaki, Fang-Hsiang Su, Teruo Tanimoto, and Simha Sethumadhavan, ”Heavy Tails in Program Structure,” IEEE Computer Architecture Letters , vol.PP, no.99, pp.1-1, May 2016.
3. Takatsugu Ono, Yotaro Konishi, Teruo Tanimoto, Noboru Iwamatsu, Takashi Miyoshi and Jun Tanaka, “A Flexible Direct Attached Storage for a Data Intensive Application”, IEICE Transaction on Information and Systems, Vol.E98-D, No.12, pp.2168-2177, Dec. 2015.
4. Takatsugu Ono, Yotaro Konishi, Teruo Tanimoto, Noboru Iwamatsu, Takashi Miyoshi and Jun Tanaka, “FlexDAS: A Flexible Direct Attached Storage for I/O Intensive Applications”, In Proceedings of IEEE International Conference on Big Data (IEEE BigData ‘14), pp.147-152, Oct. 2014.
5. Teruo Tanimoto, Takatsugu Ono, Kohta Nakashima and Takashi Miyoshi, “Hardware-assisted Scalable Flow Control of Shared Receive Queue”, In Proceedings of the 28th ACM International Conference on Supercomputing (ICS ‘14), pp.175-175, Jun. 2014. (poster session)
6. Hiroshi Sasaki, Teruo Tanimoto, Koji Inoue, and Hiroshi Nakamura, “Scalability-based Manycore Partitioning”, In Proceedings of the 21st International Conference on Parallel Architectures and Compilation Techniques (PACT ‘12), pp.107-116, Sep. 2012.

## Research Interests

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Computer architecture, hardware/software co-design, interconnects, operating systems