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# SignalCore RF Source DLL driver
import sys
import time
import ctypes
import types
import numpy as np
import logging
SUCCESS = 0
NO DEVICE = 0
# 64 bit API
LB DLL = C:\ install dir\ SignalCore\ x64\ sc5511a.dll'
try:
  lb_dll = ctypes.windll.LoadLibrary(LB_DLL)
  #lb_dll = ctypes.cdll.LoadLibrary(LB_DLL)
except Exception, e:
  s = 'Unable to load SignalCore DLL, please put sc5511a.dll in instrumentserver directory ' + str(e)
  raise ValueError(s)
class device rf params t(ctypes.Structure):
  _fields_ = [('rf1_freq', ctypes.c_ulonglong),
            ('start_freq', ctypes.c_ulonglong),
            ('stop_freq', ctypes.c_ulonglong),
            ('step_freq', ctypes.c_ulonglong),
            ('sweep_dwell_time', ctypes.c_uint),
            ('sweep_cycles', ctypes.c_uint),
            ('buffer points', ctypes.c uint),
            ('rf_level', ctypes.c_float),
            ('rf2_freq', ctypes.c_ushort)]
  def print_params(self):
    print('\ndevice rf params:\n'
        + 'rf1_freq = ' + str(self.rf1_freq) + '\n'
        + 'start_freq = ' + str(self.start_freq) + '\n'
        + 'stop_freq = ' + str(self.stop_freq) + '\n'
        + 'step freq = ' + str(self.step freq) + '\n'
        + 'sweep_dwell_time = ' + str(self.sweep_dwell_time) + '\n'
        + 'sweep_cycles = ' + str(self.sweep_cycles) + '\n'
        + 'buffer points = ' + str(self.buffer points) + '\n'
        + 'rf_level = ' + str(self.rf_level) + '\n'
        + 'rf2 freq = ' + str(self.rf2 freq))
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class list_mode_t(ctypes.Structure):
  _fields_ = [('sss_mode', ctypes.c_ubyte),
            ('sweep_dir', ctypes.c_ubyte),
            ('tri waveform', ctypes.c ubyte),
            ('hw trigger', ctypes.c ubyte),
            ('step_on_hw_trig', ctypes.c_ubyte),
            ('return_to_start', ctypes.c_ubyte),
            ('trig_out_enable', ctypes.c_ubyte),
            ('trig_out_on_cycle', ctypes.c_ubyte)]
  def print params(self):
    print('\nlist mode params:\n'
        + 'sss mode = ' + str(self.sss mode) + '\n'
        + 'sweep dir = ' + str(self.sweep dir) + '\n'
        + 'tri_waveform = ' + str(self.tri_waveform) + '\n'
        + 'hw trigger = ' + str(self.hw trigger) + '\n'
        + 'step_on_hw_trig = ' + str(self.step_on_hw_trig) + '\n'
        + 'return_to_start = ' + str(self.return_to_start) + '\n'
        + 'trig_out_enable = ' + str(self.trig_out_enable) + '\n'
        + 'trig out on cycle = ' + str(self.trig out on cycle))
class pll_status_t(ctypes.Structure):
  _fields_ = [('sum_pll_ld', ctypes.c_ubyte),
            ('crs_pll_ld', ctypes.c_ubyte),
            ('fine pll ld', ctypes.c ubyte),
            ('crs_ref_pll_ld', ctypes.c_ubyte),
            ('crs aux pll ld', ctypes.c ubyte),
            ('ref 100 pll ld', ctypes.c ubyte),
            ('ref_10_pll_ld', ctypes.c_ubyte),
            ('rf2_pll_ld', ctypes.c_ubyte)]
  def print params(self):
    print('\npll status params:\n'
        + 'sum pll Id = ' + str(self.sum pll Id) + '\n'
        + 'crs pll Id = ' + str(self.crs pll Id) + ' \ n'
        + 'fine pll Id = ' + str(self.fine pll Id) + ' \n'
        + 'crs_ref_pll_ld = ' + str(self.crs_ref_pll_ld) + '\n'
        + 'crs aux pll ld = ' + str(self.crs aux pll ld) + '\n'
        + 'ref 100 pll ld = ' + str(self.ref 100 pll ld) + '\n'
        + 'ref_10_pll_ld = ' + str(self.ref_10_pll_ld) + '\n'
        + 'rf2_pll_ld = ' + str(self.rf2_pll_ld))
class operate status t(ctypes.Structure):
  fields = [('rf1 lock mode', ctypes.c ubyte),
            ('rf1 loop gain', ctypes.c ubyte),
            ('device access', ctypes.c ubyte),
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('rf2 standby', ctypes.c ubyte),
            ('rf1_standby', ctypes.c_ubyte),
            ('auto_pwr_disable', ctypes.c_ubyte),
            ('alc_mode', ctypes.c_ubyte),
            ('rf1 out enable', ctypes.c ubyte),
            ('ext ref lock enable', ctypes.c ubyte),
            ('ext_ref_detect', ctypes.c_ubyte),
            ('ref_out_select', ctypes.c_ubyte),
            ('list_mode_running', ctypes.c_ubyte),
            ('rf1 mode', ctypes.c ubyte),
            ('over_temp', ctypes.c_ubyte),
            ('harmonic_ss', ctypes.c_ubyte)]
  def print params(self):
    print('\noperate mode status: \n'
        + 'rf1_lock_mode = ' + str(self.rf1_lock_mode) + '\n'
        + 'rf1 loop gain = ' + str(self.rf1 loop gain) + '\n'
        + 'device access = ' + str(self.device access) + '\n'
        + 'rf2_standby = ' + str(self.rf2_standby) + '\n'
        + 'rf1_standby = ' + str(self.rf1_standby) + '\n'
        + 'auto pwr disable = ' + str(self.auto pwr disable) + '\n'
        + 'alc mode = ' + str(self.alc mode) + '\n'
        + 'rf1_out_enable = ' + str(self.rf1_out_enable) + '\n'
        + 'ext ref lock enable = ' + str(self.ext ref lock enable) + '\n'
        + 'ext_ref_detect = ' + str(self.ext_ref_detect) + '\n'
        + 'ref out select = ' + str(self.ref out select) + '\n'
        + 'list_mode_running = ' + str(self.list_mode_running) + '\n'
        + 'rf1 mode = ' + str(self.rf1 mode) + '\n'
        + 'over temp = ' + str(self.over temp) + '\n'
        + 'harmonic_ss = ' + str(self.harmonic_ss))
class device status t(ctypes.Structure):
  _fields_ = [('list_mode_t', list_mode_t),
            ('operate status t', operate status t),
            ('pll_status_t', pll_status_t)]
  def print_params(self):
    self.list mode t.print params()
    self.operate status t.print params()
    self.pll_status_t.print_params()
class date(ctypes.Structure):
  fields = [('year', ctypes.c ubyte),
            ('month', ctypes.c ubyte),
            ('day', ctypes.c ubyte),
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('hour', ctypes.c_ubyte)]
class device info t(ctypes.Structure):
  _fields_ = [('product_serial_number', ctypes.c_ubyte),
         ('hardware revision', ctypes.c float),
         ('firmware_revision', ctypes.c_float),
         ('man_date', date)]
NUM MAX DEVICES = 5
ID BUFFER SIZE = 8
Next, we initialize the device, get the pointers and device handle and things like that correct:
class SC5511A(Instrument):
  def init (self, name, devid=None, serial=None):
    super(SC5511A, self).__init__(name)
    if devid is None:
      raise Exception('SignalCore driver needs devid or serial as parameter')
    string_buffers = [ctypes.create_string_buffer(ID_BUFFER_SIZE) for i in range(NUM_MAX_DEVICES)]
    pointers = (ctypes.c char p*NUM MAX DEVICES)(*map(ctypes.addressof, string buffers))
    results = [s.value for s in string buffers]
    self.dev_num = ctypes.c_char_p(devid)
    lb_dll.sc5511a_open_device.restype = ctypes.POINTER(ctypes.c_int)
    self._handle = lb_dll.sc5511a_open_device(self.dev_num)
    device_rf_params = device_rf_params_t()
    device status = device status t()
    device status temp = ctypes.pointer(device status)
    lb_dll.sc5511a_get_device_status(self._handle, device_status_temp.contents)
    lb dll.sc5511a get rf parameters(self. handle, device rf params)
    lb_dll.sc5511a_set_rf_mode(self._handle, 0)
    lb_dll.sc5511a_set_output(self._handle, 1)
Then finally we use the functions defined in the API to make our own "get" and "set" functions to
control the device settings or read them out.
  def do_get_frequency(self):
    device_rf_params = device_rf_params_t()
    Ib dll.sc5511a get rf parameters(self. handle, device rf params)
    return float(device rf params.rf1 freq)
  def do set frequency(self, freq Hz):
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return lb_dll.sc5511a_set_freq(self._handle, ctypes.c_ulonglong(int(freq_Hz)))
def do_get_power(self):
  device_rf_params = device_rf_params_t()
  lb_dll.sc5511a_get_rf_parameters(self._handle, device_rf_params)
  return device_rf_params.rf_level
def do_set_power(self, power):
  return lb_dll.sc5511a_set_level(self._handle, ctypes.c_float(power))
def do_get_rf_on(self):
  device_status = device_status_t()
  lb_dll.sc5511a_get_device_status(self._handle, device_status)
  return device_status.operate_status_t.rf1_out_enable == 1
def do_set_rf_on(self, val):
  if(val):
    return lb_dll.sc5511a_set_output(self._handle, 1)
  else:
    return lb_dll.sc5511a_set_output(self._handle, 0)
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