

Lab#2 TRANSLATING ASSEMBLY LANGUAGE TO MACHINE LANGUAGE

MIPS architecture.

Teacher: Gloria Castro Muñoz

Work made by: Christian Aaron Ortega Blanco

Translate the next instructions to machine code and show which type it is.

lw \$t9,-16(\$0)

	l-type								
	lw \$t9,-16(\$0)								
ор	op rs rt imm								
	35 0 25								
	100011 00 000 11001 1111111111110000.0								
	1000-1100-0001-1001-1111-1111-1111-0000								
				0x8C19FF	F0				

sw \$t5,-15(\$t3)

I-Type								
		sw \$t5,-15(\$t3)					
ор	op rs rt imm							
43	43 11 13 -15							
1010 11	1010 11							
	1010-1101-0110-1101 -1111 1111 1111 0001							
		AD6DFFF	1					

nor \$s4,\$s2,\$s1

					1			
R-type								
nor \$s4,\$s2,\$s1								
ор	op rs rt rd shamt funct							
0	0 18 17 20 0 39							
0 000 00	0 000 00							
0000-0010-0101-0001-1010-0000-0010-0111								
0x0251A027								

or \$s1,\$s5,\$s7

R-type								
or \$s1,\$s5,\$s7								
op rs rt rd shamt funct								
0 21 23 17 0 37								
0000 00								
0000-0010-1011-0111-1000-1000-0010-0101								
	0x	02B78825						
	21 10 101	rs rt 21 23 10 101 1 0111 0000-0010-1011-0	or \$s1,\$s5,\$s7 rs rt rd 21 23 17 10 101 1 0111 1000 1	or \$s1,\$s5,\$s7 rs rt rd shamt 21 23 17 0 10 101 1 0111 1000 1 000 00 0000-0010-1011-0111-1000-1000				

and \$s4,\$s5,\$s3

R-type									
and \$s4,\$s5,\$s3									
ор	op rs rt rd shamt funct								
0	0 21 19 20 0 36								
0000 00	0000 00								
0000-0010-1011-0011-1010-0000-0010-0100									
0x02B3A024									

addi \$t0,\$s1,10

	I-type								
	addi \$s1,-10(\$s0)								
ор	op rs rt imm								
	8 17 8 1								
	1000 10001 1000 000000000001010								
	0010-0010-0010-1000-0000-0000-1010								
			0x222800	OA .					

slt \$t8,\$s4,\$s6

R-type									
slt \$t8,\$s4,\$s6									
ор	op rs rt rd shamt funct								
0	0 20 22 24 0 42								
000000	000000 10100 10110 11000 00000 101010								
0000-10100-10110-11000-0000-0010-1010									
	0x0296C02A								

0x00400204 loop: ...
.
.
0x004004e8 j loop

J-type								
jta	0000 0000 0100 0000 0000 0010 0000 0100							
ор	addr							
000010	0000 0100 0000 0000 0010 0000 01							
0000-2	0000-1000-0001-0000-0000-0000-1000-0001							
	0x08100081							

```
0x00400004 loop: ...
.
.
.
0x00400038 bne $t9,
$t3,loop
...
```

I-Type						
bne \$t9,\$t3,loop						
op rs rt imm						
0001 01	0001 01					
0001 0111 0010 1011 1111 1111 1111 0010						
		0x172BFF	F2			

```
0x004004e8 loop: ...
.
.
.
0x00400540 beq $t6,
$s6,loop
...
```

				rs t6=14
		I-Type		rt s6=22
ор	rs	rt	imm	
0001 00	01 110	1 0110	1111 1111 1110 1001	
	0001-0001-1	101-0110-111	11-1111-1110-1001	
		0x11D6FF	E9	