FINITE STATE MACHINES: A DESIGN PATTERN FOR FPGAS AND REACT

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WHY THIS TOPIC?

REACT?

REACT?

```
Greeting = ({ name }) => (

        Hello {name}! Welcome to LCA!

)
```

REACT?

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        Hello {name}! Welcome to LCA!

)
```

<Greeting name={currentName}/>

REACT STATE

```
class Greeting extends React.Component {
  state = { name: '' }
 updateName = event => {
   this.setState({ name: event.target.value })
 render() (
   <div>
      <label for="name">Name</label>
      <input id="name" onChange={this.updateName}/>
     <Greeting name={this.state.name}/>
   </div>
```

Field Programmable Gate Array

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```
always @(posedge sysclk) begin
  red_led <= ~red_led
end</pre>
```

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always @(posedge sysclk) begin
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end</pre>
```

```
# sysclk=sysclk
NET "sysclk" LOC = "C9";
# led_5=red_led
NET "red_led" LOC = "D11" | IOSTANDARD = LVTTL | SLEW = SLOW |
```

1. A finite set of states

- 1. A finite set of states
- 2. An initial state

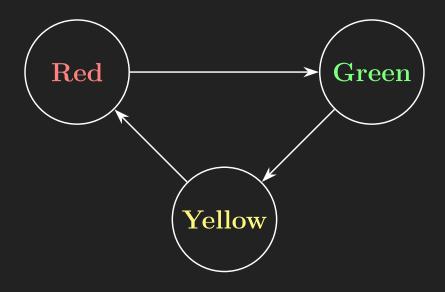
- 1. A finite set of states
- 2. An initial state
- 3. A transition function
 - (state, event) => state

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 - * Not part of official definition

TRAFFIC LIGHTS





• TCP

- TCP
- Bug tracker

- TCP
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- Registration wizard

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- Games

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- ...

WHAT DO THESE HAVE IN COMMON?

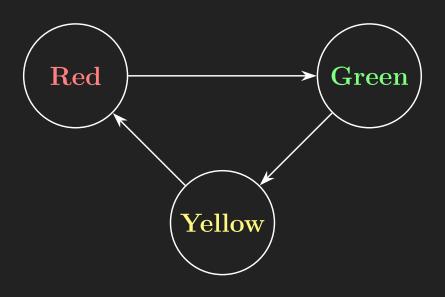
- States make sense
- Different behaviour in different states
- Clear transitions between states

If FSMs are everywhere, why are they so central to React and FPGAs?

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Event orientated systems

TRAFFIC LIGHTS — DEEP DIVE



• Some sort of timer

- Some sort of timer
- Platform dependent

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- Want to abstract that away

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Focus on the transition function

REACT

REACT

```
updateLights = event => {
  this.setState(state => {
   if (state === RED) {
     return GREEN
  } else if (state === GREEN) {
     return YELLOW
  } else if (state === YELLOW) {
     return RED
  } else {
     return state
  }
}
```

VERILOG

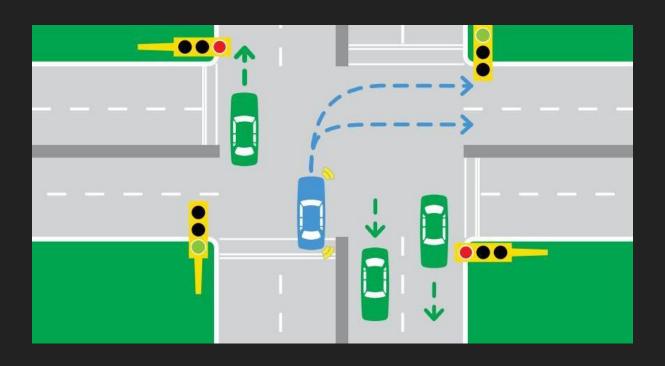
```
always @(*) begin
  if (update lights)
    case(state)
      `RED:
        next state = `GREEN;
      GREEN:
        next state = `YELLOW;
      YELLOW:
        next state = `RED;
    endcase
  else
    next state = state;
end
```

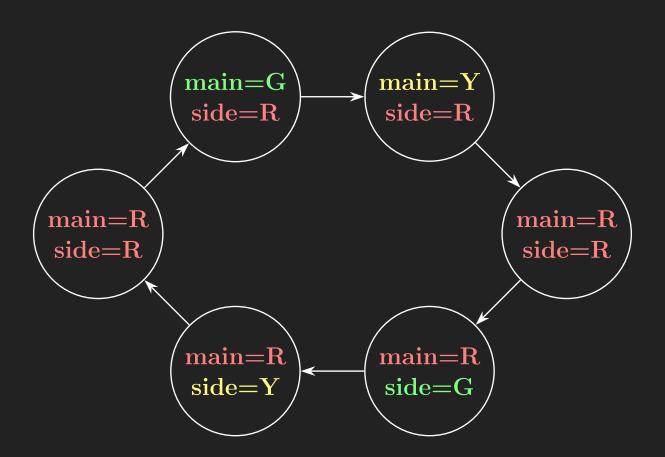
```
always @(posedge sysclk) begin
  state <= next_state
end</pre>
```

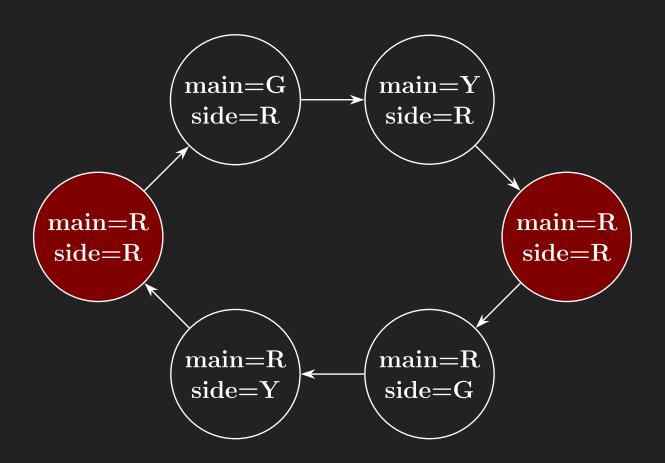
Outputs

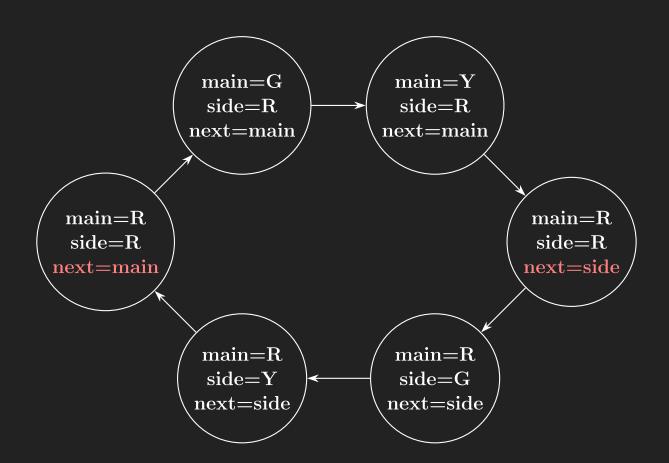
```
assign main_red_light = state == `RED;
assign main_yellow_light = state == `YELLOW;
assign main_green_light = state == `GREEN;
```

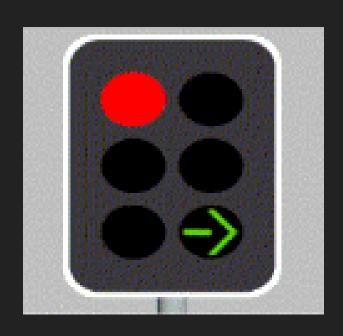
INTERSECTIONS

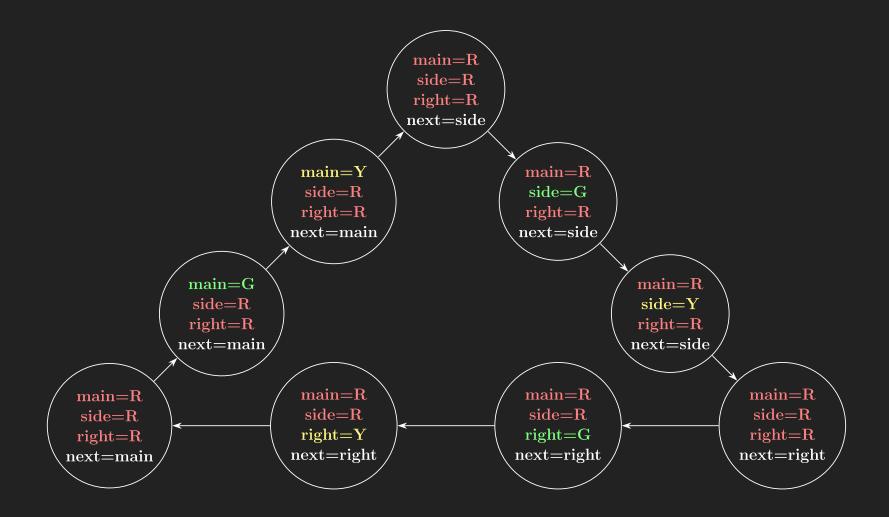












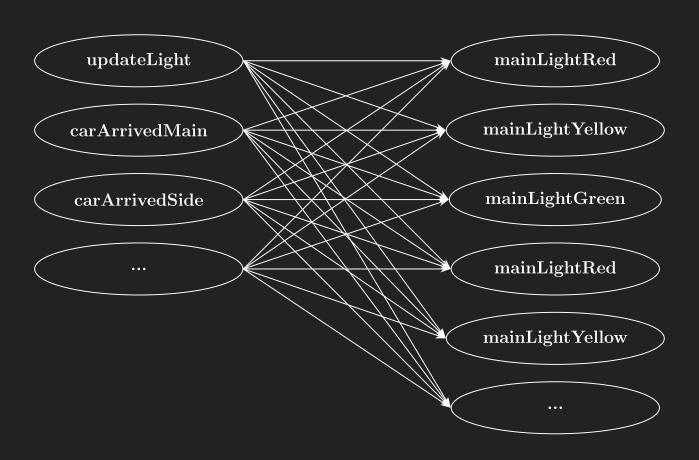
SO WHAT DOES THIS GIVE US?



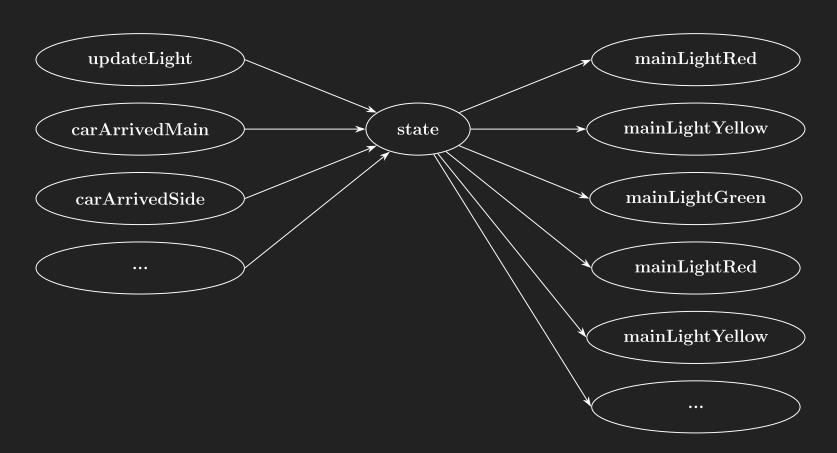
MOAR FEATURES!

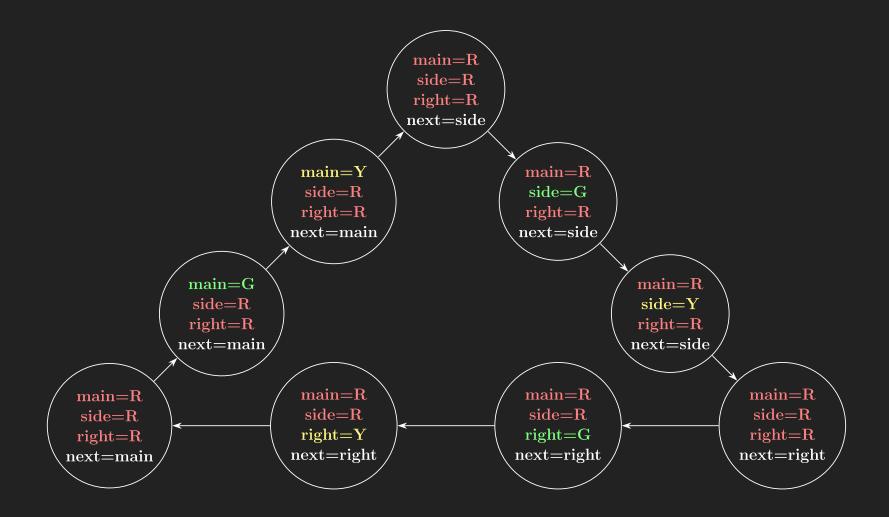
- Pedestrian lights
- Car arrival detection
- Different directions combinations
- ...

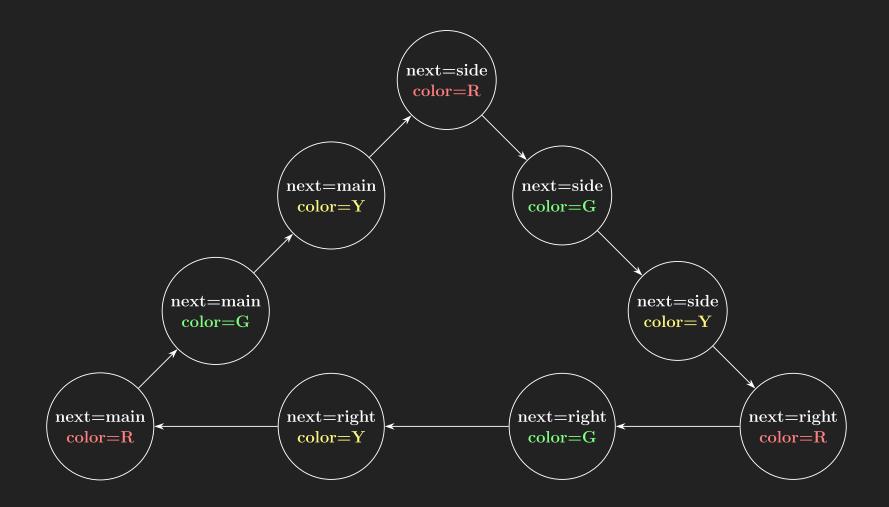
WITHOUT STATE



WITH STATE







REMEMBER

- Be intentional about your
 - States
 - Events
 - Transitions
- Draw a diagram!

Finite state machines provide a useful abstraction to help us reason about the complex behaviour of our systems

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There are a lot more similarities between disciplines than you might think

THANK YOU!

github.com/tessereth/talks