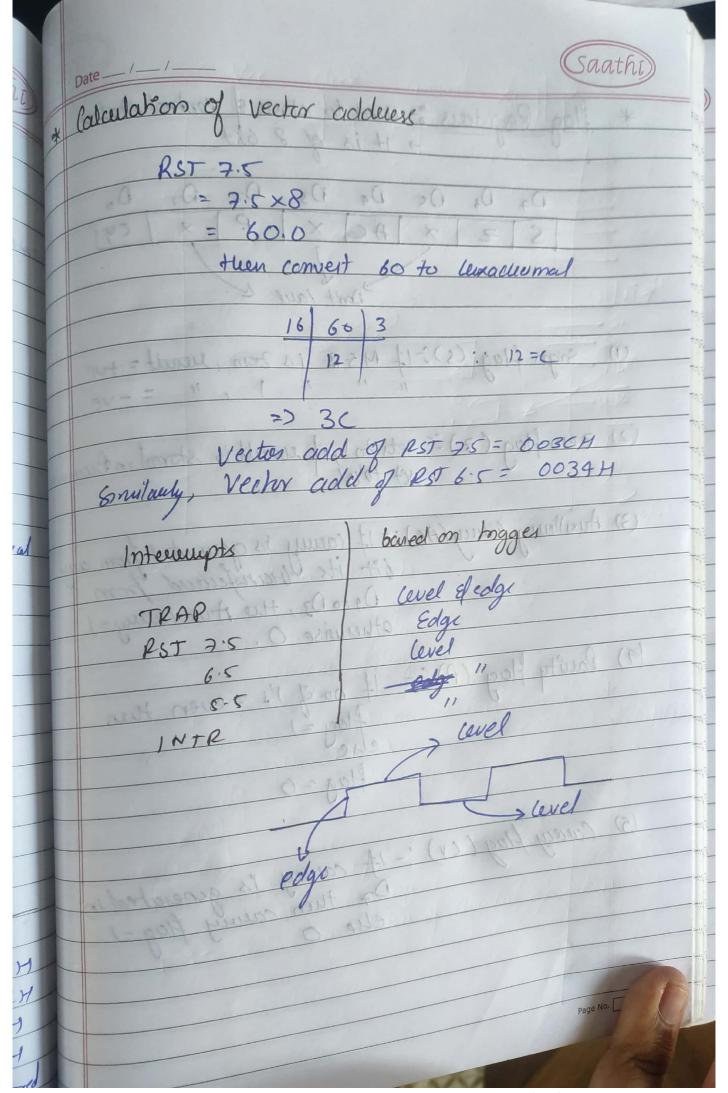
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	ALU -> Performs Arsthmake of lagical operate data encieved from the memory of lupet device.	on the
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6	8 6it data bus	20 = K90
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*	fectures of cel :- (1) Cost effective (2) Reliability (3) Venstald (4) Cow power Commission
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4	features of 8085 MP
*	Teccord of the second of the s
- Ci	40 ptn/C said storm Heron who was to
12	16 bot Address bus
(3)	8 bit data bus
(4	ning tachnology
15	brenieniu = 43 MHZ - 5MHZ)
(6)	Nexadecimal number system
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	* Hearthers was south & the sta
_	- External Architecture Internal Body technul
	2025 UP (40 p80) Marcable
-	Doute hus (ADa to ADa)
1	Address hus (ADO to ADIS)
1-3	Control & status signal (RD, WR, ALE, So, S, Jo/A)
+>	Power supply of frequency signal (Vec, Clock, Ground, X, LX) Externally linkscited signal
4	Externally linkolled common
	11 COON 111 ( 200 CON 114 COON 114 CO
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37 0	orci nie sin bus sin

	ate//_	Vcc	Clock	Ground	, 6	(aathi)
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		2012320 NOSI	Dalalau lala	1 C. 110	100	-SI
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	10.50				
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*	Externally	miliated Signa	10	Caathi	
(1)	TRAP -	Handware Inter Interrupt pag.	William Car	CHZ	
(2)	INTR 3-	Interrupt reg.	CEST 2.5, 6	5,5.5)	
(3)	INTA: Interrupt Acknowledge				
(4)	HOLD :- External Dellishard Live				
1. 24 / 14	HOLD: - External peripheral cluvices sends a Hold may to up, to leave the data				
(5)		- 3- up to ex	Tu daa	2101	
50		Hain John Shahis	remai aurices (m.	nechans.	
637		and Lat		Give	
*	Clock :-	Used for summe	misstian	to the same of the	
*	Clock: - Used for synchronisation  Reset In: - It 9s used to suset Accumulator of ALD				
	Reset out :- used to exest other external periop during				
8	Ready ?	Used to establish	sh a conn flew	slow possible	
199 19	SID & Gental I/o parts				
*	SOD 3 9 COUNTRY OF MANUE				
	Source Comment of the following the				
		penplins	waldelalo		
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	mreagupis	weeken and	Carred or Flancing	Vecror day	
	TRAP (RST 4.5)		Mankeuble .	0029H	
	RST 7.5		Non-Markable	003CH	
	6-5		"	0034H	
	5.5	1	"	002CH	
	INTR	low	2/	Non-vectored	
			Soonnad with Camea	Page No.	



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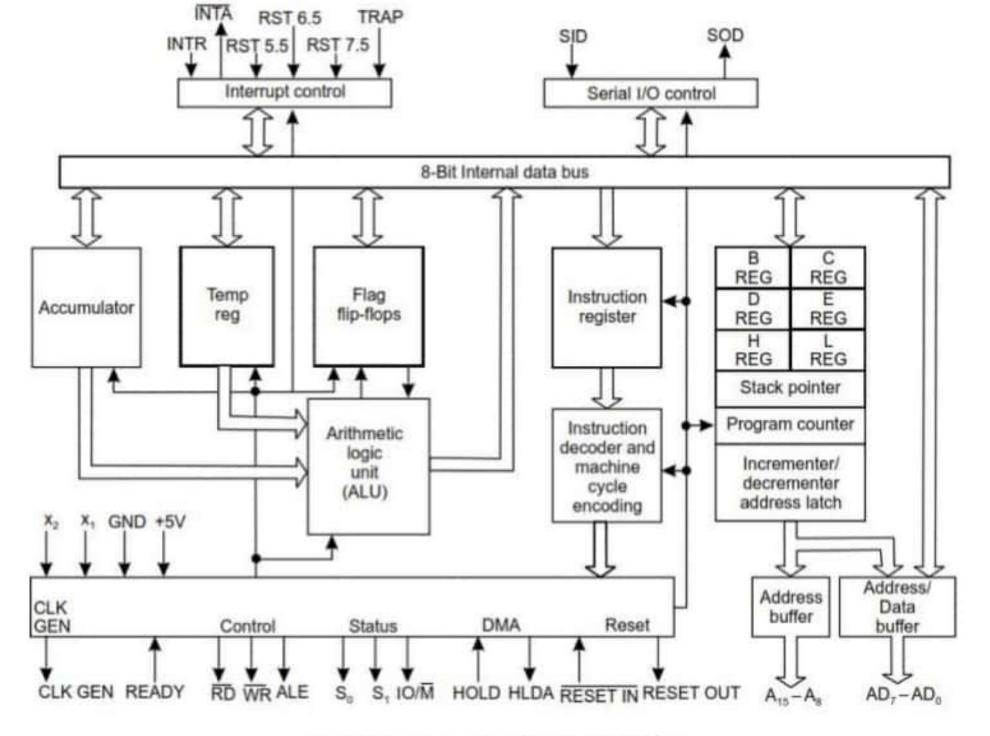
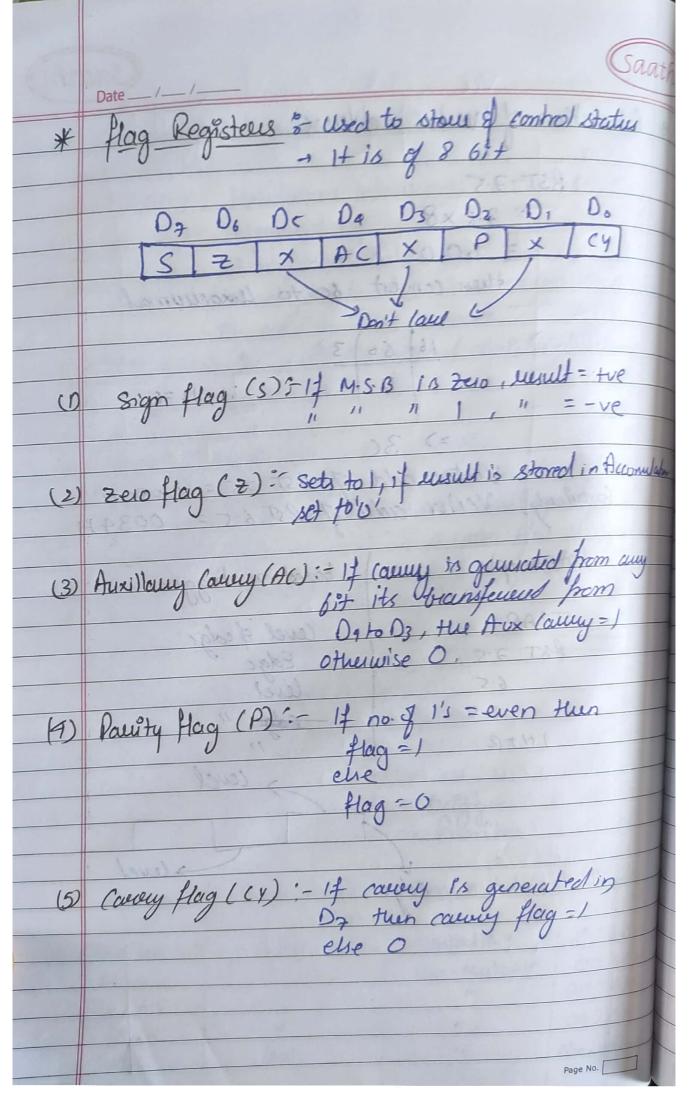


Fig 1.1 Hardware Architecture of 8085



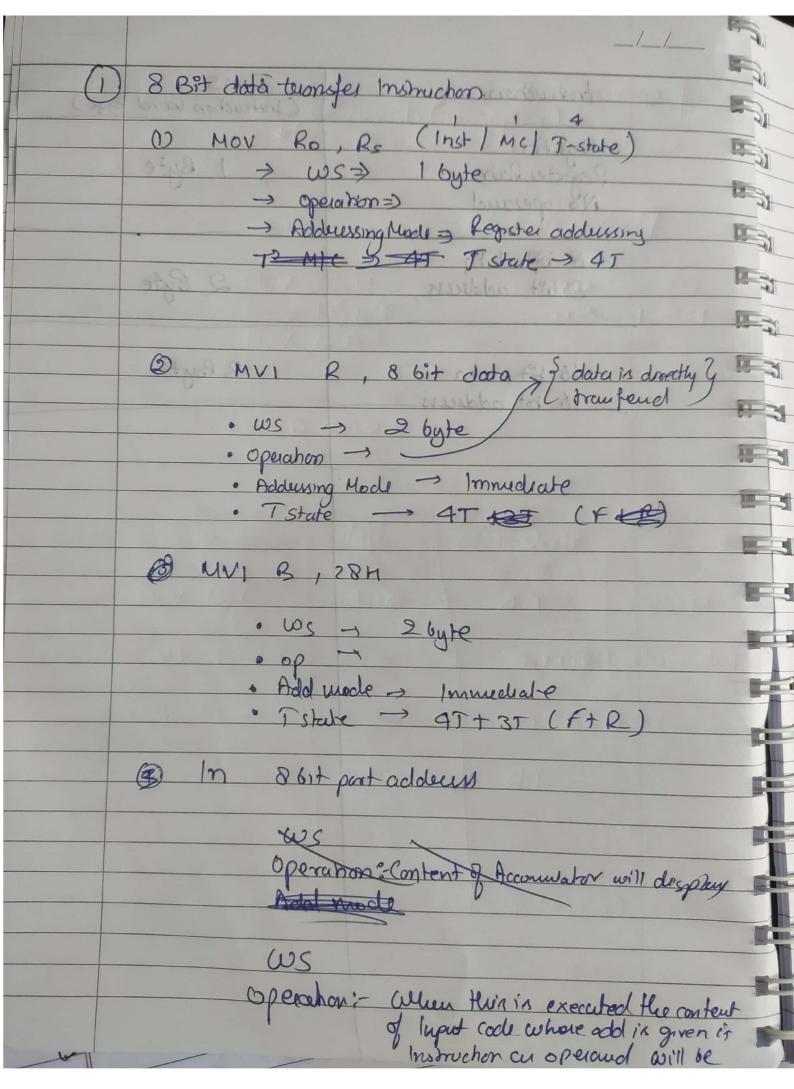
The same of the sa	Intermedia   Book
	Interrupts Garnkar
	Internal A
	Internal Architecture
	State To taline - 3/14 steer manif. 6
The second secon	(F) 1
	Instructions: Commands to the nucleoprocessor to perform tast on specific data.
T	to perform tast on specific dela
13	and as a state of the state of
	Opcode ?- Tank which is
	O Opcode ?- Task which is to be performed.
	O operand: Data which is to be operated upon
	1 state 1 & Atames ) 10 constituted to see 4+
	Instruction word Size: - No. of byte taken by the
	instruction to execute the
	surge la surge operation.
	THE PERSON STORY
	@ Madine Cycle = It is the home taken by the UP
3	Machine Cycle :- It is the home taken by the up to execute an Enstruction
	* lotal thus to execute hochuston
	> Peter Machin Cycli.
3	-> Memory food Machine agele
	→ Memony wonte Machine Cycle  → I/O machine cycle  → Bus Idle Machine cycle
	-> I/0 madine cycle
3	-> Bus Idle Madwine cycle
4 .	* 1 Madein cycle consist of a 3T to 6T
1	
	to Tstates

1000		
The state of the s		
(6)	Fetch Machine Cycle: - Consist of 4T state	
(8)	Manuella Cond Ille o	
	= Consist 3T State	
<u> </u>	Memory write MIC: - Consist 3T state	E 17
6		-
	I/o Read M/C :- Bonsist 3T state	
<b>@</b>	Bus idle M/C: - Only used for (DAD) instruction	
	Bus Idle M/C: - Only used for DAD instruction :- 3T state Soluble Add	
10	or opened of Tank which is to be performed	
-00	@ openued & Data want is to be operated up	-
#	Special Instruction (consists 6T state)	E
		-
1 1 1 0 0s	C:- Call & Return	-
Talt adults	Restant  I = INX & ONX	E
	S:- SPML & PCML	E
	P :- RUSH	
911 134		1
The left	(3) Maddin Capter = 1+ ix the Brus taken by	E
	* Total time to execute Instruction	
	- State of Marketin Confle	
	Total home = nx teix	1
	-> Desurary worther Adordison Gents	
	tax = 11 to willow of p	F
	The finball del mit	
	* 1 Moderns each constat of at 3T to 6T	E
		-
	L 11-2 1	The last

	I'M BILL
3 6	_/_/_
Bu	er The clx freq of 8085 MP 13 5MMz. If the how enguised to execute cur instruction is 1.4 user
	examped 1 8085 MP is 5 MMZ. It the how
	elegured to execute cur instruction is 1.4 uSec
	instruction is
	0) ]
	6) 6
	10) 7
7 49	d) 8
	Stob B museloft & shoot of Hack of Address & date
	J= SMHZ
	1 Ausechen francisco
	1-4×10-6
	M. A. VOM SXS
6	Addressing Mode
	1 iso to make the state provided the such
	The method by which the address of source of
	dota or addenses of decomation of esesult and
	gren in Pribruchens au called ciddessing.
	> Register Addensing : Pata is Stored in engister
1	Register Addensing: Pata is Stored en engister aux
	orden in instruction as operand.
- Ann	geven in mirruchon as operand.
	MOV ABC
	Dan . XX
5	Direct Addressing & @ No engrister is enquired  Directly data given in the inimal
2)	O'CCI O
4	01 60010110
	Ex: MVIA, 24H
4	(w+9+9) E - 2/M
1	
1	( 72 ) TC) (TD)

Direct Addressing mode - Address of data directly given
in Instruction as operand
Ex - 100
Ex: LDA 2 CD A 2500
(Coad into accumulator)
The state of the s
8 (8
Daso, and name of a negister given in Instruction as operand.
pair, and name of a lugister given in himchen
as operand.
EXT MOV B, M
Men will alway point in H2 par sp will
E
H = 90 9010 B
L > 20
12 forget as the leaves of ? told . governable as frages !
enso istropia a to min of a luggister and
a legan to materialism on apparand.
=> Implicit Add Mode :- No operand ?'s engured
59A VOM
Ex: NOP
torringer of 10 fatous all & HCTourstold doubles to
a Directly dates given in the inche
as operand
Mou D, M 2 bytes
MIC + 3 (F+R+W)
QT) (3T (3T)

Instruction I was (Instruction word 80%)
Roofster (Instruction word 87ge)
Register Register Rain No operand  8 bit data
No operand Byte
Marshor so tops & mallowers the c
8 bit data
8 69t address 2 Byte
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16 bit addeus
10 bit addeus
· Optighon -
Storberger Hade Hamedale
(SHA) TENTA - NORT.
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1188 a 8 AVIV & Madelline
940 E + 300 0
THE PART OF THE PA
9-belowned a glocal part of
(Q+A) CAR + AP = alaba
Wassloborton fass sol (8)
W.J.S.W. (150) 1155
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with the Volument of Green Company
200
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	accamulator.	Part water
Add wo	de e- Demont	- 12 ) St
T-State	:- 4T+37:	+3 T (F+R+R)
		The state of the s
THE KING SOUTH F	07.00	ashs
OUT,	8 bit Kost add	ness
100 0103 43 1	Allen H	A VOLA
406 1105 6 H U	25 - 2 by	evol
406 103 64 L	p - 00 A	1 VOM
Ad	d mode - dim	ct (C1 01 0)
T	-state - 9T+	-3T+3T (F+R+R)
	30,0	SHED
		1117
6 HLT		
w <u>c</u>	s-1 byte	1 smidsurfors
Bour boo stoke-1"	2)(4	A turnster.
	1 (1)	Fetch & VOM
IM/c	- (41)	700
	( )	HOCO IVM
( NOP	,	
	US- (AT)	fetch in min
MIO TERRIDIPATE OF		
		904
47.		
	5	NOS NI
Assect (01913) TOI	-	
	5	H 00 700
And (448+8) TO	8	
March 1		

			_/_/_	FA
				FR
0	AIM: To exchau	ign the conter	nts of 2050 (add) of verpping) b nay se manglena	-
	Store the	wesult (Su	vapping)	DED:
		e aggn ka au	bhay so mangana	- 91
0				3
	1 41 0	0200	[ Lood HL Par Load]	
	100	1-21	10 24 12 13	To be
	Mov	AH	2000 703 H 3 2051 →04	- A
	Mov	HCLOD	DON H 3 2051 -> 04	
	Mov	(04)A 00	- 90	
		to - time	A 15 13 13	
19 44	SHLO		210	<b>6</b>
	HUT	3010		
			A HIT	
		otpos!	- 200	edl =
	Instructions	MIC	J-state add um	
	MOV BICHA	(T) (4)	AT Register	
	100		7 T (Fre) / rumeble	h
	MVI 0,20M	2	7 T ( FFR ) ( Williams	
	MUI MI YOM	(74)3 (4	10 Topieno Indirect	-
	NOP	,	47	
	101 0	3	10T (GARAR) Dreet	
	1N 20H	3	10) (8-16-16) Horreen	
	007 20 H	3	OT (Ftete) Direct.	
9				
				E

1		_/_/_
*	write an instruction to display the a	content of
	MOV A, H SULD OUT OON	
Ru	A=97H B= A9H	
•	Course 1  Sorgn	10010111
	AND A, B 81 H	8 1 H [0 20000]
	Sign = 1 $2 = 0$ $AC = 0$ $PF = 1$	
	C = 0	