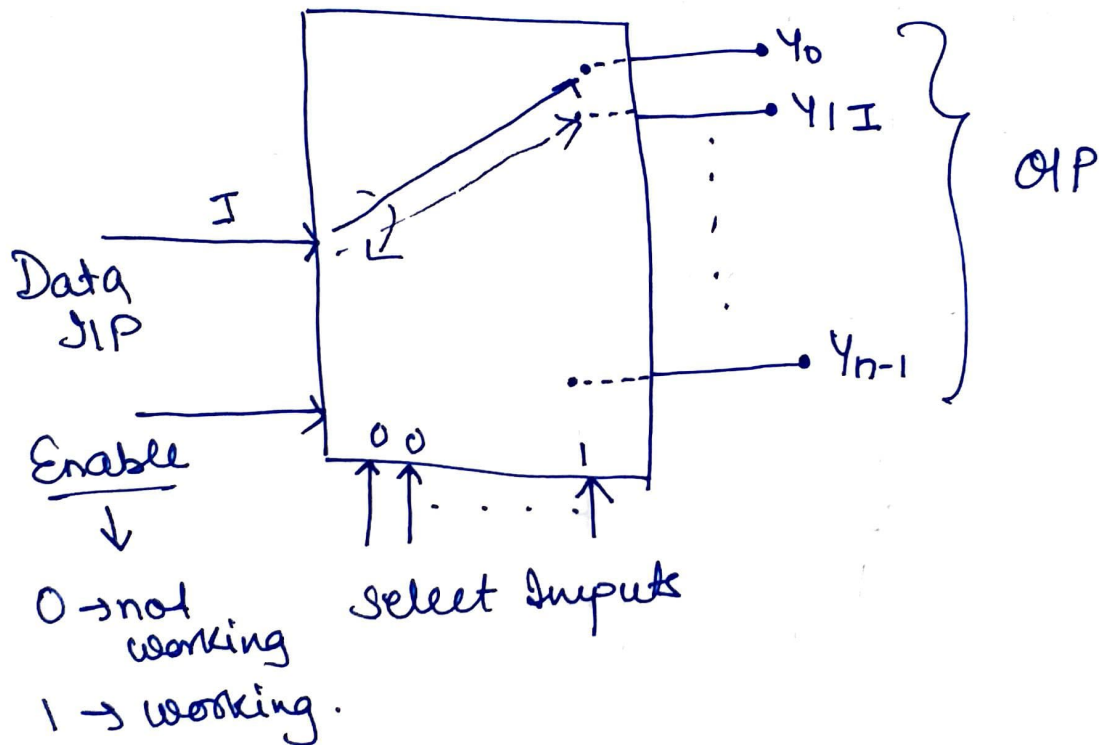


Introduction to DEMUX

(Demultiplexer)

- ↳ one IP and many OP
- ↳ Reverse operation of mux
- ↳ one to many Ckt or data distributor



$\bar{E} \rightarrow 0$ working
 $E \rightarrow 1$ Not working

↳ If I_0 to $I_5 \rightarrow$ Select lines
101

$n \rightarrow$ OP lines

$m \rightarrow$ select lines

$$n = 2^m$$

$$m = \log_2 n \rightarrow \text{no. of select lines.}$$

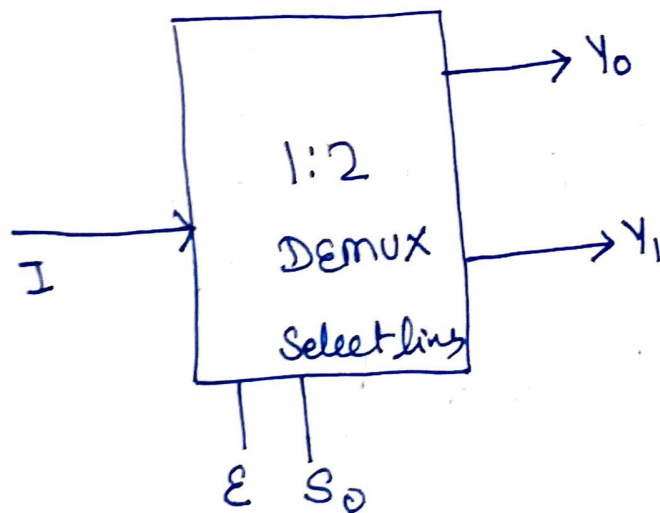
Q

1:2 DEMUX

↓ ↓

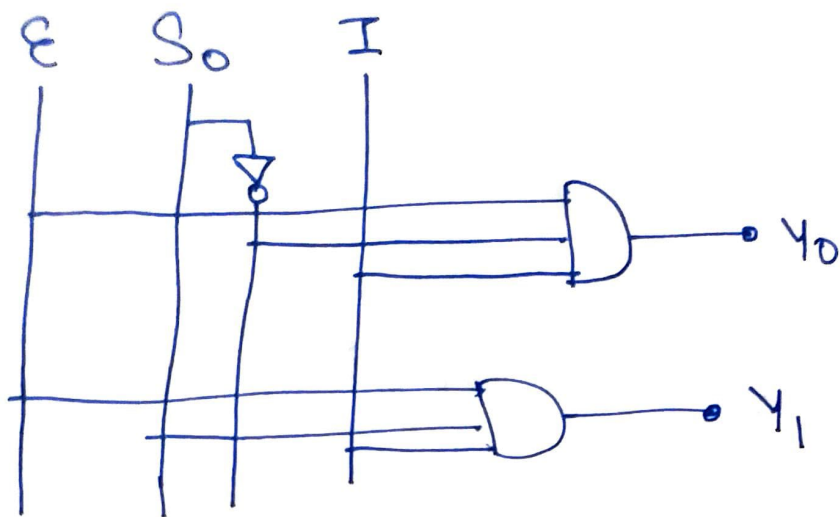
OP OP

$$m = \log_2 2$$
$$m = 1$$

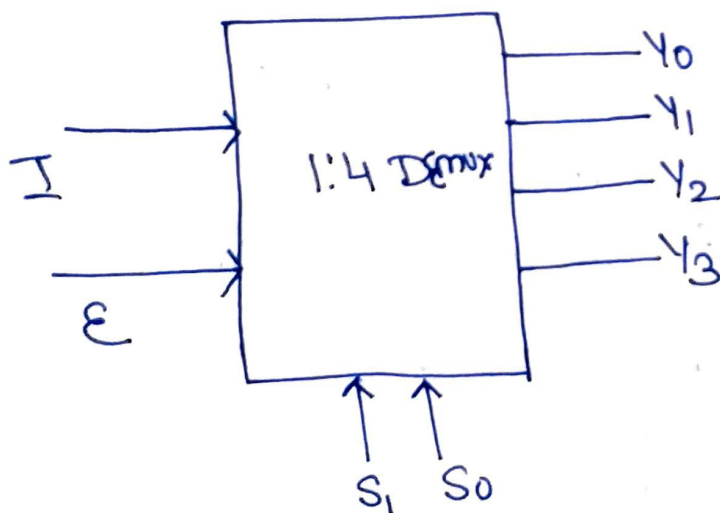


E	S0	Y0	Y1
0	0	0	0
0	1	0	0
1	0	I	0
1	1	0	I

$$Y_0 = E \bar{S}_0 I \quad Y_1 = E S_0 I$$



1:4 DEMUX



$$\begin{aligned}
 n &= 4 \\
 m &= \log_2 4 \\
 &= 2 \log_2 2 \\
 m &= 2
 \end{aligned}$$

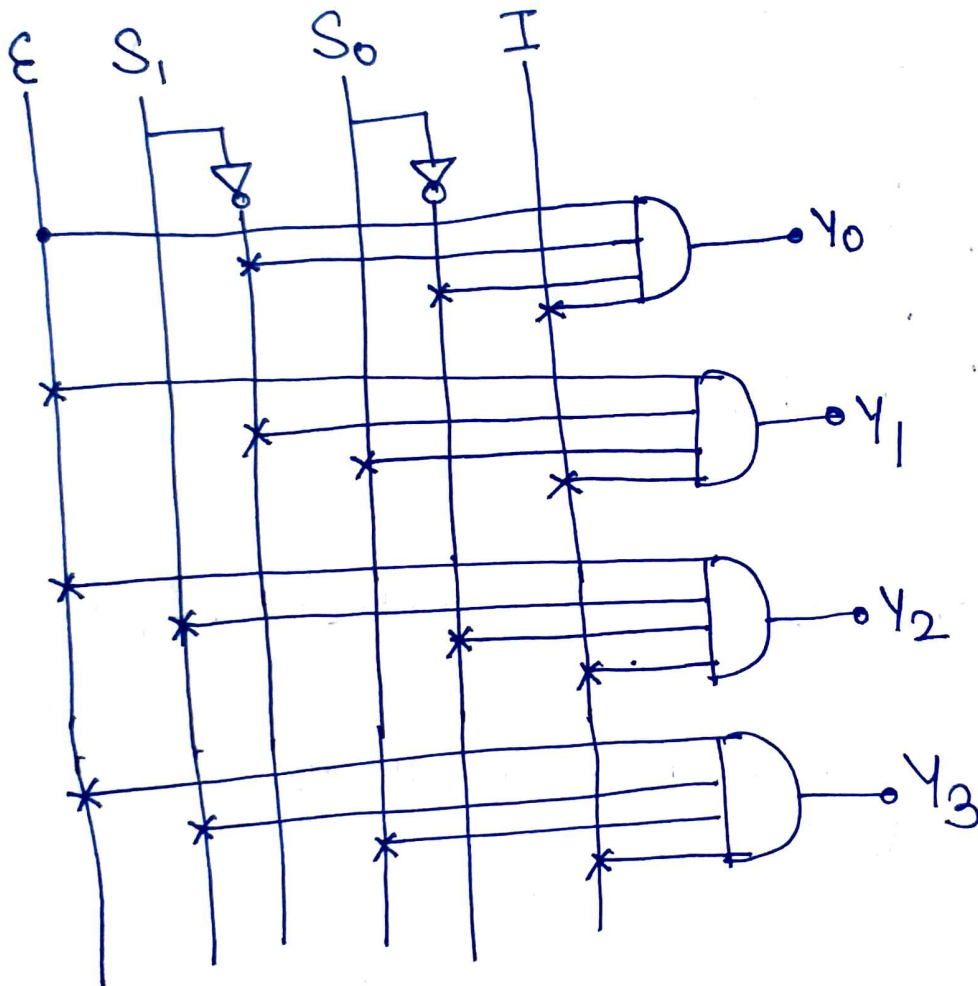
E	S_1	S_0	Y_0	Y_1	Y_2	Y_3
0	x	x	0	0	0	0
1	0	0	I	0	0	0
1	0	1	0	I	0	0
1	1	0	0	0	I	0
1	1	1	0	0	0	I

$$Y_0 = \epsilon \bar{S}_1 \bar{S}_0 I$$

$$Y_1 = \epsilon \bar{S}_1 S_0 I$$

$$Y_2 = \epsilon S_1 \bar{S}_0 I$$

$$Y_3 = \epsilon S_1 S_0 I$$



FULL SUBTRACTOR USING 1:8 DEMUX

A	B	B _i	D	B _o
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

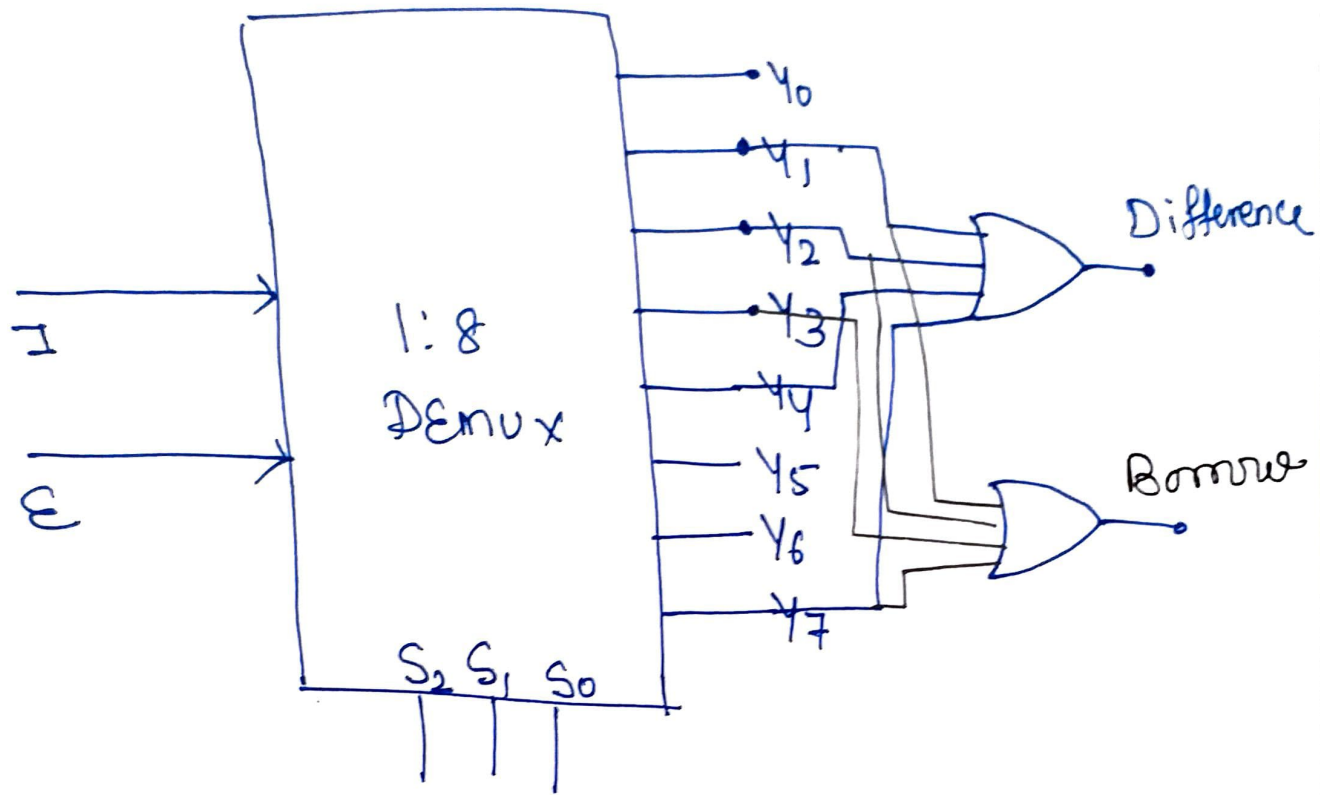
T-T of Full Subtractor

Standard SOP

$$D = \sum m(1, 2, 4, 7)$$

$$B_o = \sum m(1, 2, 3, 7)$$

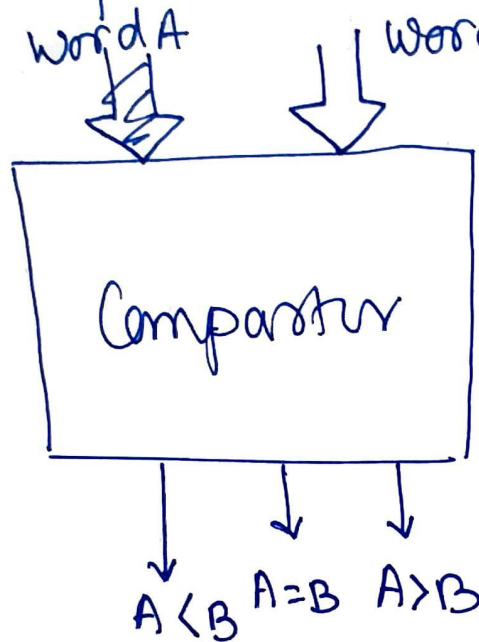
A x ₂	B x ₁	B _i x ₀	y ₀	y ₁	y ₂	y ₃	y ₄	y ₅	y ₆	y ₇
0	0	0	1	0	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	0	0	1	0	0
1	1	0	0	0	0	0	0	0	1	0
1	1	1	0	0	0	0	0	0	0	1



Q Implement full Adder by ~~8~~ 1:8 DEMUX

2-Bit Comparator

↳ A digital Comparator is a Combinational Ckt designed to Compare two n-bit binary words



↳ Comparator has 3 O/Ps

(2 bit Magnitude Comparator)

Inputs				A < B	A = B	A > B
A ₁	A ₀	B ₁	B ₀			
0	0	0	0	0	1	0
0	0	0	1	1	0	0
0	0	1	0	1	0	0
0	0	1	1	1	0	0
0	1	0	0	0	0	1
0	1	0	1	0	1	0
0	1	1	0	1	0	0
0	1	1	1	1	0	0
1	0	0	0	0	0	1
1	0	0	1	0	0	1
1	0	1	1	1	0	0

A_1	A_0	B_1	B_0	$A < B$	$A = B$	$A > B$
1	1	0	0	0	0	1
1	1	0	1	0	0	1
1	1	1	0	0	0	1
1	1	1	1	0	1	0

For $A < B$

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00		1	1	1
01			1	1
11				
10			1	

For $A = B$

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00	1			
01		1		
11			1	
10				1

$$A < B = \bar{A}_1 B_1 + \bar{A}_1 \bar{A}_0 B_0 + \bar{A}_0 B_1 B_0$$

$$A = B = (A_1 \odot B_1) (A_0 \odot B_0)$$

$A_1 A_0 \backslash B_1 B_0$	00	01	11	10
00				
01	1			
11	1		1	
10	1	1		

Implement
this
by gates.

$$A > B = A_1 \bar{B}_1 + A_0 \bar{B}_1 B_0 + A_1 A_0 \bar{B}_0$$