

Absolute encoder package

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version: exported

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1 Description

1.1 Overview

The `abs_enc_pkg` implements components for absolute encoder masters and slaves.

The term *master* refers to the component driving the clock, or at least initiating the data transfer. It is sometimes called the controller. The term *slave* refers to the actual encoder device.

This package is optimized for applications that can be dynamically configured to implement one amongst different types of encoders at a particular time. As much as possible, exclusive resources that can be shared across encoder types are factorized (counters, comparators, shift registers ...). However, and in order to avoid penalizing simpler applications, static configuration allows to exclude resources associated with an unused encoder type.

The following encoder types are available:

- ENDAT,
- BISS,
- SSI.

1.2 Supported features

TODO:

1.3 Architecture

TODO:

2 Interfaces

EXTRACTED from ../src/abs_enc_pkg.vhd

EXTRACTED from ../src/abs_enc_pkg.vhd

3 Usage

EXTRACTED from ../sim/common/main.vhd

EXTRACTED from ../sim/common/main.vhd