

Entrega 6 AC

Ejercicios 3.2, 3.10 y 3.11

3.2

$T_b = 16 \text{ bytes}$

Asociatividad = 2

$n. \text{ lin} = 8$

$W_t + W_{NA}$

8 líneas = 3 bits $\rightarrow 2^3 = 8$

Siempre se escribe en MP

No se trae a MC, se escribe en MP directamente

4 conjuntos = 2 bits

C0		C1		C2		C3	
AB4	0	889	1	EC6	0	EC7	0
EC6	1	ABA	0	23D	1	23D	1

ABA4 = 1010 1011 1010 0100

4(conj)

entonces el bloque no sirve

2AE9 ?

3.10

1.2V

2GHz

Corriente fuga = 3A

Capacitiva = 5nf

a) $P_{md} = CV^2 \cdot f = 114.4 \text{ W}$

(Commutacion)

$P_{me} = I \cdot V = 3 \cdot 1.2 = 3.6 \text{ W}$

$P_{tot} = 18 \text{ W}$

b) ~~conj~~ 128 KB capacidad
bloque de 64B

$n \text{ bloques} = \frac{128 \text{ KB}}{64 \text{ B}} = \frac{128 \cdot 2^{10}}{64} = 2048 \text{ bloques}$

$\frac{2048 \text{ bloq}}{2} = 1024 \text{ conjuntos}$

$n \text{ vias} = 2$

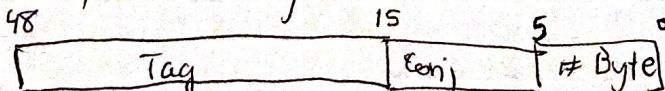
2 asociativa = 2 vias

Bloques por via = $\frac{1024 \text{ conjuntos}}{2} = 512$ (dos ~~conjuntos~~ vias)

c) direccioner de 48 ~~bits~~ bits

$2^x = 1024 \Rightarrow \underline{10} = x$

bloque de 64 bytes = 2^6



d)

$$\text{Tamaño m datos} = 1024 \text{ bloques} \cdot 64 \text{ bytes} \cdot 8 \text{ bits/byte} = \underline{524288 \text{ bits}}$$

$$T_m \text{ etiquetas} = 1024 \text{ bloques} \cdot 32 \text{ bits de cada tag} = \underline{32768 \text{ bits}}$$

$$e) P = I \cdot V = 1.671 \text{ A} \cdot 1.2 \cdot 2 = \underline{4 \text{ W}}$$

$$\text{bits cache} = T_m \text{ datos} + T_m \text{ etiq} = 557.056 \text{ b}$$

$$\text{corr. de fuga} = 3 \mu\text{A} \cdot \text{bits cache} = 1.671 \text{ A}$$

$$f) \text{ MFLOPS} = \frac{2 \cdot 10^9}{5s} \Rightarrow \underline{400 \times 10^6 \text{ FLOPS}} \rightarrow \underline{400 \text{ MFLOPS}}$$

$$g) \text{ Ciclos CPI} = 2 \text{ GHz} \cdot 5s = 10 \cdot 10^9 \text{ cic}$$

$$\text{CPI par} = \frac{\text{Ciclos CPI}}{\# \text{ inst. din}} = 2 \text{ s/ci}$$

$$\text{cic fallo} = \frac{0.1 \text{ fallo}}{1 \text{ acc}} \cdot 10^9 \cdot 20 = 2 \cdot 10^9 \text{ cic}$$

$$\text{CPI id} = \frac{(10 - 2) \cdot 10^9}{4 \cdot 10^9} = \underline{2 \text{ cic}}$$

$$h) \text{ Energía} = \underline{30 \text{ nJ}}$$

$$i) \frac{10^9}{5} = 0.2 \cdot 10^9 \text{ accesses/s}$$

$$P = E/t = 0.2 \cdot 10^9 \cdot 30 \cdot 10^{-9} = \underline{6 \text{ W}}$$

j)

$$k) E = P \cdot t = 6 \cdot 5 = \underline{30 \text{ J}}$$

$$m) E = 2 \cdot 5 \text{ nJ} + 25 \text{ nJ} = \underline{35 \text{ nJ}}$$

$$n) 10^9 / 5.4s = 0.183 \times 10^9 \text{ acc/s}$$

$$P = 0.183 \times 10^9 \cdot 35 \cdot 10^{-9} = \underline{6.42 \text{ W}}$$

$$o) P_{tc} = 18 + 6.42 + 4 = \underline{28.42 \text{ W}}$$

$$p) E = P \cdot t = 28.42 \cdot 5.4s = \underline{155 \text{ J}}$$

$$\text{Eficiencia} = \frac{367}{28.42} = \underline{12.91 \text{ MFLOPS}}$$

q) No, ya que ~~predice~~ primero comprueba que existe en la cache

$$r) I_{\text{fuga}} = 8182 \text{ b} \cdot 3 \cdot 10^{-6} = \underline{26.6 \text{ mA}}$$

$$P_{\text{fuga}} = I \cdot V = 1.2 \cdot 26.6 \cdot 10^{-3} = \underline{29.5 \text{ mW}}$$

$$s) \text{ ciclos} = 10 \cdot 10^9 + 6 \cdot 2 \cdot 10^9 \cdot 1 = 10'2 \cdot 10^9 \text{ ciclos}$$

$$T_{\text{exe}} = \frac{1}{2 \cdot 10^9} \cdot 10'2 \cdot 10^9 = 5'1$$

$$\text{Flops} = \frac{2 \cdot 10^9}{5'1} = 392 \cdot 10^6 \rightarrow \text{MFLOPS} = \underline{\underline{392}}$$

d)

$$\text{Aguerto} \rightarrow E = 1nJ + 5nJ + 25nJ = 31nJ$$

$$\text{fallo} \rightarrow E = 1nJ + 10 + 50 = 60nJ$$

$$\text{media} = 0'8 \cdot 31 + 0'2 \cdot 61 = \underline{\underline{37nJ}}$$

$$u) \frac{10^9}{5'1} = 0'196 \cdot 10^9 \text{ acc/s}$$

$$P = \frac{E}{t} = \frac{0'196 \cdot 10^9 \cdot 37 \cdot 10^{-9}}{1} = \underline{\underline{7'25 \text{ W}}}$$

$$v) P_{\text{tot}} = 18 + 4 + 7'25 + 0'03 = 29'28 \text{ W}$$

$$w) E = 29'28 \cdot 5'1 = 149 \text{ J}$$

$$\text{eficiencia} = \frac{392}{29'28} = 13'39 \frac{\text{MFLOP}}{\text{W}}$$

$$x) g. \text{ Serie a paralelo} = \frac{12'91}{11'76} = \underline{\underline{1'098}}$$

$$g. \text{ via a serie} = \frac{13'39}{12'91} = \underline{\underline{1'037}}$$

3.11) a) $\boxed{X1}$ $T_c = M_{\text{datos}} + M_{\text{ux}} = 0'55 \text{ ns}$
 $T_{\text{ac}} = 0'55 \text{ ns}$

$$\boxed{X2} \quad T_c = M_{\text{datos}} + M_{\text{ux}} + R_{\text{eg}} = 0'6 \text{ ns}$$

$$T_{\text{ac}} = 0'6 \cdot 2 \text{ ciclos} = 1'2 \text{ ns}$$

$$\boxed{X3} \quad T_c = M_{\text{em Dator}} + R_{\text{eg}} = 0'55 \text{ ns}$$

$$T_{\text{ac}} = 0'55 \cdot 3 = 1'65 \text{ ns}$$

$$\boxed{X4} \quad T_c = M_{\text{Dat}} + R_{\text{eg}} = 0'55 \text{ ns}$$

$$T_{\text{ac}} = 0'55 \cdot 4 = 2'2 \text{ ns}$$

b) ~~X2~~ X2 porque tiene el peor tiempo de ciclo.
X4 " " " " acceso.

$$c) f_{X1} = \frac{1}{T_c} = 1'82 \text{ GHz}$$

$$f_{X3} = \frac{1}{T_c} = 1'82 \text{ GHz}$$

d) $2 \cdot 10^9$ instrucciones

60% ant $\rightarrow 5c$

20% salto $\rightarrow 4c$

20% Acc. a M + $4c$ + acc. cache $\begin{matrix} x_1 = 1 \\ x_3 = 3 \end{matrix}$

$$CPI_{x1} = 0'6 \cdot 5 + 0'2 \cdot 4 + 0'2 \cdot 5 = \underline{4'8/i}$$

$$CPI_{x3} = 0'6 \cdot 5 + 0'2 \cdot 4 + 0'2 \cdot 7 = \underline{5'2 c/i}$$

e) $T_{exe x1} = 2 \cdot 10^9 \times 4'8 \times 0'55 \cdot 10^{-9} = \underline{5'28s}$

$$T_{exe x3} = 2 \cdot 10^9 \cdot 5'2 \cdot 0'55 \cdot 10^{-9} = \underline{5'72s}$$

$$Speedup = \frac{5'72}{5'2} = 1'1 = 10\%$$

f) 10% fallos en MC $penal = 60c/clock$

(X1) $CPI_{real} = \cancel{4'8} + 0'2 \cdot 0'1 \cdot 60 = 6c/i$
 $T_{exe} = 2 \cdot 10^9 \cdot 6 \cdot 0'55 \cdot 10^{-9} = \underline{6'6s}$

(X2) $CPI_{real} = 5'2 + 0'2 \cdot 0'1 \cdot 60 = 6'4 c/i$
 $T_{exe} = 6'4 \cdot 2 \cdot 10^9 \cdot 0'55 \cdot 10^{-9} = \underline{7'04s}$

$$Speedup = \frac{7'04}{6'6} = 1'06 = 6'6\%$$