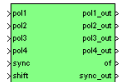


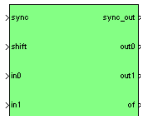
fft_biplex
FFTSize=4,
arch=Vrtex2Pro,
opt_target=Logic



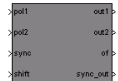
fft_biplex_real_4x
FFTSize=4



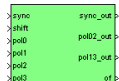
fft_direct
FFTSize=2



fft
FFTSize=4, n_inputs=1



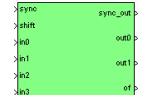
biplex_core
FFTSize=3,
opt_target=Multipliers,
arch=Vrtex2Pro



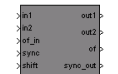
fft_biplex_real_2x
FFTSize=7



fft_unscrambler
FFTSize=6, n_inputs=2



fft_wideband_real
FFTSize=5



fft_stage_n
FFTSize=3, FFTStage=2,
coeffs_bram=off,
delays_bram=off



butterfly_direct
FFTSize=8, coeff_bit_width=18



Twiddle