Unit: mm

TOSHIBA Photocoupler Photorelay

TLP174GA

Modem-Fax Cards, Modems in PC Telecommunications PBX Measurement Equipment

The Toshiba TLP174GA consists of an infrared emitting diode optically

coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

Because of the high-voltage MOS FET used for the output stage, the TLP174G is suitable for replacement of dial pulse relay and hook relay of modem and facsimile, and also suitable for PBX and the line interface section of exchange.

In addition, the MOS FET control circuit is provided the current limiting function, and it conforms to the FCC Part 68 new standard.

• 4-pin SOP (2.54SOP4): Height = 2.1 mm, Pitch = 2.54 mm

• 1-Form-A

• Peak Off-state voltage: 400 V (min)

• Trigger LED current: 3 mA (max)

• On-state current: 120 mA (max)

• Limit current: 150 mA to 300 mA (t = 5 ms)

• On-state resistance: 35Ω (max)

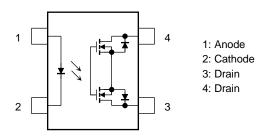
• Isolation voltage: 1500 Vrms (min)

• UL-recognized: UL 1577, File No.E67349

JEDEC — JEITA — TOSHIBA 11-5H1

Weight: 0.1 g (typ.)

Pin Configuration (top view)



Start of commercial production 2002-01



Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit	
	Forward current	lF	50	mA	
	Forward current derating (Ta≥25°C)	ΔI _F /°C	-0.5	mA/°C	
	Peak forward current (100 μs pulse, 100 pps)	IFP	1	Α	
LED	Reverse voltage	VR	5	V	
	Diode power dissipation	PD	50	mW	
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP _D /°C	-0.5	mW/°C	
	Junction temperature	Tj	125	°C	
Detector	Off-state output terminal voltage	Voff	400	V	
	On-state current	Ion	120	mA	
	On-state current derating (Ta≥25°C)	ΔI _{ON} /°C	-1.2	mA/°C	
	Output power dissipation	Pc	350	mW	
	Output power dissipation derating (Ta ≥ 25°C)	ΔP _C /°C	-3.5	mW /°C	
	Junction temperature	Tj	125	°C	
Storage to	emperature range	T _{stg}	-55 to 125	°C	
Operating	g temperature range	T _{opr}	-40 to 85	°C	
Lead solo	dering temperature (10 s)	T _{sol}	260	°C	
Isolation	voltage (AC, 60 s, R.H.≤ 60 %) (Note 1)	BVS	1500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{DD}	_	_	320	V
Forward current	lF	5	7.5	25	mA
On-state current	Ion	_	_	120	mA
Operating temperature	T _{opr}	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V _R = 5 V	_	_	10	μΑ
	Capacitance between terminals	Ст	VF = 0 V, f = 1 MHz	_	30	_	pF
Detector	Off-state current	loff	Voff = 400 V	_	_	1	μА
Detector	Capacitance between terminals	Coff	V = 0 V, f = 1 MHz		70	_	pF

TLP174GA



Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 120 mA	_	1	3	mA
Close LED current	IFC	I _{OFF} = 100 μA	0.1	_	_	mA
Load current limiting	ILIM	$I_F = 5 \text{ mA}, V_{DD} = 5 \text{ V}, t = 5 \text{ ms}$	150	_	300	mA
On-state resistance	Ron	I _{ON} = 120 mA, I _F = 5 mA		17	35	Ω

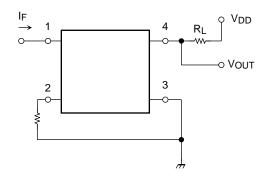
Isolation Characteristics (Ta = 25°C)

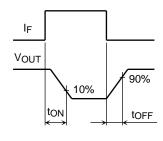
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	Rs	Vs = 500 V, R.H. ≦ 60 %	5 × 10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 60 s	1500		_	Vrms

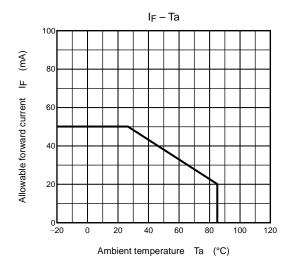
Switching Characteristics (Ta = 25°C)

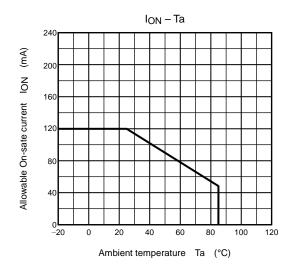
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	$R_L = 200 \Omega$	_	0.3	1	
Turn-off time	toff	$V_{DD} = 20 \text{ V, IF} = 5 \text{ mA}$ (Note 2)	_	0.1	1	ms

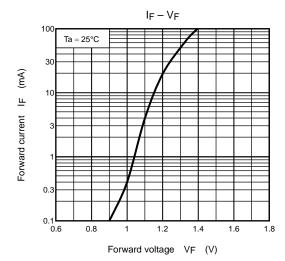
Note 2: Switching time test circuit

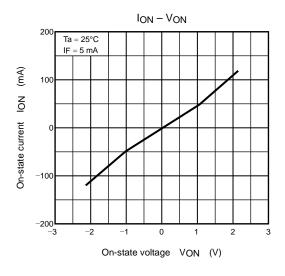


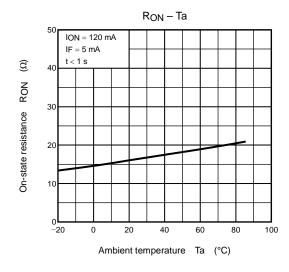


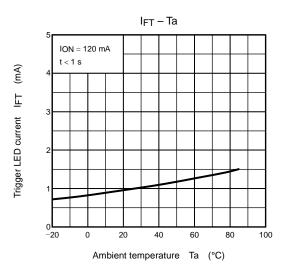




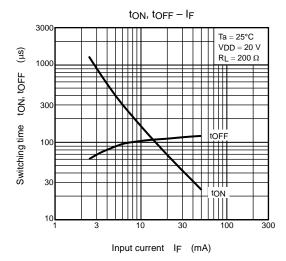


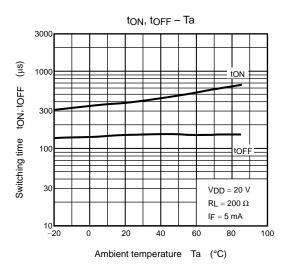


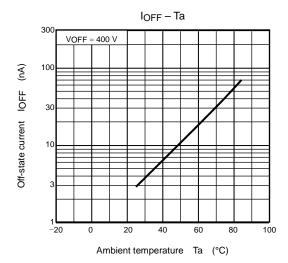




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.







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