Thomas Gorham IV

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Objective:
To build essential products and processes that empower their users to work and live more meaningfully and effectively.

Education:
Kansas State University - *B.S. in Electrical Engineering* Graduated May 2018
Specialization: *Communication Systems and Design* GPA: 3.97, Summa Cum Laude
Awards/Associations:

- Tau Beta Pi - Engineering Honorary Fraternity for students in top eighth of class.
- Electronics Design Club - Designed electronic displays for engineering department showcases.
- Marching Band - 4 years, Section Leader for 2017 season.

Skills:

- Proficient in C/C++, Python (Numpy, Scipy, Pandas), FPGA(Verilog/VHDL), Golang, Linux, Docker, AutoCAD/Inventor, and Microsoft Office.
- Highly versed in Electronics Design (Digital design, Microcontrollers, FPGA, Power architecture).
- Passionate individual contributor, effective communicator, and strong leader.
- Intrinsically motivated and highly capable self-teacher.

Experience:

Garmin International
Design Engineer II/Project Engineer *May 2018 - Present*

- Lead the electrical design of a Yocto Linux and FPGA powered Marine Radar on the Xilinx Zynq platform.
- Lead a team of electrical and software engineers in the design, test, and manufacture of the consumer marine industry's first L1/L5 Band GPS Sensor.
- Design efficient and intuitive automated testing systems in Python interfacing with GPIB, SCPI, SQLITE, and proprietary UDP-based protocols to maximize development efficiency.
- Create documentation to standardize GPS and Radar testing practices within our department.
- Develop low-level embedded drivers in C for resource-constrained devices running an in-house RTOS.
- Design, maintain, and support a memory-mapped FPGA radar processing system written in VHDL.

Design Engineer Intern - Marine *May 2017 - May 2018*

- Designed a low-power Bluetooth human interface device for use with a newly designed trolling motor.
- Designed a cost-effective alternative to Ideal Diode Controller reverse polarity protection.
- Contributed to development of a C embedded linux application to manage an FPGA in a solid state radar.
- Developed VHDL code to control timing and signal processing in a pulse compression radar.

Software Engineer Intern - Aviation *May 2016 - May 2017*

- Architected software tests to formally verify functional coverage of system software written in C, resulting in the certification and release of an aviation audio processing device.
- Designed system-level Python test scripts to ensure proper functionality and robustness of product.

KSU iTAC Instructional Technology
Student Technician *August 2015 - May 2016*

- Installed, upgraded, and maintained multimedia and computing laboratory infrastructure across campus.
- Provided on-call support and technical training to educators and faculty.