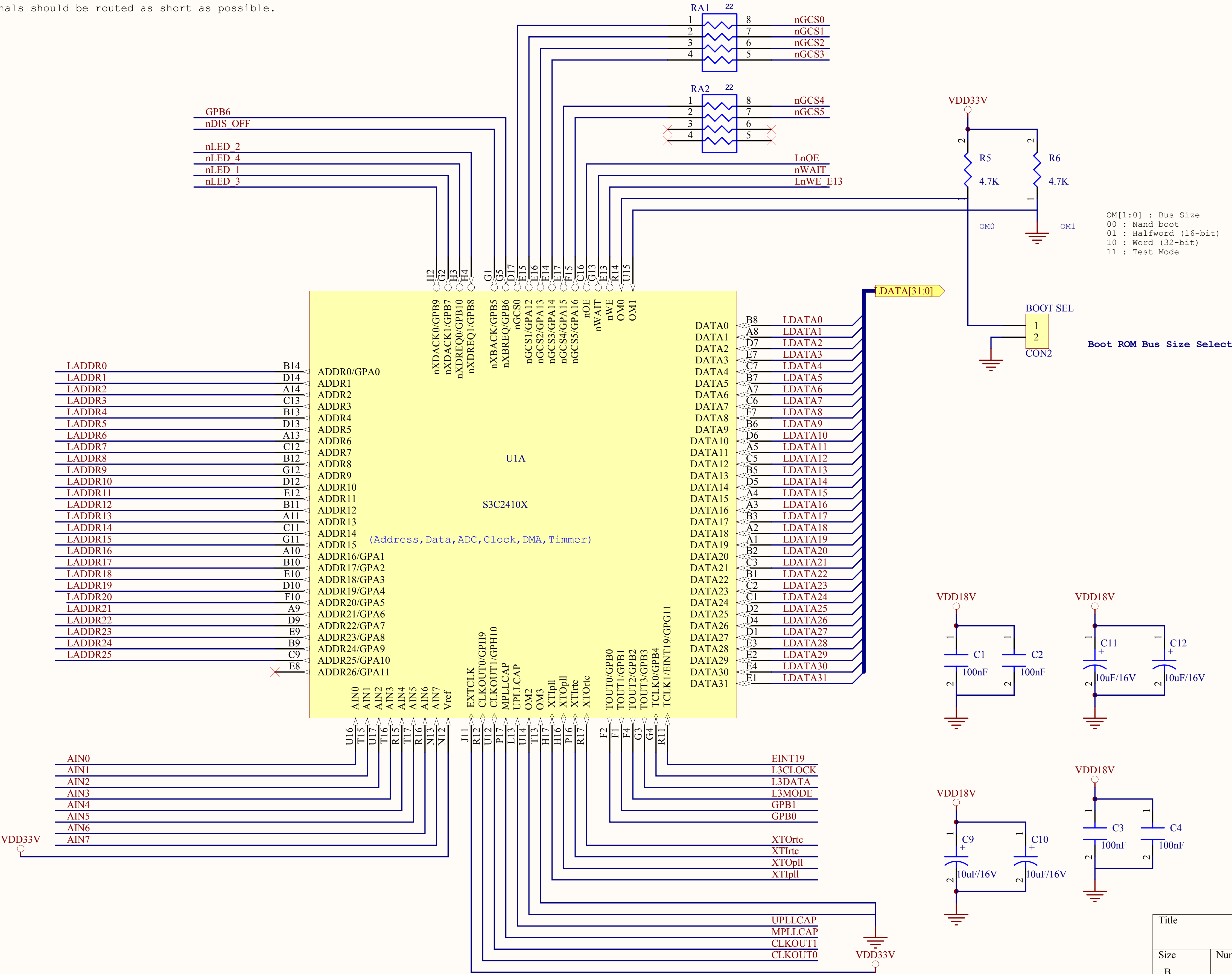
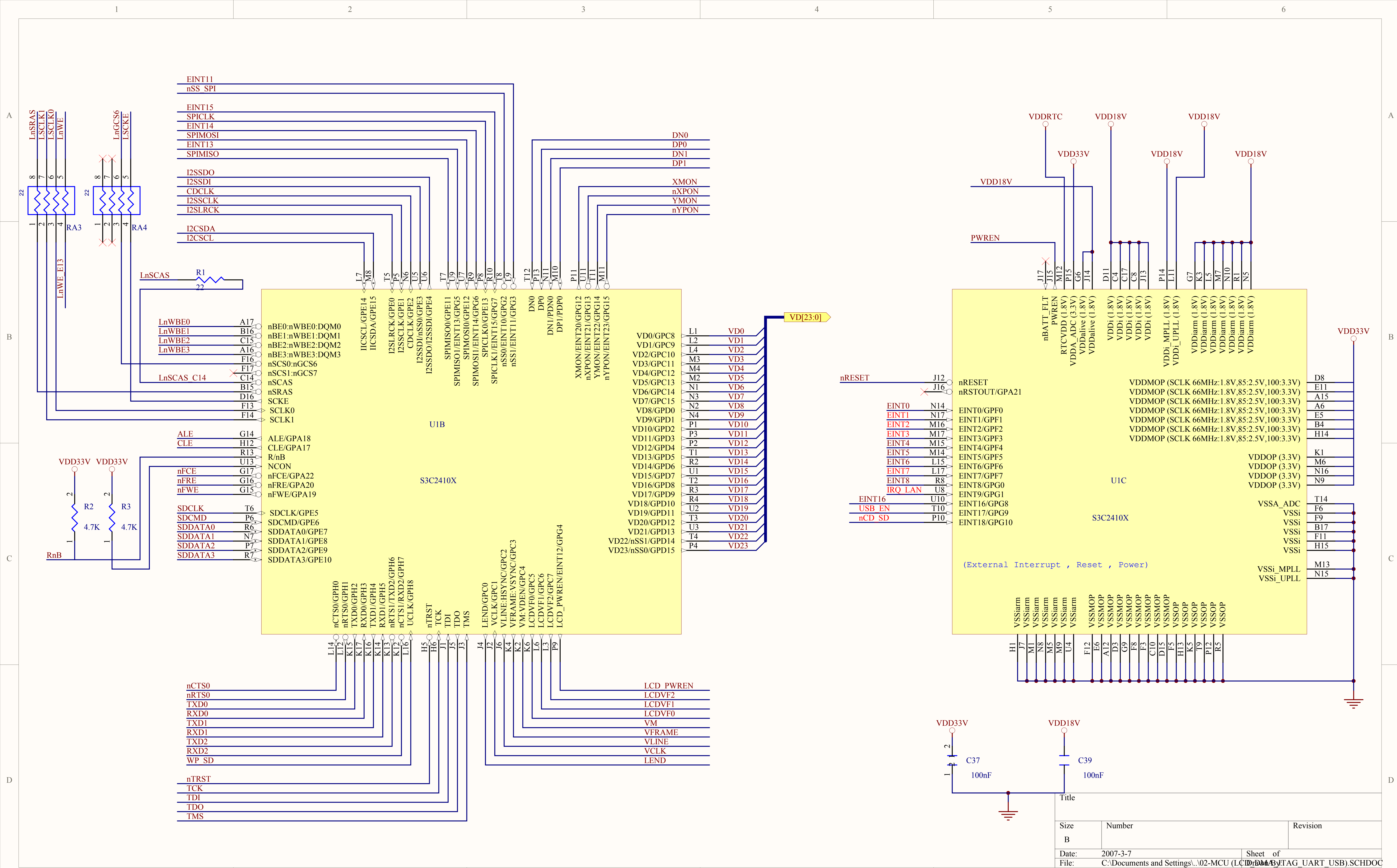


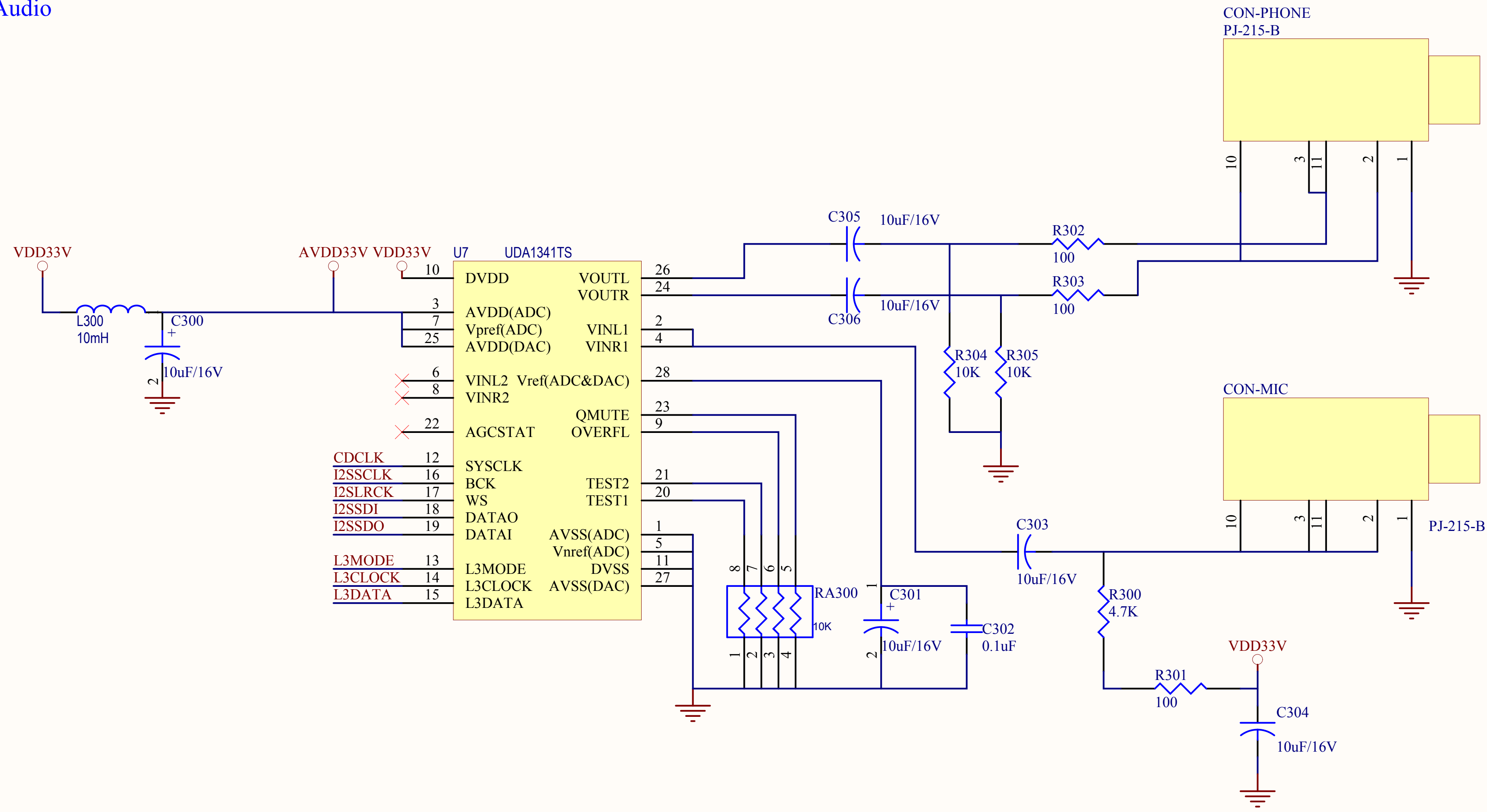
NOTE!!!
Lxxxx signals should be routed as short as possible.



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Date:	2007-3-7	Sheet of
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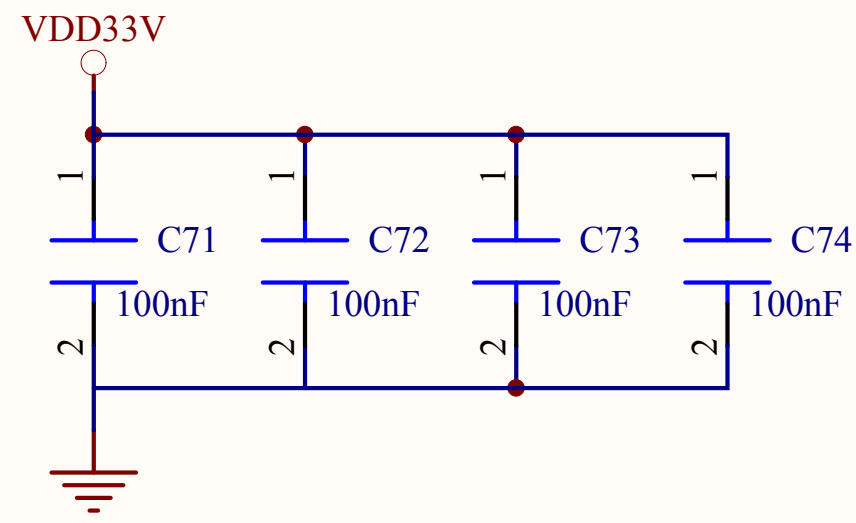
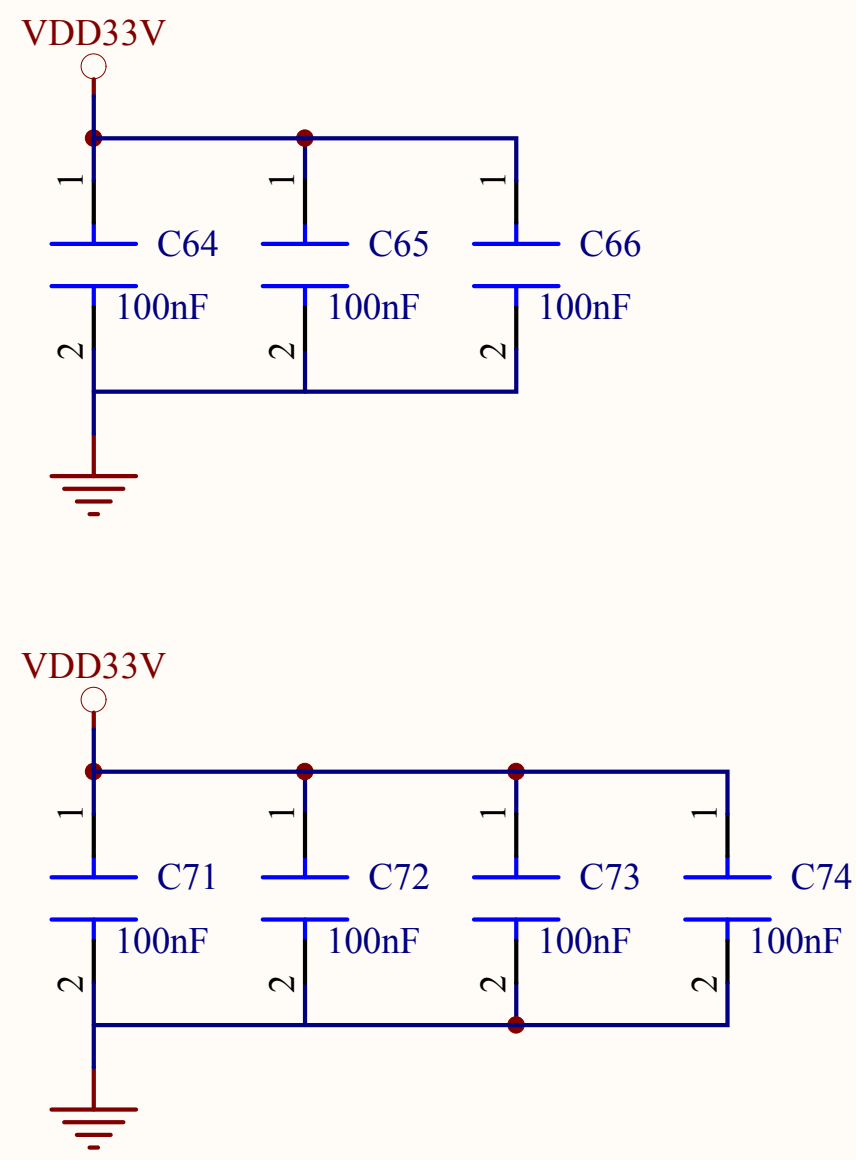
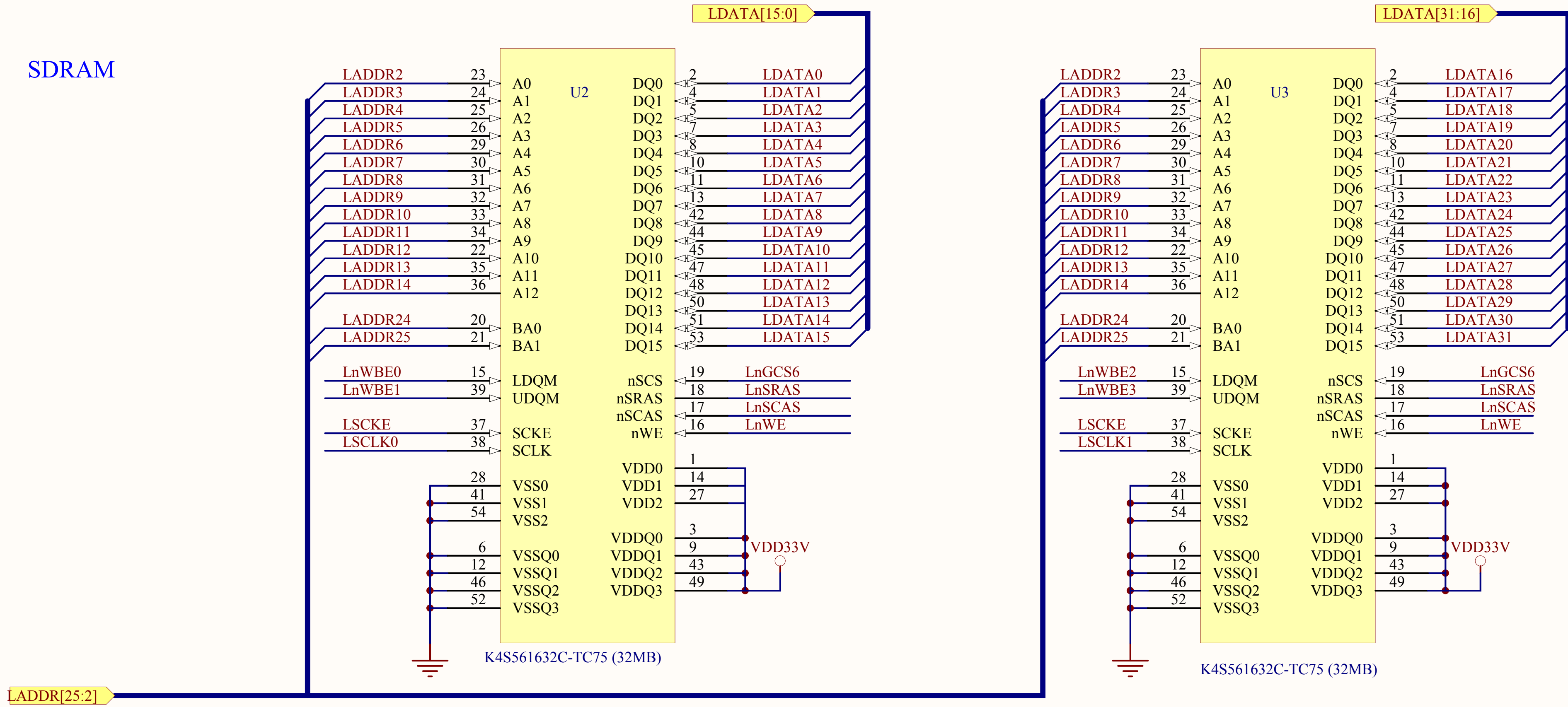


Audio

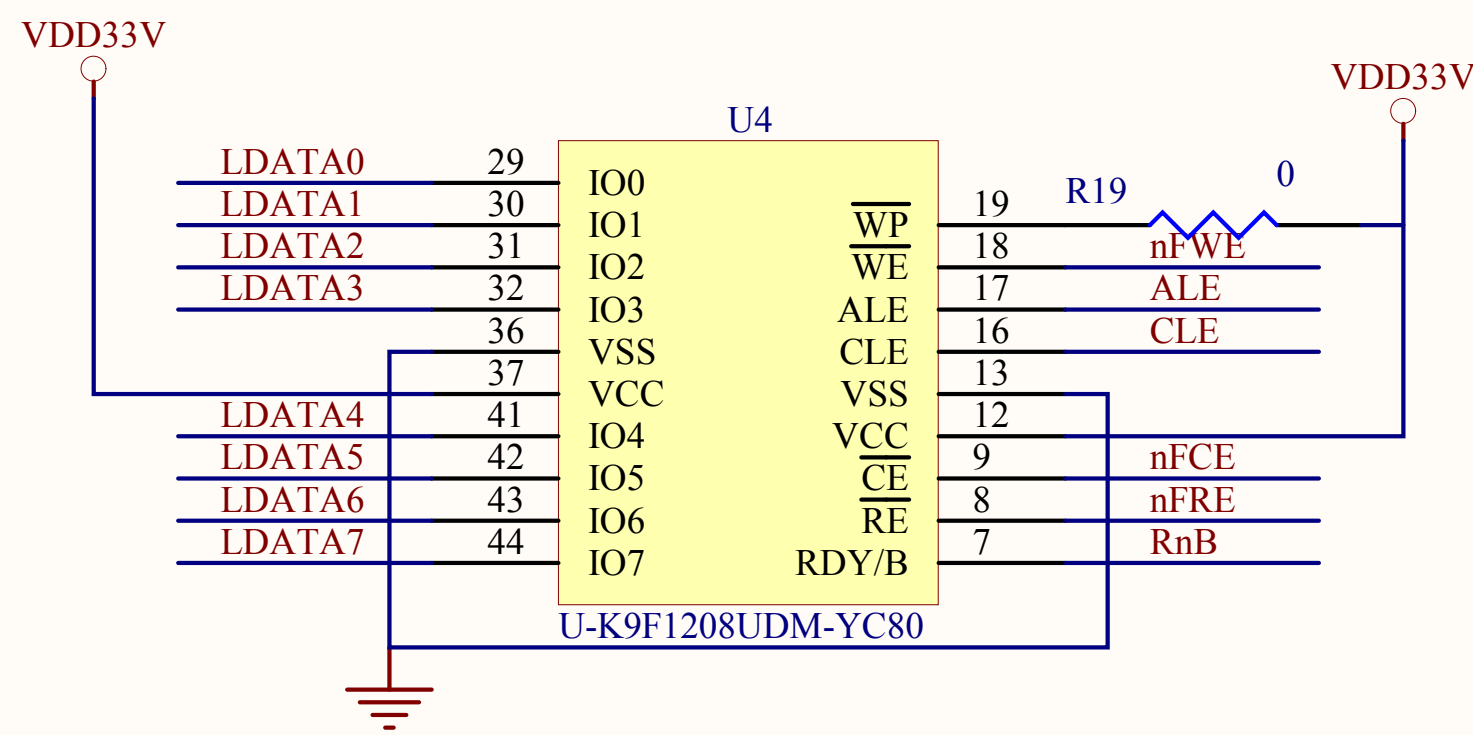


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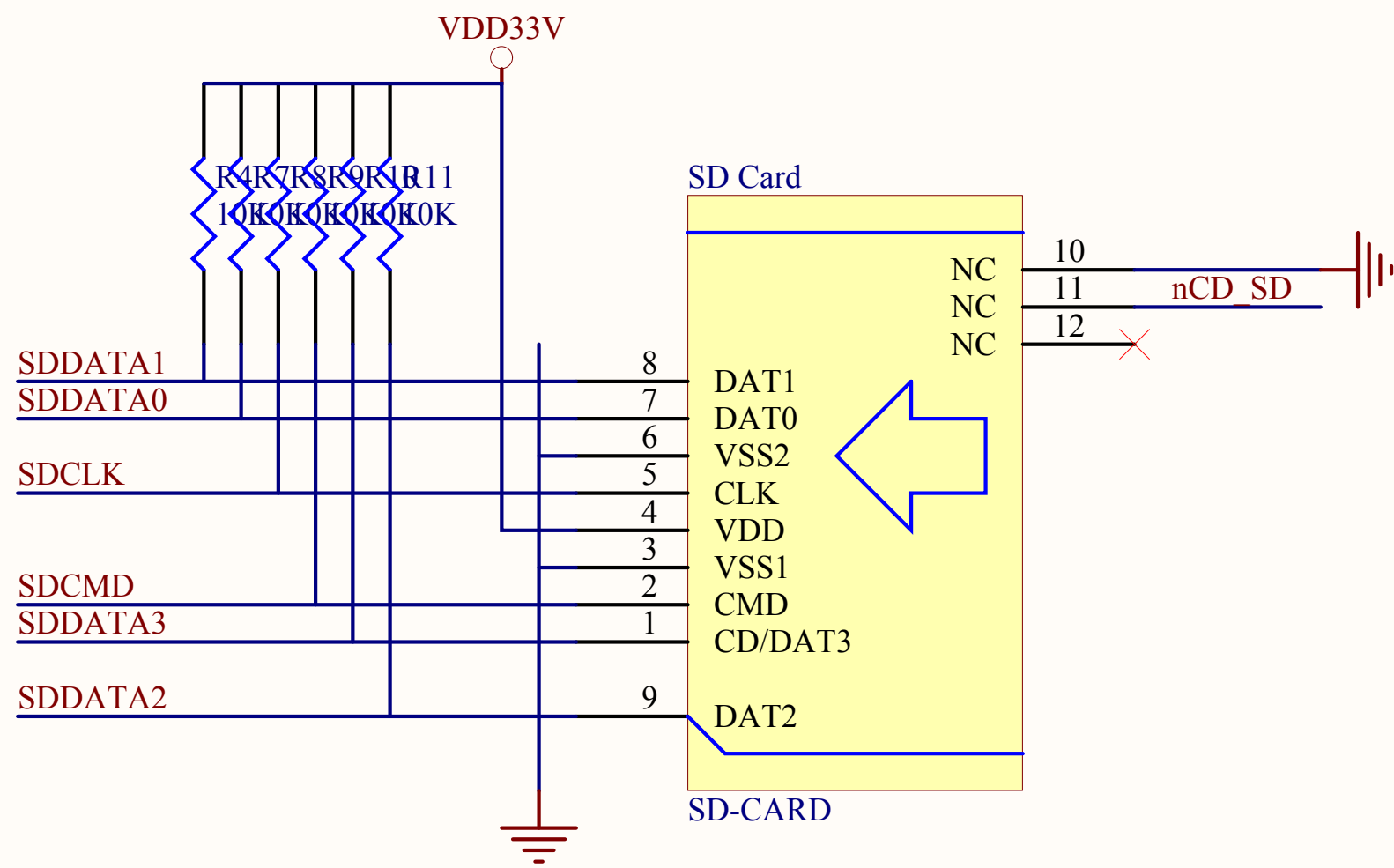
SDRAM



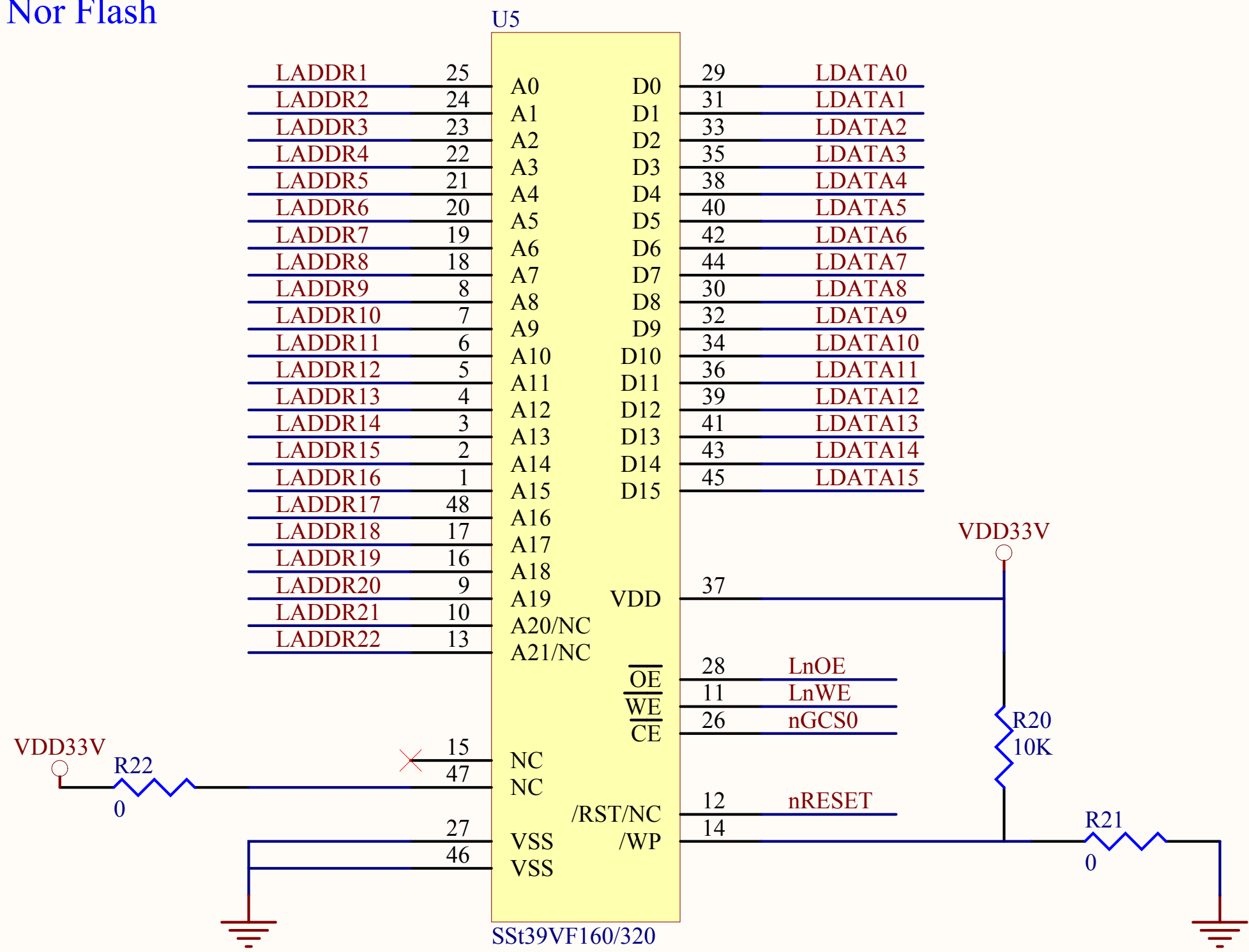
Nand Flash



SD Card

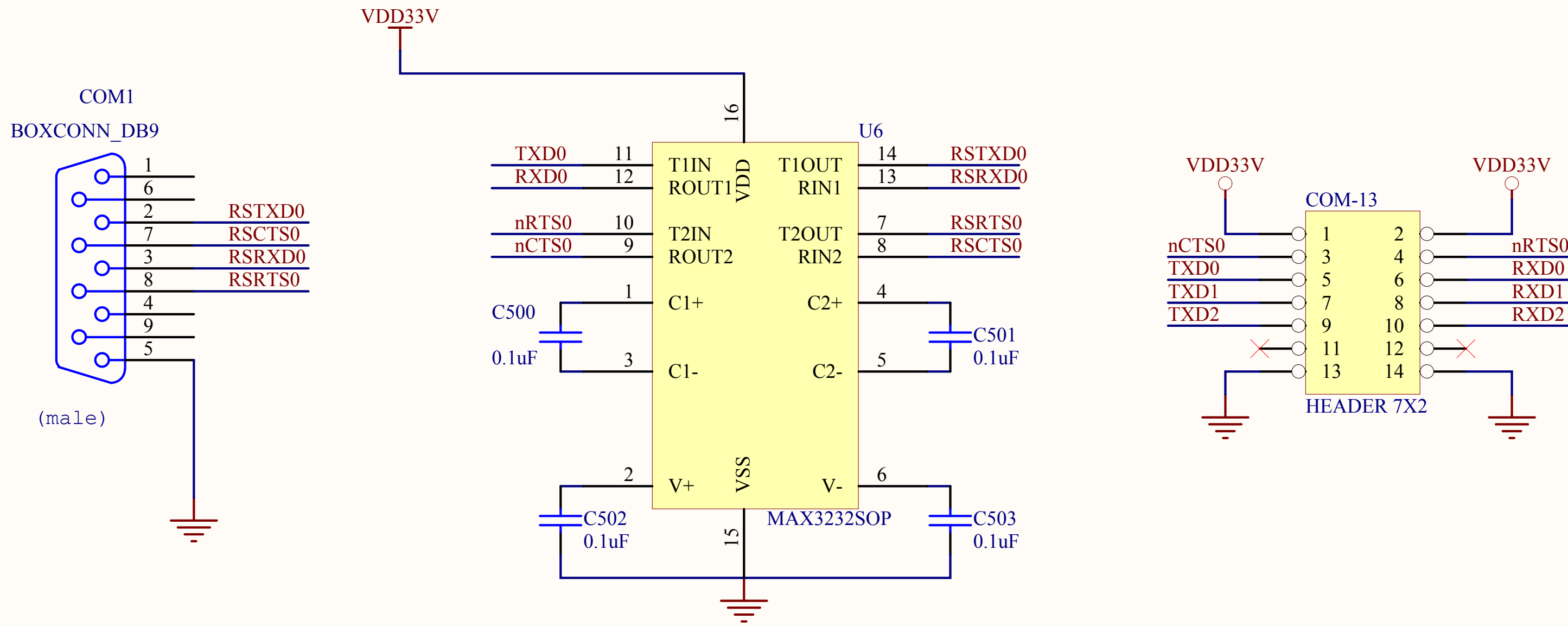


Nor Flash

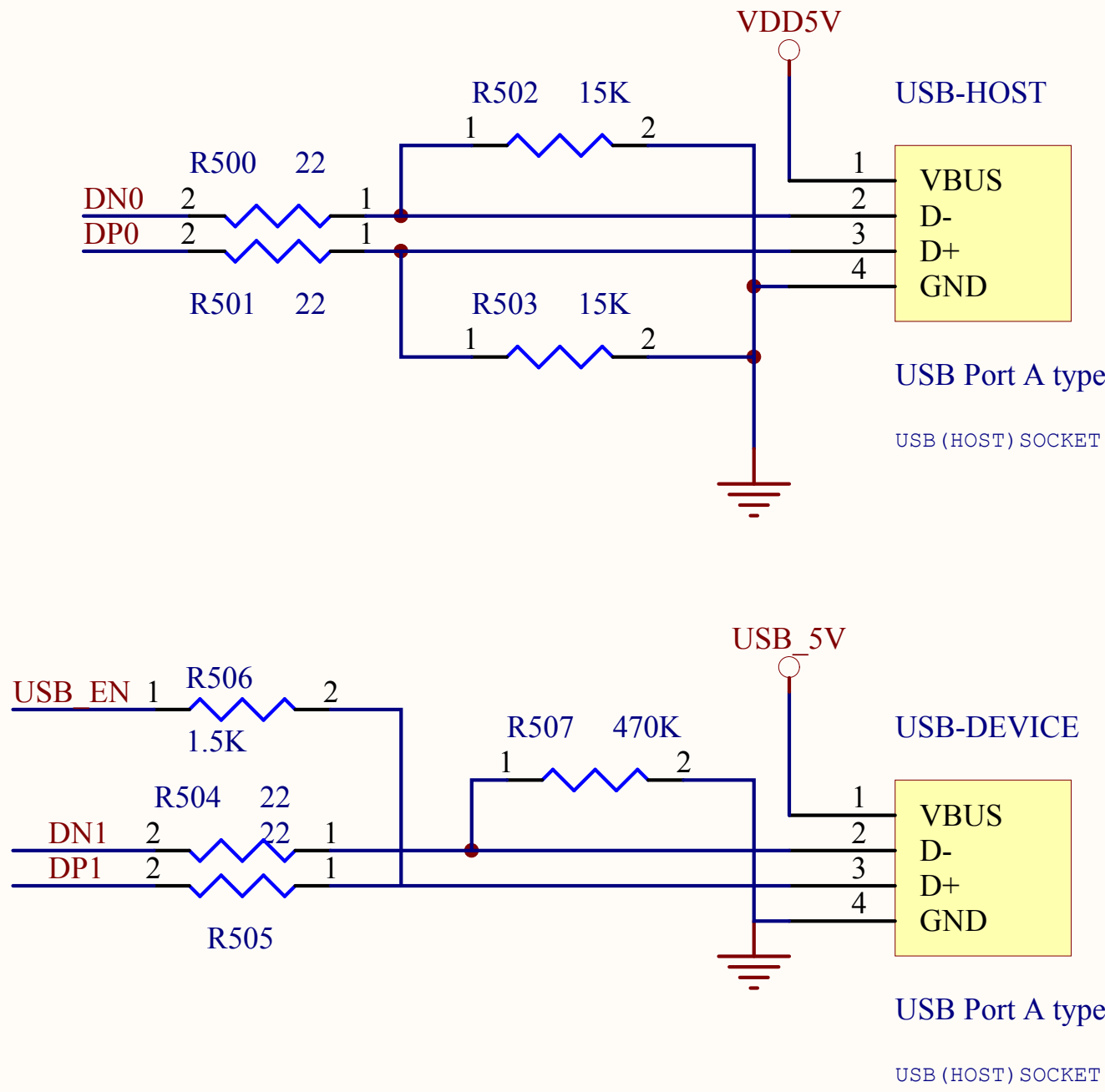


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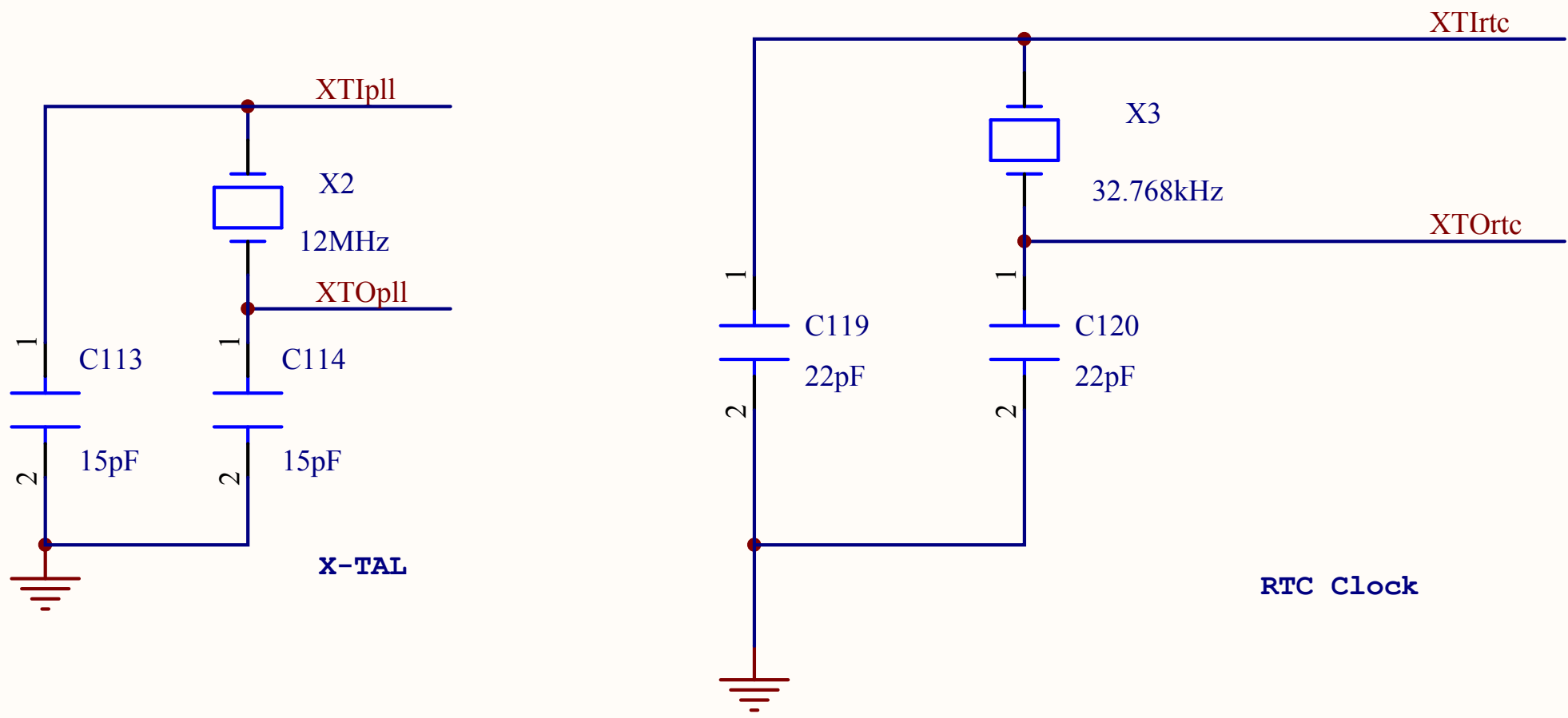
UART



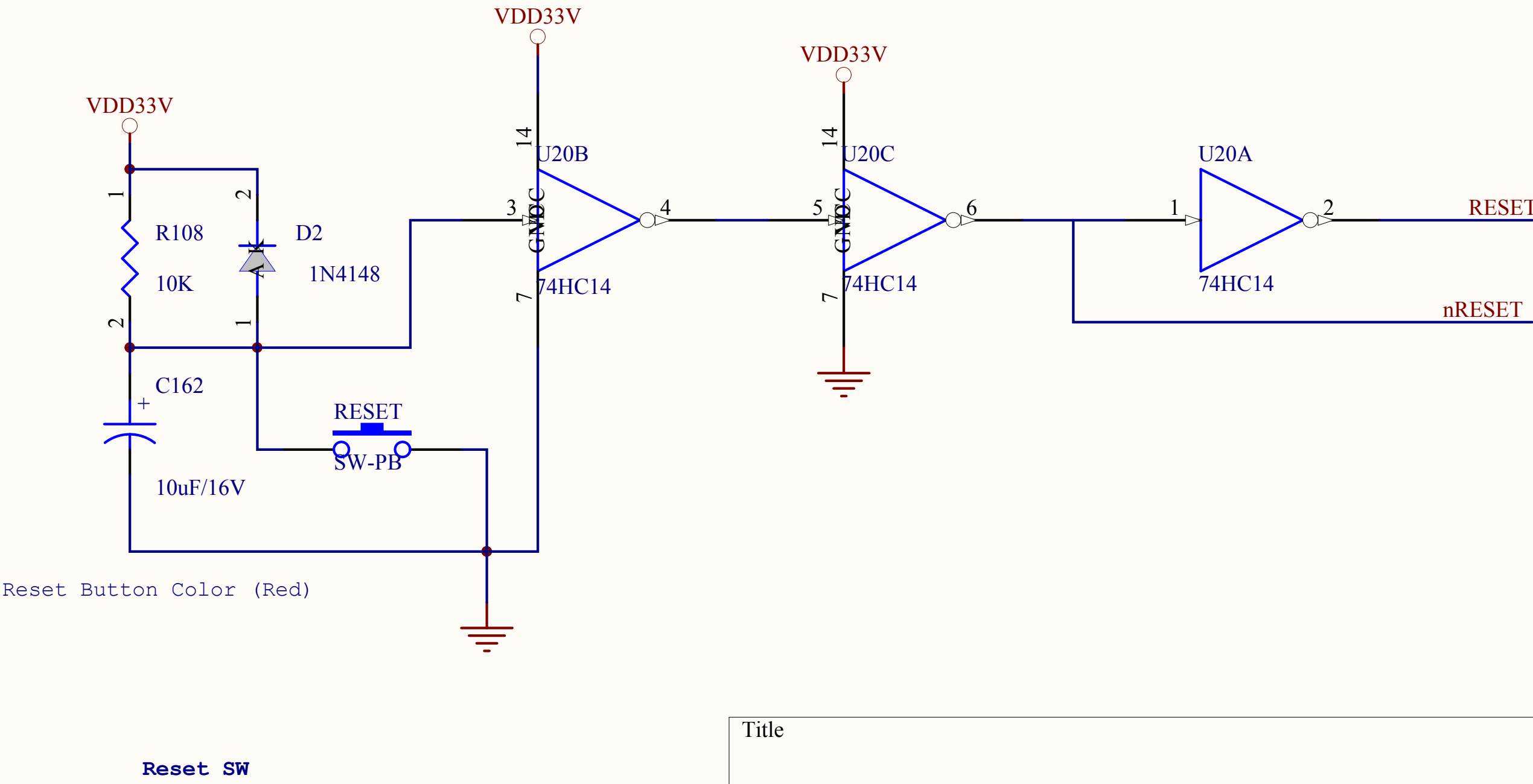
USB



Clock

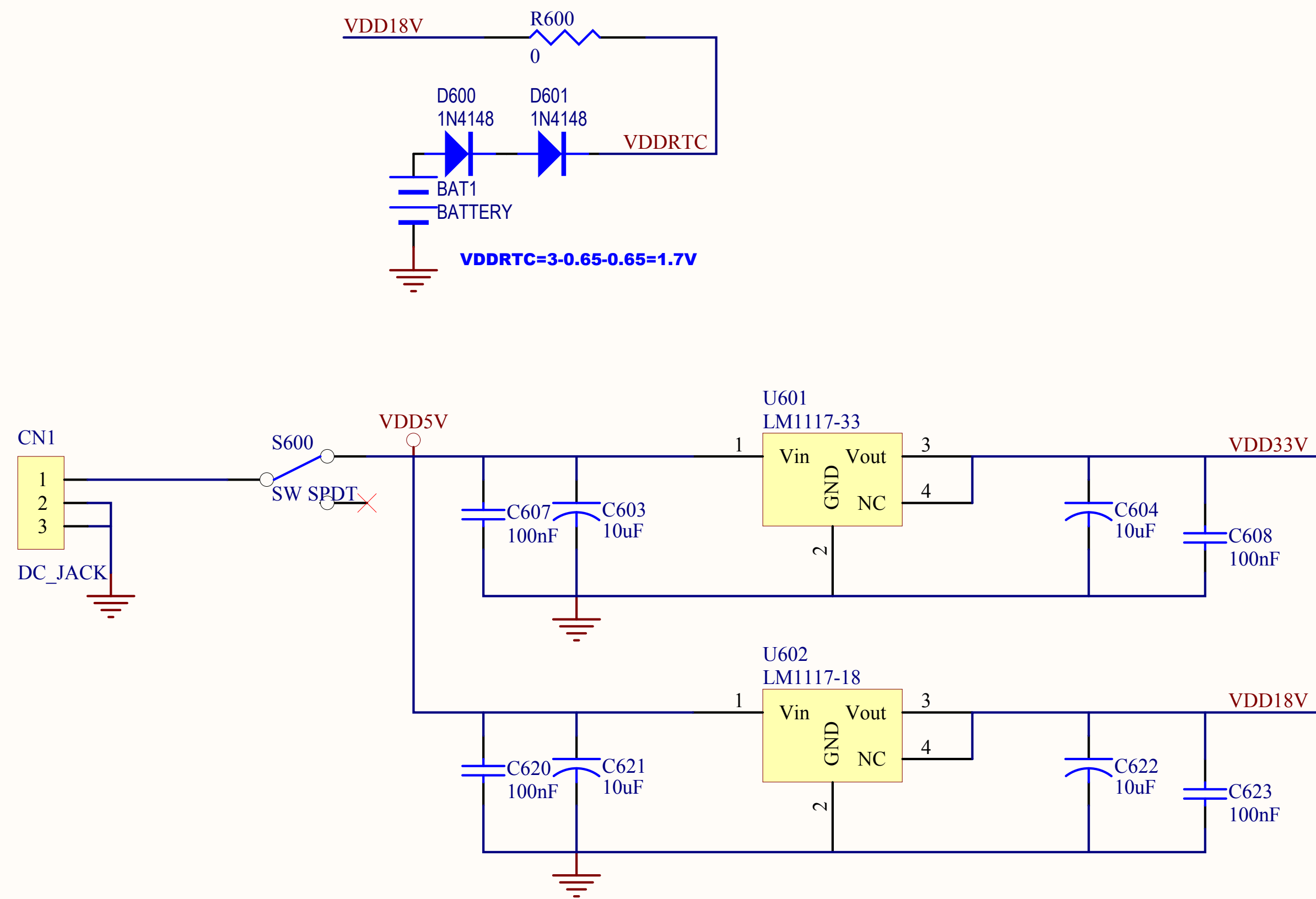


Reset

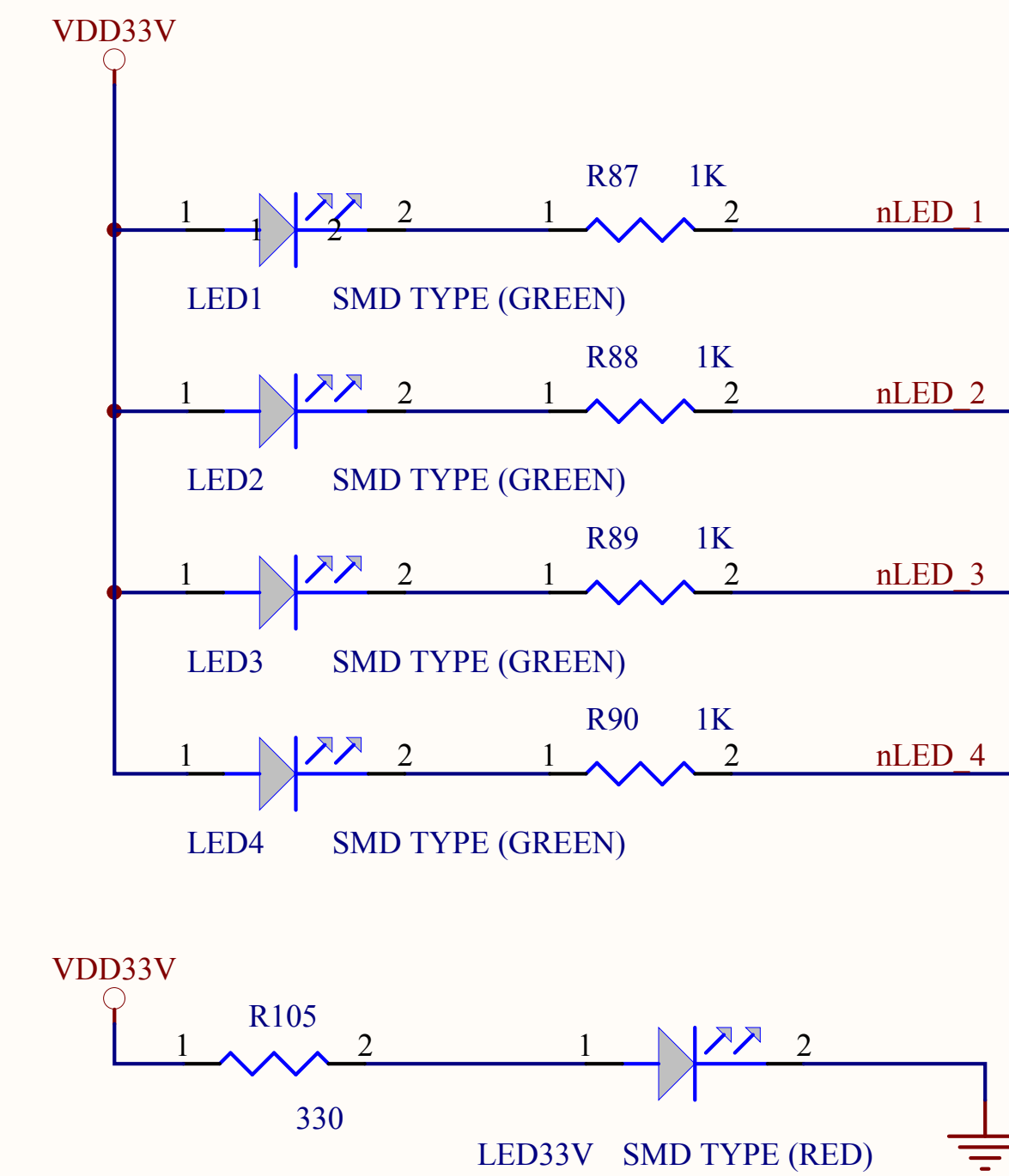


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Date:	2007-3-7	Sheet of
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Power



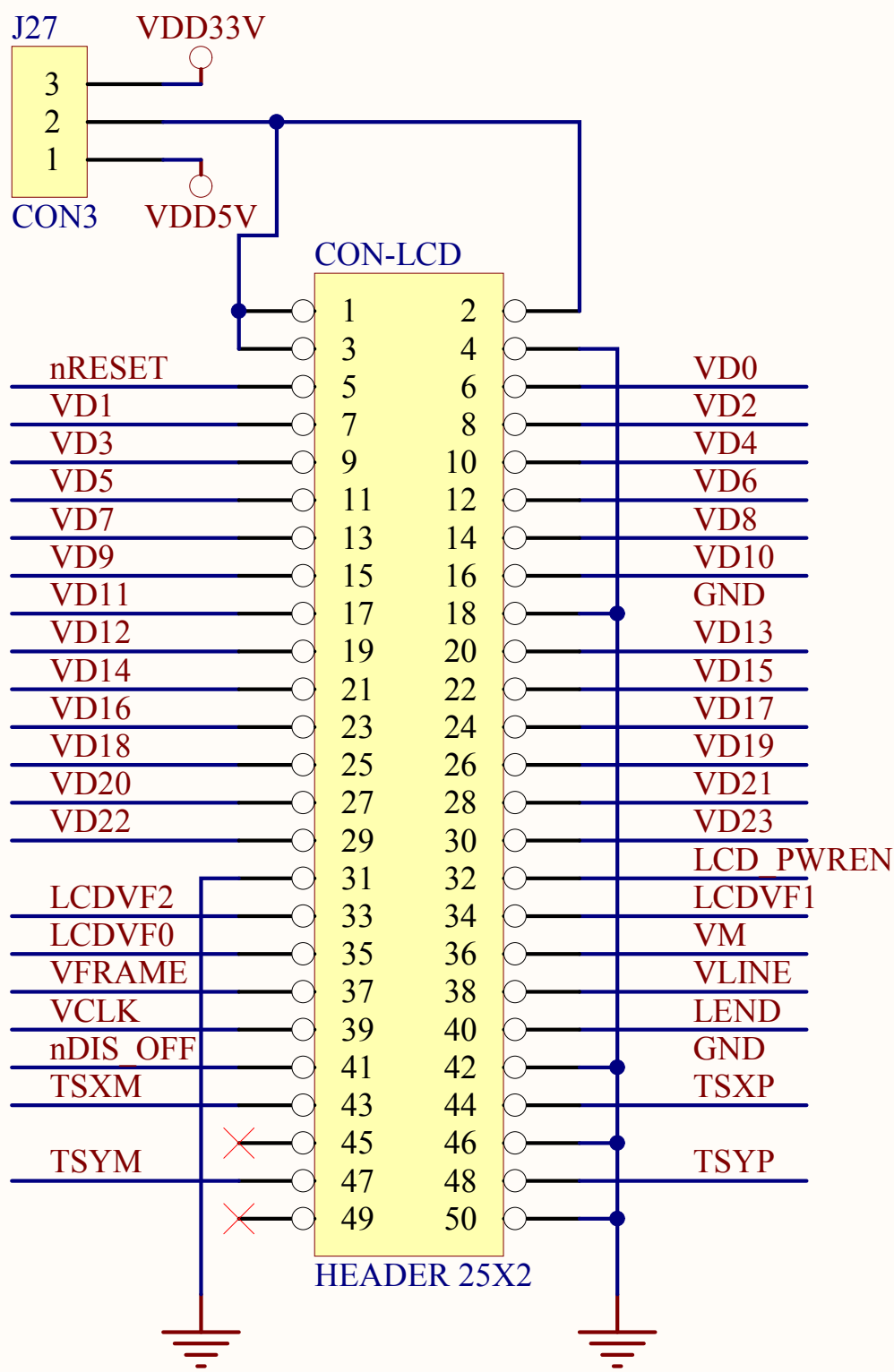
User LED



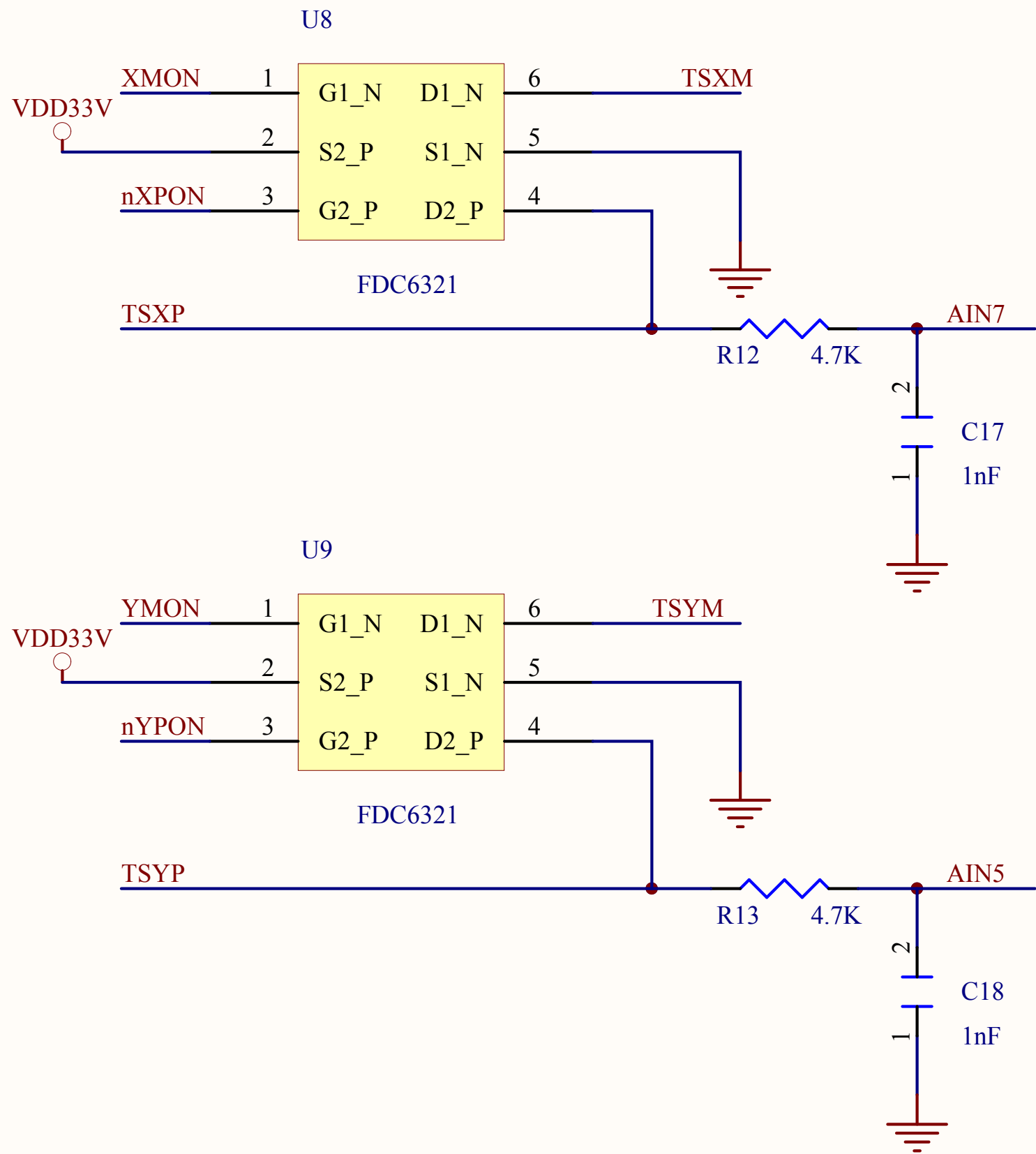
Power & DEBUG LED

Title		
Size B	Number	Revision
Date:	2007-3-7	Sheet of
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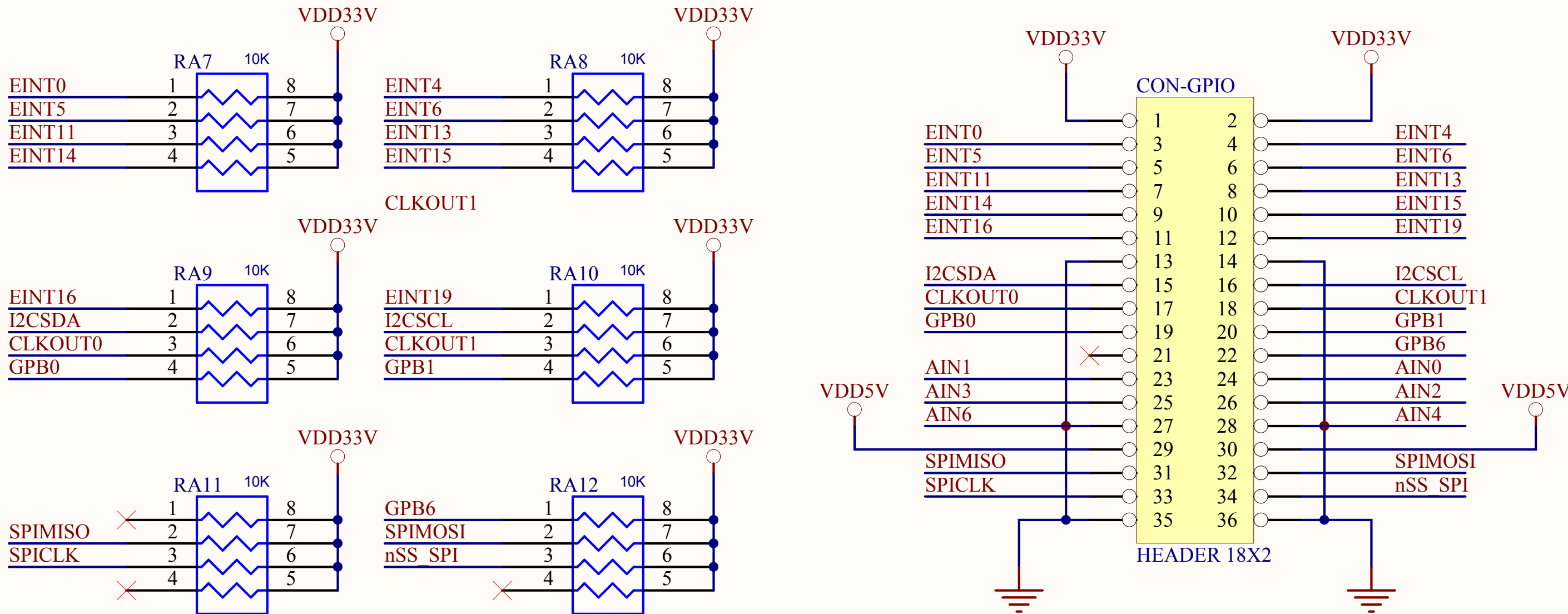
LCD



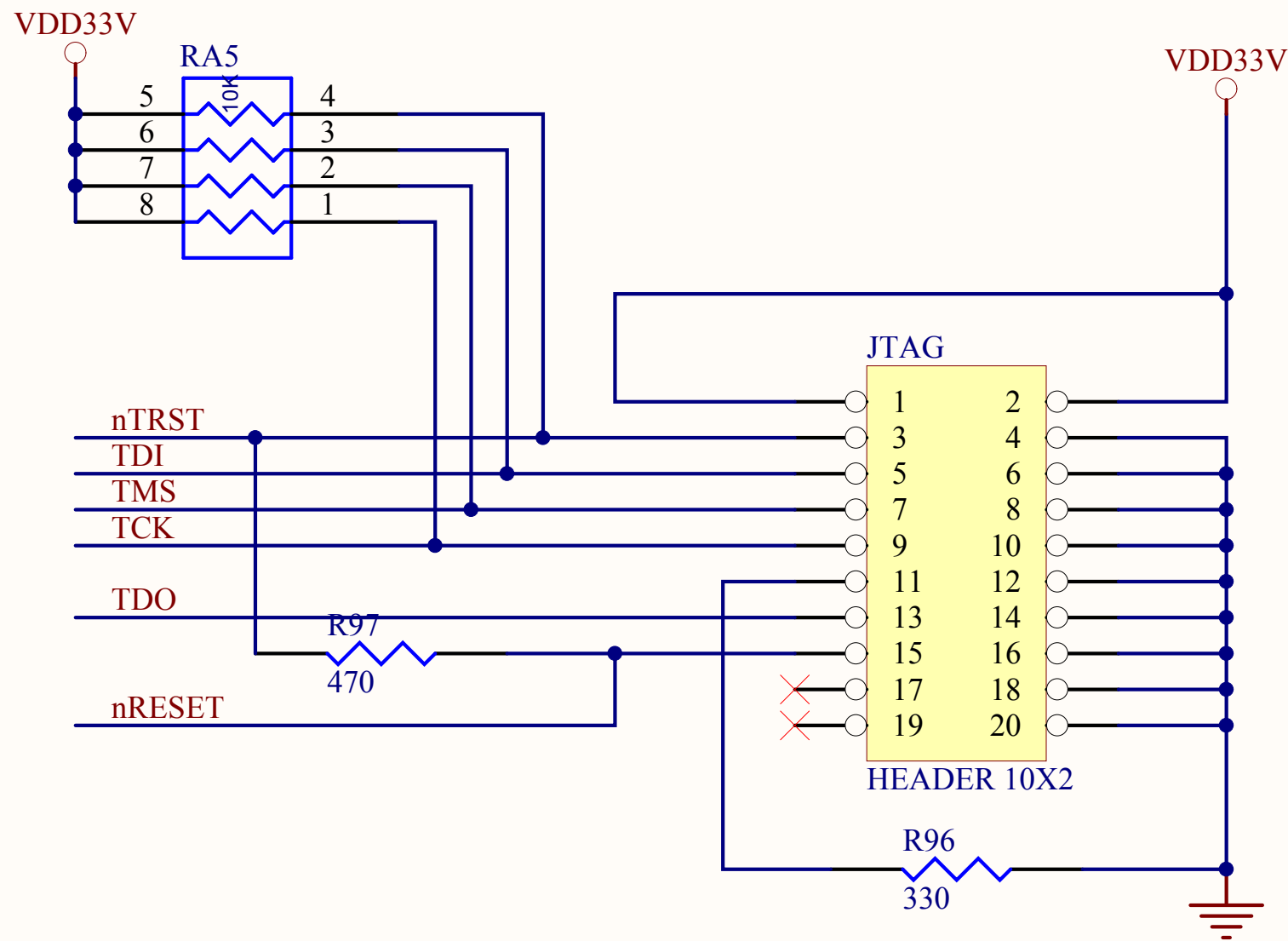
TouchPanel



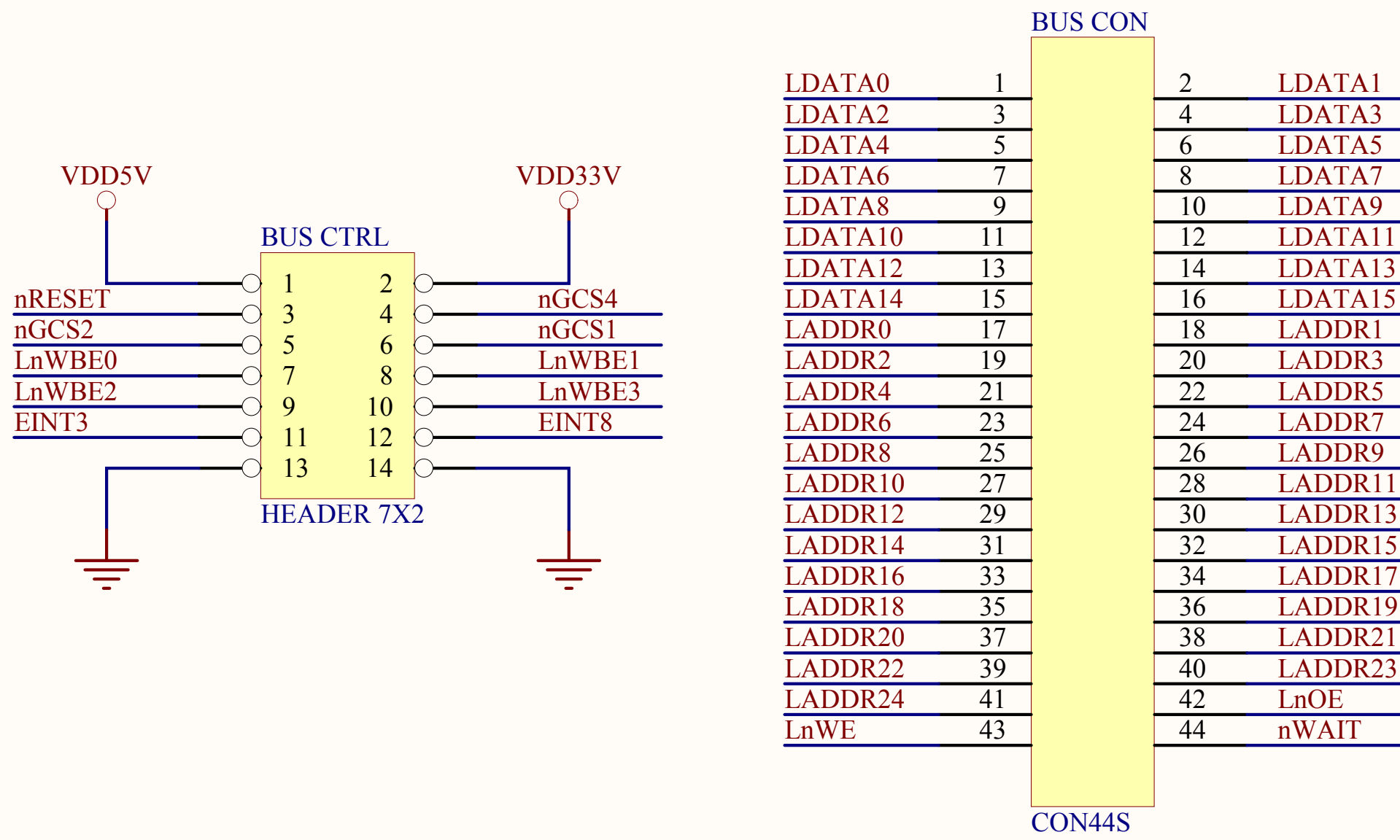
GPIO



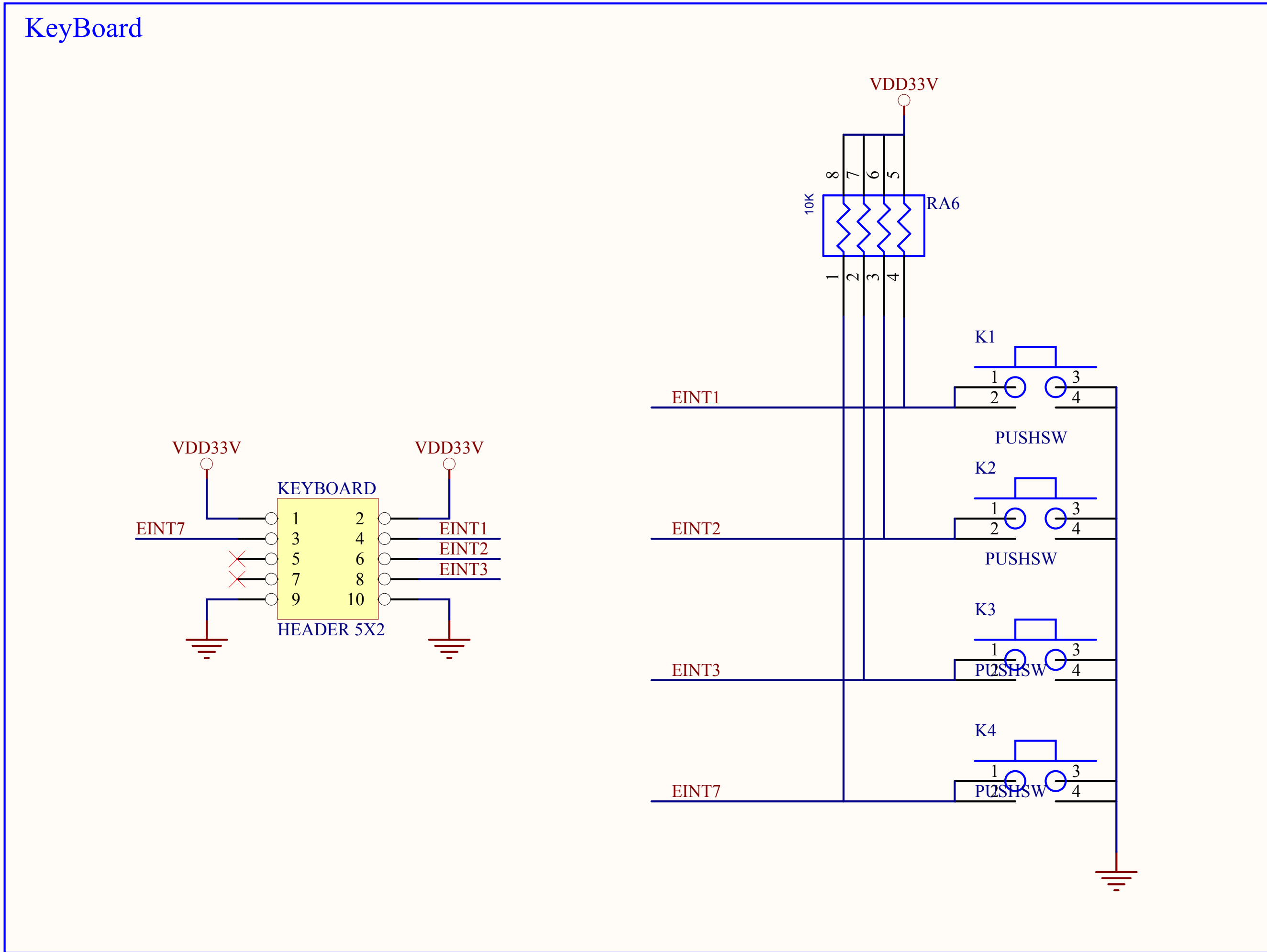
JTAG



SYSTEM BUS



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File:	C:\Documents and Settings\...\07-Interface_Schematic	SDH003



Title		
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File:	C:\Documents and Settings\...\09-KEYBOARD.DWG	1 of 1

