Test bench

EMBEDDED SYSTEM - PS

NIOS II + On-chip Memory

Application in C

Data array 1 = Output buffer

Data array 2 = Input buffer

BSP SPI Software Driver

AVALON bus controller (HW)

SPI CORE (MASTER)

I/O buffers (one Word e.g. 8bits)

MISO MOSI SPICLK CS

"Slave" Module
Verilog (Behavioral)