Year I:  • Algorithm development (latency/area/accuracy)  • Output database definition  • Implementation in scouting system (distributed versus single-FPGA)	<ul> <li>Year II:</li> <li>Implementation and testing in scouting system</li> <li>First deployment at L1 in Run3</li> <li>Analysis of first data</li> </ul>	Year III:  • Development of new algorithm for HL-LHC (based on ParticleFlow)  • Test ParticleFlow-based algorithm at HLT	Year IV:  • Finalise algorithm and hardware implementation for HL-LHC