

27th IFIP/IEEE International Conference on
Very Large Scale Integration

VLSI-SoC 2019

October 6-9, 2019
Cusco, Peru



VLSI-SoC 2019 Best Paper Award

Atishay, Ankit Gupta, Rashmi Sonawat, Helik Kanti Thacker and Prasanth

for the paper entitled **SEARS: A Statistical Error and Redundancy Analysis Simulator** presented at the IFIP/ IEEE 27th International Conference on Very Large Scale Integration – VLSI-SoC 2019 held in Cuzco, Peru, October 6-9, 2019, in recognition of the outstanding contribution to the conference.

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