

```
1  %% Simulation loop start
2  warning('*** Simulation Loop Start')
3  while (kt<=ktmax)
4      k_start = kt+1;
5
6      if kt==ktmax
7          k_end = kt + k_inc(ks);
8      else
9          k_end = kt + k_inc(ks) + 1;
10     end
11
12     for k = k_start:k_end
13         %% step 3a: network solution
14
15         % display k and t at k_inc and every ...th step - thad
16         if ( mod(k,50)==0 ) || k == 1 || k == k_end
17             fprintf('*** k = %5d, \tt(k) = %7.4f\n',k,t(k)) % DEBUG
18         end
19
20         % mach_ref(k) = mac_ang(syn_ref,k);
21         mach_ref(k) = 0;
22         pmech(:,k+1) = pmech(:,k);
23         tmig(:,k+1) = tmig(:,k);
24
25         if n_conv~=0
26             cur_ord(:,k+1) = cur_ord(:,k);
27         end
28
29         % Trip gen - Copied from v2.3 06/01/20 - thad
30         [f,mac_trip_states] = mac_trip_logic(mac_trip_flags,mac_trip_states,t,k);
31         mac_trip_flags = mac_trip_flags | f;
32
33         %% Flag = 1
34         flag = 1;
35         timestep = int2str(k);
36         % network-machine interface
37         mac_ind(0,k,bus_sim,flag);
38         mac_igen(0,k,bus_sim,flag);
39         mac_sub(0,k,bus_sim,flag);
40         mac_tra(0,k,bus_sim,flag);
41         mac_em(0,k,bus_sim,flag);
42         mdc_sig(t(k),k);
43         dc_cont(0,k,10*(k-1)+1,bus_sim,flag);
44
45         %% Calculate current injections and bus voltages and angles
46         if k >= sum(k_inc(1:3))+1
47             %% fault cleared
48             line_sim = line_pf2;
```

```
49     bus_sim = bus_pf2;
50     bus_int = bus_intpf2;
51     Y1 = Y_gpf2;
52     Y2 = Y_gncpf2;
53     Y3 = Y_ncgpf2;
54     Y4 = Y_ncpf2;
55     Vr1 = V_rgpf2;
56     Vr2 = V_rncpf2;
57     bo = bopf2;
58     % i_simu forms the network interface variables
59     if k == 50 % DEBUG - showing of networ solution call
60         warning('*** Performing network solution via i_simu')
61     end
62     %h_sol = i_simu(k,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
63     % duplicate call?
64     % h_sol calculated after this 'if' block...
65
66     elseif k >=sum(k_inc(1:2))+1
67         %% near bus cleared
68         line_sim = line_pf1;
69         bus_sim = bus_pf1;
70         bus_int = bus_intpf1;
71         Y1 = Y_gpf1;
72         Y2 = Y_gncpf1;
73         Y3 = Y_ncgpf1;
74         Y4 = Y_ncpf1;
75         Vr1 = V_rgpf1;
76         Vr2 = V_rncpf1;
77         bo = bopf1;
78
79         %h_sol = i_simu(k,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
80
81     elseif k>=k_inc(1)+1
82         %% fault applied
83         line_sim = line_f;
84         bus_sim = bus_f;
85         bus_int = bus_intf;
86         Y1 = Y_gf;
87         Y2 = Y_gncf;
88         Y3 = Y_ncgf;
89         Y4 = Y_ncf;
90         Vr1 = V_rgf;
91         Vr2 = V_rncf;
92         bo = bof;
93
94         %h_sol = i_simu(k,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
95
96     elseif k<k_inc(1)+1
```

```
97     %% pre fault
98     line_sim = line;
99     bus_sim = bus;
100    bus_int = bus_intprf;
101    Y1 = Y_gprf;
102    Y2 = Y_gncprf;
103    Y3 = Y_ncgprf;
104    Y4 = Y_ncprf;
105    Vr1 = V_rgprf;
106    Vr2 = V_rncprf;
107    bo = boprpf;
108
109    %h_sol = i_simu(k,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
110 end
111
112 %% apply gen trip - added from v2.3 - 06/01/20 - thad
113 if sum(mac_trip_flags)>0.5
114     genBuses = mac_con(mac_trip_flags==1,2);
115     for kB=1:length(genBuses)
116         nL = find(genBuses(kB)==line_sim(:,1) | genBuses(kB)==line_sim(:,2));
117         if isempty(nL); error(' '); end
118         line_sim(nL,4) = 1e7; %make reactance infinity
119     end
120     [Y1,Y2,Y3,Y4,Vr1,Vr2,bo] = red_ybus(bus_sim,line_sim);
121     clear nL kB genBuses
122 end
123
124 %% solve
125 if k == 50 % DEBUG - showing of network solution call
126     warning('*** k == 50; Performing network solution via i_simu')
127 end
128 h_sol = i_simu(k,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
129
130 %% HVDC
131 if ndcr_ud~=0
132     % calculate the new value of bus angles rectifier user defined control
133     tot_states=0;
134     for jj = 1:ndcr_ud
135         b_num1 = dcr_dc{jj,3};
136         b_num2 = dcr_dc{jj,4};
137         conv_num = dcr_dc{jj,2};
138         angdcr(jj,k) = (theta(bus_int(b_num1),k)-theta(bus_int(b_num2),k));
139         dcrd_sig(jj,k)=angdcr(jj,k);
140         st_state = tot_states+1;
141         dcr_states = dcr_dc{jj,7};
142         tot_states = tot_states+dcr_states;
143         ydcrmx=dcr_dc{jj,5};
144         ydcrmn = dcr_dc{jj,6};
```

```
145         dcr_dsig(jj,k) = ...
146
147         ↪ dcr_sud(jj,k,flag,dcr_dc{jj,1},dcrd_sig(jj,k),ydcrmx,ydcrmn,xdcr_dc(st_state:tot_s
148     end
149 end
150 if ndci_ud~=0
151     % calculate the new value of bus angles inverter user defined control
152     for jj = 1:ndci_ud
153         tot_states=0;
154         b_num1 = dci_dc{jj,3};
155         b_num2 = dci_dc{jj,4};
156         conv_num = dci_dc{jj,2};
157         angdci(jj,k)=theta(bus_int(b_num1),k)-theta(bus_int(b_num2),k);
158         dci_dsig(jj,k)=(angdci(jj,k)-angdci(jj,k-1))/(t(k)-t(k-1));
159         st_state = tot_states+1;
160         dci_states = dci_dc{jj,7};
161         tot_states = tot_states+dci_states;
162         ydcimx=dci_dc{jj,5};
163         ydcimn = dci_dc{jj,6};
164         dci_dsig(jj,k) = ...
165
166         ↪ dci_sud(jj,k,flag,dci_dc{jj,1},dci_dsig(jj,k),ydcimx,ydcimn,xdci_dc(st_state:tot_s
167     end
168 end
169
170 dc_cont(0,k,10*(k-1)+1,bus_sim,flag);
171
172 %% network interface for control models
173 dpwf(0,k,bus_sim,flag);
174 mexc_sig(t(k),k);
175 smpexc(0,k,bus_sim,flag);
176 smppi(0,k,bus_sim,flag);
177 exc_st3(0,k,bus_sim,flag);
178 exc_dc12(0,k,bus_sim,flag);
179 mtg_sig(k);
180 tg(0,k,flag);
181 tg_hydro(0,k,bus_sim,flag);
182
183 if n_dcud~=0
184     %% set the new line currents
185     for jj=1:n_dcud
186         l_num = svc_dc{jj,3};
187         svc_num = svc_dc{jj,2};
188         from_bus = bus_int(line_sim(l_num,1));
189         to_bus = bus_int(line_sim(l_num,2));
190         svc_bn = bus_int(svc_con(svc_num,2));
191         V1 = bus_v(from_bus,k);
192         V2 = bus_v(to_bus,k);
```

```
191         R = line_sim(l_num,3);
192         X = line_sim(l_num,4);
193         B = line_sim(l_num,5);
194         tap = line_sim(l_num,6);
195         phi = line_sim(l_num,7);
196         [l_if,l_it] = line_cur(V1,V2,R,X,B,tap,phi);
197
198         if svc_bn == from_bus
199             d_sig(jj,k) = abs(l_if);
200         elseif svc_bn == to_bus
201             d_sig(jj,k) = abs(l_it);
202         end
203     end
204 end
205
206 if n_tcscud~=0
207     % set the new bus voltages
208     for jj=1:n_tcscud
209         b_num = tcsc_dc{jj,3};tcsc_num = tcsc_dc{jj,2};
210         td_sig(jj,k)=abs(bus_v(bus_int(b_num),k));
211     end
212 end
213
214 %% Live plot call
215 livePlotFlag = 1; % for possible fugure sim flags
216 if livePlotFlag
217     livePlot
218 end
219
220 %% step 3b: compute dynamics and integrate
221 flag = 2;
222 sys_freq(k) = 1.0; % why?... 5/21/20
223 mpm_sig(t(k),k);
224 mac_ind(0,k,bus_sim,flag);
225 mac_igen(0,k,bus_sim,flag);
226 mac_sub(0,k,bus_sim,flag);
227 mac_tra(0,k,bus_sim,flag);
228 mac_em(0,k,bus_sim,flag);
229 dpwf(0,k,bus_sim,flag);
230 pss(0,k,bus_sim,flag);
231 mexc_sig(t(k),k);
232 smpexc(0,k,bus_sim,flag);
233 smppi(0,k,bus_sim,flag);
234 exc_st3(0,k,bus_sim,flag);
235 exc_dc12(0,k,bus_sim,flag);
236 mtg_sig(k);
237 tg(0,k,flag);
238 tg_hydro(0,k,bus_sim,flag);
```

```
239
240     if n_svc~=0
241         v_svc = abs(bus_v(bus_int(svc_con(:,2)),k));
242         if n_dcud~=0
243             tot_states=0;
244             for jj = 1:n_dcud
245                 ysvcmx = svc_dc{jj,4};
246                 ysvcmn = svc_dc{jj,5};
247                 svc_num = svc_dc{jj,2};
248                 st_state = tot_states+1;
249                 svc_states = svc_dc{jj,6};
250                 tot_states = tot_states+svc_states;
251
252                 ↪ [svc_dsig(svc_num,k),xsvc_dc(st_state:tot_states,k),dxsvc_dc(st_state:tot_states,k),l
253                 ↪ =...
254
255                 ↪ svc_sud(jj,k,flag,svc_dc{jj,1},d_sig(jj,k),ysvcmx,ysvcmn,xsvc_dc(st_state:tot
256
257             end
258         end
259         msvc_sig(t(k),k);
260         svc(0,k,bus_sim,flag,v_svc);
261     end
262     if n_tcsc~=0
263         if n_tcscud~=0
264             tot_states=0;
265             for jj = 1:n_tcscud
266                 ytcscmx = tcsc_dc{jj,4};
267                 ytcscmn = tcsc_dc{jj,5};
268                 tcsc_num = tcsc_dc{jj,2};
269                 st_state = tot_states+1;
270                 tcsc_states = tcsc_dc{jj,6};
271                 tot_states = tot_states+tcsc_states;
272
273                 ↪ [tcsc_dsig(tcsc_num,k),xtcsc_dc(st_state:tot_states,k),dxtcsc_dc(st_state:tot_stat
274                 ↪ =...
275
276                 ↪ tcsc_sud(jj,k,flag,tcsc_dc{jj,1},td_sig(jj,k),ytcscmx,ytcscmn,xtcsc_dc(st_stat
277
278             end
279         end
280         mtcsc_sig(t(k),k);
281         tcsc(0,k,bus_sim,flag);
282     end
283
284     % modified in v2.3 - thad 06/01/20
285     if g.lmod.n_lmod~=0
286         ml_sig(k); % removed t - thad
287         lmod(0,k,flag); % removed bus input - thad
288     end
```

```
281
282     if n_rlmod~=0
283         rml_sig(t(k),k);
284         rlmod(0,k,bus_sim,flag);
285     end
286
287     %% pwrmod - copied from v2.3 - 06/01/20 -thad
288     if n_pwrmod~=0
289         Pst = cell(n_pwrmod,1);
290         Qst = Pst;
291         for index=1:n_pwrmod
292             Pst{index} = pwrmod_p_sigst{index}(:,k);
293             Qst{index} = pwrmod_q_sigst{index}(:,k);
294         end
295         [~,~,dp,dq,~,~] = pwrmod_dyn(Pst,Qst,bus,t,k,flag,n_pwrmod);
296         if (~iscell(dp) || ~iscell(dq))
297             error('Error in pwrmod_dyn, dp and dq must be cells');
298         end
299         if (any(size(dp)-[n_pwrmod 1]) || any(size(dq)-[n_pwrmod 1]))
300             error('Dimension error in pwrmod_dyn');
301         end
302         for index=1:n_pwrmod
303             if ((size(dp{index},2)~=1) || (size(dq{index},2)~=1))
304                 error('Dimension error in pwrmod_dyn');
305             end
306             if size(dp{index},1)~=size(dpwrmod_p_sigst{index},1)
307                 error('Dimension error in pwrmod_dyn');
308             end
309             if size(dq{index},1)~=size(dpwrmod_q_sigst{index},1)
310                 error('Dimension error in pwrmod_dyn');
311             end
312             dpwrmod_p_sigst{index}(:,k) = dp{index};
313             dpwrmod_q_sigst{index}(:,k) = dq{index};
314         end
315         [P,Q,~,~] = pwrmod_dyn(Pst,Qst,bus,t,k,1,n_pwrmod); %update pwrmod_p_sig and
316         ↪ pwrmod_q_sig
317         if (length(P)~=n_pwrmod) || (length(Q)~=n_pwrmod)
318             error('Dimension error in pwrmod_dyn');
319         end
320         pwrmod_p_sig(:,k) = P;
321         pwrmod_q_sig(:,k) = Q;
322         pwrmod_p(0,k,bus_sim,flag);
323         pwrmod_q(0,k,bus_sim,flag);
324         clear P Q Pst Qst dp dq index
325     end
326
327     %% ivm modulation - copied from v2.3 - 06/01/20 -thad
328     if n_ivm>0
```

```
328     dst = cell(n_ivm,1);
329     est = dst;
330     for index=1:n_ivm
331         dst{index} = ivmmod_d_sigst{index}(:,k);
332         est{index} = ivmmod_e_sigst{index}(:,k);
333     end
334     [d,e,~,~,~,~] = ivmmod_dyn(dst,est,bus,t,k,1); %get internal voltage signals
335     if (length(d)~=n_ivm) || (length(e)~=n_ivm)
336         error('Dimension error in ivmmod_dyn');
337     end
338     ivmmod_d_sig(:,k) = d;
339     ivmmod_e_sig(:,k) = e;
340     mac_ivm(0,k,bus_sim,flag);
341     [~,~,dd,de,~,~] = ivmmod_dyn(dst,est,bus,t,k,flag);
342     if (~iscell(dd) || ~iscell(de))
343         error('Error in ivmmod_dyn, dd and de must be cells');
344     end
345     if (any(size(dd)-[n_ivm 1]) || any(size(de)-[n_ivm 1]))
346         error('Dimension error in ivmmod_dyn');
347     end
348     for index=1:n_ivm
349         if ((size(dd{index},2)~=1) || (size(de{index},2)~=1))
350             error('Dimension error in ivmmod_dyn');
351         end
352         if size(dd{index},1)~=size(divmmod_d_sigst{index},1)
353             error('Dimension error in ivmmod_dyn');
354         end
355         if size(de{index},1)~=size(divmmod_e_sigst{index},1)
356             error('Dimension error in ivmmod_dyn');
357         end
358         divmmod_d_sigst{index}(:,k) = dd{index};
359         divmmod_e_sigst{index}(:,k) = de{index};
360     end
361     clear d e dd de dst est
362 end
363
364
365 %% integrate dc at ten times rate (DC Stuff? 5/14/20)
366 mdc_sig(t(k),k);
367 if n_conv~=0
368     hdc_sol = h_sol/10;
369     for kk = 1:10
370         kdc=10*(k-1)+kk;
371
372         ↪ [xdcr_dc(:,kdc:kdc+1),dxdcr_dc(:,kdc:kdc+1),xdci_dc(:,kdc:kdc+1),dxdci_dc(:,kdc:kdc+1),
373         ↪ = ...
374         dc_sim(k,kk,dcr_dc,dci_dc,xdcr_dc(:,kdc),xdci_dc(:,kdc),bus_sim,hdc_sol);
375     end
376 end
```



```
374     else
375         dc_cont(0,k,k,bus_sim,2);
376         dc_line(0,k,k,bus_sim,2);
377     end
378
379     %% following statements are predictor steps
380     j = k+1;
381     mac_ang(:,j) = mac_ang(:,k) + h_sol*dmac_ang(:,k);
382     mac_spd(:,j) = mac_spd(:,k) + h_sol*dmac_spd(:,k);
383     edprime(:,j) = edprime(:,k) + h_sol*dedprime(:,k);
384     eqprime(:,j) = eqprime(:,k) + h_sol*deqprime(:,k);
385     psikd(:,j) = psikd(:,k) + h_sol*dpsikd(:,k);
386     psikq(:,j) = psikq(:,k) + h_sol*dpsikq(:,k);
387     Efd(:,j) = Efd(:,k) + h_sol*dEfd(:,k);
388     V_R(:,j) = V_R(:,k) + h_sol*dV_R(:,k);
389     V_As(:,j) = V_As(:,k) + h_sol*dV_As(:,k);
390     R_f(:,j) = R_f(:,k) + h_sol*dR_f(:,k);
391     V_TR(:,j) = V_TR(:,k) + h_sol*dV_TR(:,k);
392     sdpw1(:,j) = sdpw1(:,k) + h_sol*dsdpw1(:,k);
393     sdpw2(:,j) = sdpw2(:,k) + h_sol*dsdpw2(:,k);
394     sdpw3(:,j) = sdpw3(:,k) + h_sol*dsdpw3(:,k);
395     sdpw4(:,j) = sdpw4(:,k) + h_sol*dsdpw4(:,k);
396     sdpw5(:,j) = sdpw5(:,k) + h_sol*dsdpw5(:,k);
397     sdpw6(:,j) = sdpw6(:,k) + h_sol*dsdpw6(:,k);
398     pss1(:,j) = pss1(:,k) + h_sol*dpss1(:,k);
399     pss2(:,j) = pss2(:,k) + h_sol*dpss2(:,k);
400     pss3(:,j) = pss3(:,k) + h_sol*dpss3(:,k);
401
402     % modified to g - thad
403     g.tg.tg1(:,j) = g.tg.tg1(:,k) + h_sol*g.tg.dtg1(:,k);
404     g.tg.tg2(:,j) = g.tg.tg2(:,k) + h_sol*g.tg.dtg2(:,k);
405     g.tg.tg3(:,j) = g.tg.tg3(:,k) + h_sol*g.tg.dtg3(:,k);
406     g.tg.tg4(:,j) = g.tg.tg4(:,k) + h_sol*g.tg.dtg4(:,k);
407     g.tg.tg5(:,j) = g.tg.tg5(:,k) + h_sol*g.tg.dtg5(:,k);
408
409     vdp(:,j) = vdp(:,k) + h_sol*dvdp(:,k);
410     vqp(:,j) = vqp(:,k) + h_sol*dvqp(:,k);
411     slip(:,j) = slip(:,k) + h_sol*dslip(:,k);
412     vdpig(:,j) = vdpig(:,k) + h_sol*dvdpig(:,k);
413     vqpig(:,j) = vqpig(:,k) + h_sol*dvqpig(:,k);
414     slig(:,j) = slig(:,k) + h_sol*dslig(:,k);
415     B_cv(:,j) = B_cv(:,k) + h_sol*dB_cv(:,k);
416     B_con(:,j) = B_con(:,k) + h_sol*dB_con(:,k);
417     xsvc_dc(:,j) = xsvc_dc(:,k) + h_sol* dxsvc_dc(:,k);
418     B_tcsc(:,j) = B_tcsc(:,k) + h_sol*dB_tcsc(:,k);
419     xtcsc_dc(:,j) = xtcsc_dc(:,k) + h_sol* dxtcsc_dc(:,k);
420
421     %lmod_st(:,j) = lmod_st(:,k) + h_sol*dlmod_st(:,k); % original line - thad
```

```
422     g.lmod.lmod_st(:,j) = g.lmod.lmod_st(:,k) + h_sol*g.lmod.dlmod_st(:,k); % line using g
423
424     rlmod_st(:,j) = rlmod_st(:,k)+h_sol*drlmod_st(:,k);
425
426     %% Copied from v2.3 - 06/01/20 - thad
427     pwrmod_p_st(:,j) = pwrmod_p_st(:,k)+h_sol*dpwrmod_p_st(:,k);
428     pwrmod_q_st(:,j) = pwrmod_q_st(:,k)+h_sol*dpwrmod_q_st(:,k);
429     %% pwrmod
430     if n_pwrmod~=0
431         for index=1:n_pwrmod
432             pwrmod_p_sigst{index}(:,j) =
433                 ↪ pwrmod_p_sigst{index}(:,k)+h_sol*dpwrmod_p_sigst{index}(:,k);
434             pwrmod_q_sigst{index}(:,j) =
435                 ↪ pwrmod_q_sigst{index}(:,k)+h_sol*dpwrmod_q_sigst{index}(:,k);
436         end
437     end
438     %% ivmmod
439     if n_ivm~=0
440         for index=1:n_ivm
441             ivmmod_d_sigst{index}(:,j) =
442                 ↪ ivmmod_d_sigst{index}(:,k)+h_sol*divmmod_d_sigst{index}(:,k);
443             ivmmod_e_sigst{index}(:,j) =
444                 ↪ ivmmod_e_sigst{index}(:,k)+h_sol*divmmod_e_sigst{index}(:,k);
445         end
446     end
447
448     %% Flag = 1
449     flag = 1;
450     % mach_ref(j) = mac_ang(syn_ref,j);
451     mach_ref(j) = 0;
452     % perform network interface calculations again with predicted states
453     mpm_sig(t(j),j);
454     mac_ind(0,j,bus_sim,flag);
455     mac_igen(0,j,bus_sim,flag);
456     mac_sub(0,j,bus_sim,flag);
457     mac_tra(0,j,bus_sim,flag);
458     mac_em(0,j,bus_sim,flag);
459     % assume Vdc remains unchanged for first pass through dc controls interface
460     mdc_sig(t(j),j);
461     dc_cont(0,j,10*(j-1)+1,bus_sim,flag);
462
463     % Calculate current injections and bus voltages and angles
464     if j >= sum(k_inc(1:3))+1
465         % fault cleared
466         bus_sim = bus_pf2;
467         bus_int = bus_intpf2;
468         Y1 = Y_gpf2;
```

```
466     Y2 = Y_gncpf2;
467     Y3 = Y_ncgpf2;
468     Y4 = Y_ncpf2;
469     Vr1 = V_rgpf2;
470     Vr2 = V_rncpf2;
471     bo = bopf2;
472     %h_sol = i_simu(j,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
473 elseif j >=sum(k_inc(1:2))+1
474     % near bus cleared
475     bus_sim = bus_pf1;
476     bus_int = bus_intpf1;
477     Y1 = Y_gpf1;
478     Y2 = Y_gncpf1;
479     Y3 = Y_ncgpf1;
480     Y4 = Y_ncpf1;
481     Vr1 = V_rgpf1;
482     Vr2 = V_rncpf1;
483     bo = bopf1;
484     %h_sol = i_simu(j,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
485 elseif j>=k_inc(1)+1
486     % fault applied
487     bus_sim = bus_f;
488     bus_int = bus_intf;
489     Y1 = Y_gf;
490     Y2 = Y_gncf;
491     Y3 = Y_ncgf;
492     Y4 = Y_ncf;
493     Vr1 = V_rgf;
494     Vr2 = V_rncf;
495     bo = bof;
496     %h_sol = i_simu(j,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
497 elseif k<k_inc(1)+1 % JHC - DKF thinks k should be j
498     % pre fault
499     bus_sim = bus;
500     bus_int = bus_intprf;
501     Y1 = Y_gprf;
502     Y2 = Y_gncprf;
503     Y3 = Y_ncgprf;
504     Y4 = Y_ncprf;
505     Vr1 = V_rgprf;
506     Vr2 = V_rncprf;
507     bo = boprpf;
508     %h_sol = i_simu(j,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);
509 end
510
511 % apply gen trip - copied from v2.3 - 06/01/20 - thad
512 if sum(mac_trip_flags)>0.5
513     genBuses = mac_con(mac_trip_flags==1,2);
```

```
514     for kB=1:length(genBuses)
515         nL = find(genBuses(kB)==line_sim(:,1) | genBuses(kB)==line_sim(:,2));
516         if isempty(nL)
517             error('nL is empty.');
```

518 end

519 line_sim(nL,4) = 1e7; *%make reactance infinity*

520 end

521 [Y1,Y2,Y3,Y4,Vr1,Vr2,bo] = red_ybus(bus_sim,line_sim);

522 clear nL kB genBuses

523 end

524

525 *%% solve*

526 if k == 50 *% DEBUG - showing of network solution call*

527 warning('*** k == 50; Performing network solution via i_simu')

528 end

529 h_sol = i_simu(j,ks,k_inc,h,bus_sim,Y1,Y2,Y3,Y4,Vr1,Vr2,bo);

530

531 vex(:,j)=vex(:,k);

532 cur_ord(:,j) = cur_ord(:,k);

533 *% calculate the new value of bus angles rectifier user defined control*

534 if ndcr_ud~=0

535 tot_states=0;

536 for jj = 1:ndcr_ud

537 b_num1 = dcr_dc{jj,3};

538 b_num2 = dcr_dc{jj,4};

539 conv_num = dcr_dc{jj,2};

540 angdcr(jj,j) = theta(bus_int(b_num1),j)-theta(bus_int(b_num2),j);

541 dcrd_sig(jj,j)=angdcr(jj,j);

542 st_state = tot_states+1;

543 dcr_states = dcr_dc{jj,7};

544 tot_states = tot_states+dcr_states;

545 ydcrmx=dcr_dc{jj,5};ydcrmn = dcr_dc{jj,6};

546 dcr_dsig(jj,j) = ...

547

548 ↪ dcr_sud(jj,j,flag,dcr_dc{jj,1},dcrd_sig(jj,j),ydcrmx,ydcrmn,xdcr_dc(st_state:tot_states));

549 end

550 end

551 if ndci_ud~=0

552 *% calculate the new value of bus angles inverter user defined control*

553 for jj = 1:ndci_ud

554 tot_states=0;

555 b_num1 = dci_dc{jj,3};

556 b_num2 = dci_dc{jj,4};

557 conv_num = dci_dc{jj,2};

558 angdci(jj,j) = theta(bus_int(b_num1),j)-theta(bus_int(b_num2),j);

559 dci_dsig(jj,j) = (angdci(jj,j)-angdci(jj,k))/(t(j)-t(k));

560 st_state = tot_states+1;

561 dci_states = dci_dc{jj,7};

```
561         tot_states = tot_states+dc_i_states;
562         ydcimx=dc_i_dc{jj,5};
563         ydcimn = dc_i_dc{jj,6};
564         dc_i_dsig(jj,j) = ...
565
566         ↪ dc_i_sud(jj,j,flag,dc_i_dc{jj,1},dcid_sig(jj,j),ydcimx,ydcimn,xdc_i_dc(st_state:tot_s
567     end
568 end
569
570 dc_cont(0,j,10*(j-1)+1,bus_sim,flag);
571 dpwf(0,j,bus_sim,flag);
572 pss(0,j,bus_sim,flag);
573 mexc_sig(t(j),j);
574 smpec(0,j,bus_sim,flag);
575 smppi(0,j,bus_sim,flag);
576 exc_st3(0,j,bus_sim,flag);
577 exc_dc12(0,j,bus_sim,flag);
578 tg(0,j,flag);
579 tg_hydro(0,j,bus_sim,flag);
580
581 if n_dcud~=0
582     % set the new line currents
583     for jj=1:n_dcud
584         l_num = svc_dc{jj,3};svc_num = svc_dc{jj,2};
585         from_bus = bus_int(line_sim(l_num,1));
586         to_bus = bus_int(line_sim(l_num,2));
587         svc_bn = bus_int(svc_con(svc_num,2));
588         V1 = bus_v(from_bus,j);
589         V2 = bus_v(to_bus,j);
590         R = line_sim(l_num,3);
591         X = line_sim(l_num,4);
592         B = line_sim(l_num,5);
593         tap = line_sim(l_num,6);phi = line_sim(l_num,7);
594         [l_if,l_it] = line_cur(V1,V2,R,X,B,tap,phi);
595         if svc_bn == from_bus;
596             d_sig(jj,j)=abs(l_if);
597         elseif svc_bn==to_bus;
598             d_sig(jj,j)=abs(l_it);
599         end
600     end
601 end
602
603 if n_tcscud~=0
604     % set the new line currents
605     for jj=1:n_tcscud
606         b_num = tcsc_dc{jj,3};
607         tcsc_num = tcsc_dc{jj,2};
608         td_sig(jj,j) = abs(bus_v(bus_int(b_num),j));
```

```
608         end
609     end
610
611     %% Flag = 2, for 'corrector step' d's
612     flag = 2;
613     mac_ind(0,j,bus_sim,flag);
614     mac_igen(0,j,bus_sim,flag);
615     mac_sub(0,j,bus_sim,flag);
616     mac_tra(0,j,bus_sim,flag);
617     mac_em(0,j,bus_sim,flag);
618     dpwf(0,j,bus_sim,flag);
619     pss(0,j,bus_sim,flag);
620     mexc_sig(t(j),j);
621     smpexc(0,j,bus_sim,flag);
622     smppi(0,j,bus_sim,flag);
623     exc_st3(0,j,bus_sim,flag);
624     exc_dc12(0,j,bus_sim,flag);
625     mtg_sig(j);
626     tg(0,j,flag);
627     tg_hydro(0,j,bus_sim,flag);
628
629     if n_svc~=0
630         msvc_sig(t(j),j);
631         if n_dcud~=0
632             tot_states=0;
633             for jj = 1:n_dcud
634                 ysvcmx = svc_dc{jj,4};
635                 ysvcmn = svc_dc{jj,5};
636                 svc_num = svc_dc{jj,2};
637                 st_state = tot_states+1;
638                 svc_states = svc_dc{jj,6};
639                 tot_states = tot_states+svc_states;
640
641                 ↪ [svc_dsig(svc_num,j),xsvc_dc(st_state:tot_states,j),dxsvc_dc(st_state:tot_states,j),
642                 ↪ =...
643
644                 ↪ svc_sud(jj,j,flag,svc_dc{jj,1},d_sig(jj,j),ysvcmx,ysvcmn,xsvc_dc(st_state:tot_states,j));
645             end
646         end
647     end
648     v_svc = abs(bus_v(bus_int(svc_con(:,2)),j));
649     bus_sim = svc(0,j,bus_sim,flag,v_svc);
650 end
651
652 if n_tcsc~=0
653     mtcsc_sig(t(j),j); % this has changed since v 2.3...
654     if n_tcscud~=0
655         tot_states=0;
656         for jj = 1:n_tcscud
```

```
653         ytcscmx = tcsc_dc{jj,4};
654         ytcscmn = tcsc_dc{jj,5};
655         tcsc_num = tcsc_dc{jj,2};
656         st_state = tot_states+1;
657         tcsc_states = tcsc_dc{jj,6};
658         tot_states = tot_states+tcsc_states;
659
        ↪ [tcsc_dsig(tcsc_num,j),xtcsc_dc(st_state:tot_states,j),dxtcsc_dc(st_state:tot_states,j),
        ↪ ...
660
        ↪ tcsc_sud(jj,j,flag,tcsc_dc{jj,1},td_sig(jj,j),ytcscmx,ytcscmn,xtcsc_dc(st_state:tot_states,j));
661     end
662 end
663 tcsc(0,j,bus_sim,flag);
664 end
665
666 % modified to handle g - thad 06/01/20
667 if g.lmod.n_lmod~=0
668     ml_sig(j); % removed t - thad
669     lmod(0,j,flag); % removed bus - thad
670 end
671 if n_rlmod~=0
672     rml_sig(t(j),j);
673     rlmod(0,j,bus_sim,flag);
674 end
675
676 % copied from v2.3 - thad - 06/01/20
677 if n_pwrmod~=0
678     Pst = cell(n_pwrmod,1);
679     Qst = Pst;
680     for index=1:n_pwrmod
681         Pst{index} = pwrmod_p_sigst{index}(:,j);
682         Qst{index} = pwrmod_q_sigst{index}(:,j);
683     end
684     [~,~,dp,dq,~,~] = pwrmod_dyn(Pst,Qst,bus,t,j,flag,n_pwrmod);
685     if (~iscell(dp) || ~iscell(dq))
686         error('Error in pwrmod_dyn, dp and dq must be cells');
687     end
688     if (any(size(dp)-[n_pwrmod 1]) || any(size(dq)-[n_pwrmod 1]))
689         error('Dimension error in pwrmod_dyn');
690     end
691
692     for index=1:n_pwrmod
693         if ((size(dp{index},2)~=1) || (size(dq{index},2)~=1))
694             error('Dimension error in pwrmod_dyn');
695         end
696         if size(dp{index},1)~=size(dpwrmod_p_sigst{index},1)
697             error('Dimension error in pwrmod_dyn');
```

```
698         end
699         if size(dq{index},1)~=size(dpwrmod_q_sigst{index},1)
700             error('Dimension error in pwrmod_dyn');
701         end
702         dpwrmod_p_sigst{index}(:,j) = dp{index};
703         dpwrmod_q_sigst{index}(:,j) = dq{index};
704     end
705     [P,Q,~,~,~] = pwrmod_dyn(Pst,Qst,bus,t,j,1,n_pwrmod); %update pwrmod_p_sig and
706     ↪ pwrmod_q_sig
707     if (length(P)~=n_pwrmod) || (length(Q)~=n_pwrmod)
708         error('Dimension error in pwrmod_dyn');
709     end
710     pwrmod_p_sig(:,j) = P;
711     pwrmod_q_sig(:,j) = Q;
712     pwrmod_p(0,j,bus_sim,flag);
713     pwrmod_q(0,j,bus_sim,flag);
714     clear P Q Pst Qst dp dq index
715 end
716 if n_ivm>0
717     dst = cell(n_ivm,1);
718     est = dst;
719     for index=1:n_ivm
720         dst{index} = ivmmod_d_sigst{index}(:,j);
721         est{index} = ivmmod_e_sigst{index}(:,j);
722     end
723     [d,e,~,~,~] = ivmmod_dyn(dst,est,bus,t,j,1);
724     if (length(d)~=n_ivm) || (length(e)~=n_ivm)
725         error('Dimension error in ivmmod_dyn');
726     end
727     ivmmod_d_sig(:,j) = d;
728     ivmmod_e_sig(:,j) = e;
729     mac_ivm(0,j,bus_sim,flag);
730     [~,~,dd,de,~,~] = ivmmod_dyn(dst,est,bus,t,j,flag);
731     if (~iscell(dd) || ~iscell(de))
732         error('Error in ivmmod_dyn, dd and de must be cells');
733     end
734     if (any(size(dd)-[n_ivm 1]) || any(size(de)-[n_ivm 1]))
735         error('Dimension error in ivmmod_dyn');
736     end
737     for index=1:n_ivm
738         if ((size(dd{index},2)~=1) || (size(de{index},2)~=1))
739             error('Dimension error in ivmmod_dyn');
740         end
741         if size(dd{index},1)~=size(divmmod_d_sigst{index},1)
742             error('Dimension error in ivmmod_dyn');
743         end
744         if size(de{index},1)~=size(divmmod_e_sigst{index},1)
```



```
745         error('Dimension error in ivmmod_dyn');
746     end
747     divmmod_d_sigst{index}(:,j) = dd{index};
748     divmmod_e_sigst{index}(:,j) = de{index};
749     end
750     clear d e dd de dst est
751 end
752 % end copied from...
753
754 if n_conv~=0
755     hdc_sol = h_sol/10;
756     for kk = 1:10
757         jdc=10*(j-1)+kk;
758
759         ↪ [xdcr_dc(:,jdc:jdc+1),dxdcr_dc(:,jdc:jdc+1),xdci_dc(:,jdc:jdc+1),dxdci_dc(:,jdc:jdc+1),
760         ↪ = ...
761         dc_sim(j,kk,dcr_dc,dci_dc,xdcr_dc(:,jdc),xdci_dc(:,jdc),bus_sim,hdc_sol);
762     end
763 else
764     dc_cont(0,j,j,bus_sim,2);
765     dc_line(0,j,j,bus_sim,2);
766 end
767
768 %% following statements are corrector steps
769 mac_ang(:,j) = mac_ang(:,k) + h_sol*(dmac_ang(:,k)+dmac_ang(:,j))/2.;
770 mac_spd(:,j) = mac_spd(:,k) + h_sol*(dmac_spd(:,k)+dmac_spd(:,j))/2.;
771 edprime(:,j) = edprime(:,k) + h_sol*(dedprime(:,k)+dedprime(:,j))/2.;
772 eqprime(:,j) = eqprime(:,k) + h_sol*(deqprime(:,k)+deqprime(:,j))/2.;
773 psikd(:,j) = psikd(:,k) + h_sol*(dpsikd(:,k)+dpsikd(:,j))/2.;
774 psikq(:,j) = psikq(:,k) + h_sol*(dpsikq(:,k)+dpsikq(:,j))/2.;
775 Efd(:,j) = Efd(:,k) + h_sol*(dEfd(:,k)+dEfd(:,j))/2.;
776 V_R(:,j) = V_R(:,k) + h_sol*(dV_R(:,k)+dV_R(:,j))/2.;
777 V_As(:,j) = V_As(:,k) + h_sol*(dV_As(:,k)+dV_As(:,j))/2.;
778 R_f(:,j) = R_f(:,k) + h_sol*(dR_f(:,k)+dR_f(:,j))/2.;
779 V_TR(:,j) = V_TR(:,k) + h_sol*(dV_TR(:,k)+dV_TR(:,j))/2.;
780 sdpw1(:,j) = sdpw1(:,k) +h_sol*(dsdpw1(:,k)+dsdpw1(:,j))/2.;
781 sdpw12(:,j) = sdpw2(:,k) +h_sol*(dsdpw2(:,k)+dsdpw2(:,j))/2.;
782 sdpw13(:,j) = sdpw3(:,k) +h_sol*(dsdpw3(:,k)+dsdpw3(:,j))/2.;
783 sdpw14(:,j) = sdpw4(:,k) +h_sol*(dsdpw4(:,k)+dsdpw4(:,j))/2.;
784 sdpw15(:,j) = sdpw5(:,k) +h_sol*(dsdpw5(:,k)+dsdpw5(:,j))/2.;
785 sdpw16(:,j) = sdpw6(:,k) +h_sol*(dsdpw6(:,k)+dsdpw6(:,j))/2.;
786 pss1(:,j) = pss1(:,k) +h_sol*(dpss1(:,k)+dpss1(:,j))/2.;
787 pss2(:,j) = pss2(:,k) +h_sol*(dpss2(:,k)+dpss2(:,j))/2.;
788 pss3(:,j) = pss3(:,k) +h_sol*(dpss3(:,k)+dpss3(:,j))/2.;
789
790 % modified to g
791 g.tg.tg1(:,j) = g.tg.tg1(:,k) + h_sol*(g.tg.dtg1(:,k) + g.tg.dtg1(:,j))/2.;
792 g.tg.tg2(:,j) = g.tg.tg2(:,k) + h_sol*(g.tg.dtg2(:,k) + g.tg.dtg2(:,j))/2.;
```

```
791 g.tg.tg3(:,j) = g.tg.tg3(:,k) + h_sol*(g.tg.dtg3(:,k) + g.tg.dtg3(:,j))/2.;
792 g.tg.tg4(:,j) = g.tg.tg4(:,k) + h_sol*(g.tg.dtg4(:,k) + g.tg.dtg4(:,j))/2.;
793 g.tg.tg5(:,j) = g.tg.tg5(:,k) + h_sol*(g.tg.dtg5(:,k) + g.tg.dtg5(:,j))/2.;
794
795 vdp(:,j) = vdp(:,k) + h_sol*(dvdv(:,j) + dvdv(:,k))/2.;
796 vqp(:,j) = vqp(:,k) + h_sol*(dvqp(:,j) + dvqp(:,k))/2.;
797 slip(:,j) = slip(:,k) + h_sol*(dslip(:,j) + dslip(:,k))/2.;
798 vdpig(:,j) = vdpig(:,k) + h_sol*(dvdpig(:,j) + dvdpig(:,k))/2.;
799 vqpig(:,j) = vqpig(:,k) + h_sol*(dvqpig(:,j) + dvqpig(:,k))/2.;
800 slig(:,j) = slig(:,k) + h_sol*(dslig(:,j) + dslig(:,k))/2.;
801 B_cv(:,j) = B_cv(:,k) + h_sol*(dB_cv(:,j) + dB_cv(:,k))/2.;
802 B_con(:,j) = B_con(:,k) + h_sol*(dB_con(:,j) + dB_con(:,k))/2.;
803 xsvc_dc(:,j) = xsvc_dc(:,k) + h_sol*(dxsvc_dc(:,j) + dxsvc_dc(:,k))/2.;
804 B_tcsc(:,j) = B_tcsc(:,k) + h_sol*(dB_tcsc(:,j) + dB_tcsc(:,k))/2.;
805 xtcsc_dc(:,j) = xtcsc_dc(:,k) + h_sol*(dxtcsc_dc(:,j) + dxtcsc_dc(:,k))/2.;
806
807 % modified to g
808 g.lmod.lmod_st(:,j) = g.lmod.lmod_st(:,k) + h_sol*(g.lmod.dlmod_st(:,j) +
    ↪ g.lmod.dlmod_st(:,k))/2.; % modified line with g
809
810 rlmod_st(:,j) = rlmod_st(:,k) + h_sol*(drlmod_st(:,j) + drlmod_st(:,k))/2.;
811
812 % Copied from v2.3 - 06/01/20 - thad
813 pwrmod_p_st(:,j) = pwrmod_p_st(:,k)+h_sol*(dpwrmod_p_st(:,j) + dpwrmod_p_st(:,k))/2;
814 pwrmod_q_st(:,j) = pwrmod_q_st(:,k)+h_sol*(dpwrmod_q_st(:,j) + dpwrmod_q_st(:,k))/2;
815 if n_pwrmod~=0
816     for index=1:n_pwrmod
817         pwrmod_p_sigst{index}(:,j) =
            ↪ pwrmod_p_sigst{index}(:,k)+h_sol*(dpwrmod_p_sigst{index}(:,j) +
            ↪ dpwrmod_p_sigst{index}(:,k))/2;
818         pwrmod_q_sigst{index}(:,j) =
            ↪ pwrmod_q_sigst{index}(:,k)+h_sol*(dpwrmod_q_sigst{index}(:,j) +
            ↪ dpwrmod_q_sigst{index}(:,k))/2;
819     end
820 end
821 if n_ivm~=0
822     for index=1:n_ivm
823         ivmmod_d_sigst{index}(:,j) =
            ↪ ivmmod_d_sigst{index}(:,k)+h_sol*(divmmod_d_sigst{index}(:,j) +
            ↪ divmmod_d_sigst{index}(:,k))/2;
824         ivmmod_e_sigst{index}(:,j) =
            ↪ ivmmod_e_sigst{index}(:,k)+h_sol*(divmmod_e_sigst{index}(:,j) +
            ↪ divmmod_e_sigst{index}(:,k))/2;
825     end
826 end
827
828 end
829 % counter increment
```

```
830     kt = kt + k_inc(ks);  
831     ks = ks+1;  
832 end% end simulation loop
```
