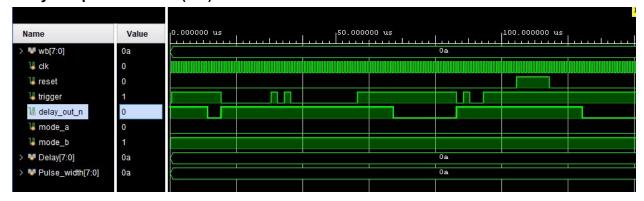
### Simulation Results, Vivado 2020.1

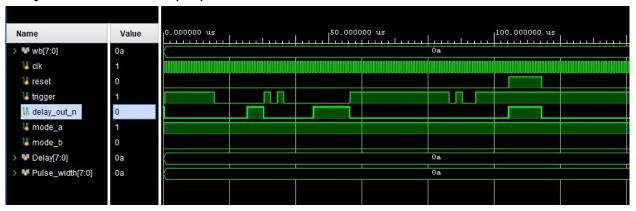
# One Shot Mode (OS) : AB = 00



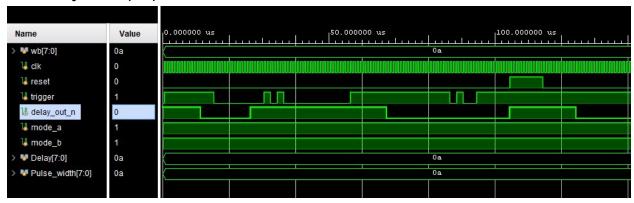
### Delayed Operated Mode (DO): AB = 01



## Delayed Released Mode (DR): AB = 10



# Dual Delay Mode (DD) : AB = 11



Credit goes to fpga4student.com for testbench code.