

Duc thang Nguyen

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Profile

A motivated international engineering student with a steadfast dedication to excellence. Equipped with exceptional problem-solving abilities, adept at clarifying ambiguous requirements, and driven to continuously acquire, cultivate, and apply theoretical knowledge for optimal organizational impact. A results-oriented engineer well-versed in system engineering and research methodology.

Skills

① TECHNICAL SKILLS

C, C++



Matlab



Python



FPGA (Verilog, VHDL)



Machine Learning (Pytorch, Matlab)



Microsoft Office



Strengths

- # Leadership, teamwork, adaptability
- # Strong communication, organization, problem-solving
- # Critical thinking, time management, initiative

Education

Electrical and Electronic | Engineering Ho Chi Minh University of Technology

📅 2017 – 2021 📍 VIETNAM

Computer Engineering | Bachelor of Engineering (Honours), Electrical and Electronic The University of Adelaide

📅 2021 – present 📍 ADELAIDE, AUSTRALIA

Project

Implement inverse polish notation using stack (C language)

Image processing by FPGA (Verilog)

Train station management with object-oriented programming design (C++)

- OOP properties such as Encapsulation, Abstraction, Inheritance, and Polymorphism are practiced.

Routing protocol simulation with Distance Vector and Split Horizon algorithms (C++)

- This project requires simulating a routing protocol that applies a Distance Vector and Split Horizon algorithm to generate a routing table for a given set of routers and link weights.

Machine learning techniques for radar target recognition (MATLAB and Python)

- Honour project
- Detail: GitHub: <https://bit.ly/446FNgo>