



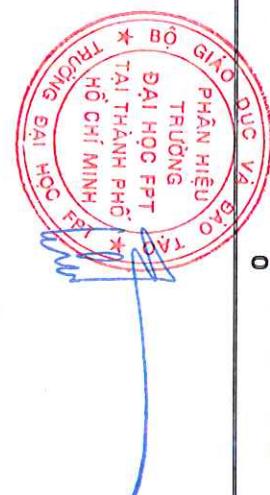
PERFORMANCE STATEMENT

YEAR I

ENROLLMENT NO :	JK-ENR-HA-11715
NAME :	NGUYỄN THANH HÀO
CENTRE :	FPT-CHIP DESIGN-HCM
DURATION :	From 07-Feb-25 to 18-Jun-25

Modular Examination	Number of hours	Marks Obtained			Result
		Theory (Max: 30)	Practical (Max: 35)	Assignment (Max: 35)	
1 C/C++ Programming Fundamentals	42	27	33.3	33.3	93.5 Pass
2 Digital Logic	58	23	35	32	90 Pass
3 Electronics Fundamentals and Circuit Analysis	12	29	28	29.7	86.8 Pass
4 Exam & Projects 01	24	n/a	n/a	99	99 Pass
5 Semiconductor physics and materials science	38	28	29.8	29.8	87.5 Pass
6 Analog, Digital Design and Computer Architecture	40				
7 Communication Systems and Linear circuits	32				
8 Familiarizing with Chip industry, IC Design & Applications	56				
9 Exam & Projects 02	16				
10 Steps to Chip Design	52				
Evaluation Details		Max Mark	Marks Obtained	Grade	
1 Overall Weighted Final Score for the Semester 01		100	94.2	Excellent	
2 Overall Weighted Final Score for the Semester 02		100			
Summary					
1 Σ Modules Studied		5			
2 Σ Fail Modules		0			

Award of Grades:
 Excellent (A+) (90% and above);
 Very Good (A) (80%-89%);
 Good (B+) (70%-79%);
 Satisfactory (B) (60%-69%);
 Average(C) (<50%).



14-Jul-2025
Date of Issue

LE THI HONG HANH
 DIRECTOR