

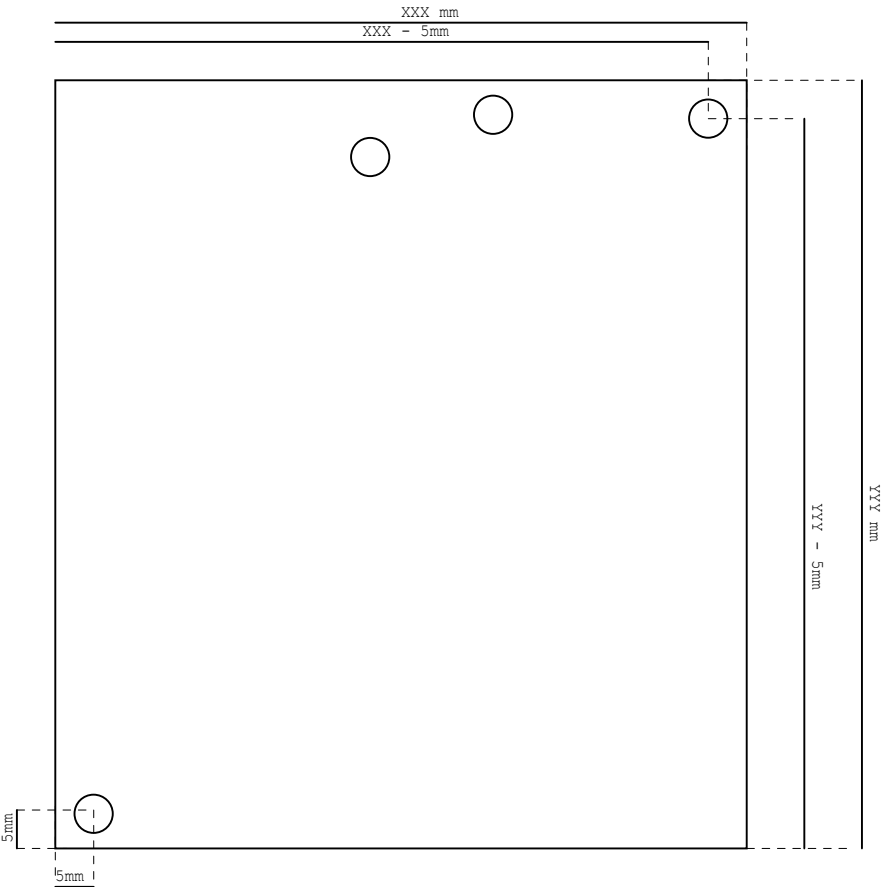
ZDP03A rev 2, Z-Wave Development Platform.

PCB Layers

Layer 1: Signal
Layer 2: Power (GND)
Layer 3: Power (VCC)
Layer 4: Signal

Schematic

[SCHEMATIC2](#)



Layout Constrains

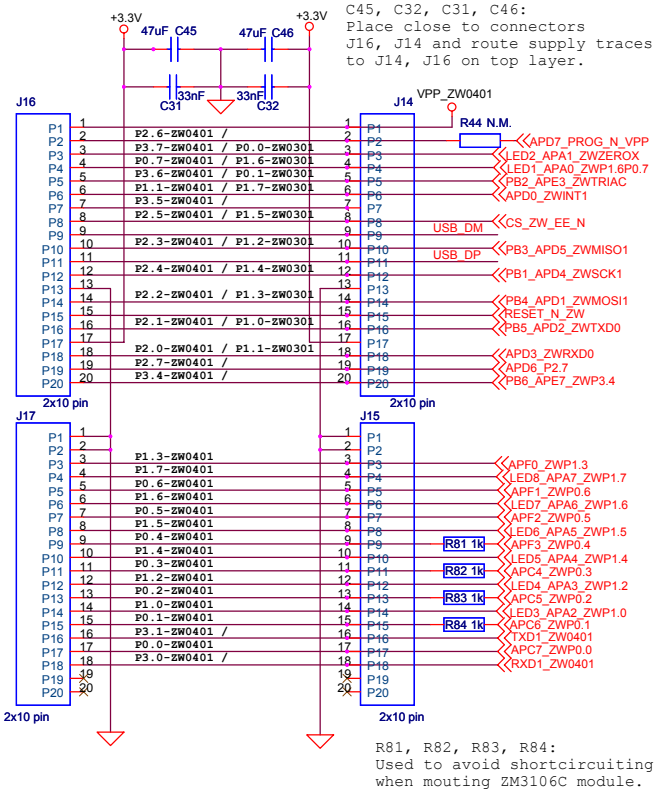
- 1: Place J3 and J4 near each other and with 2mm spacing
Leave 10mm wide space next to J3 and J4 for label.
- 2: Place J14/J15 and J16/J17 at least 9mm a part
- 3: Spacing between push buttons as big as possible

Zensys A/S
Emdrupvej 26
DK-2100 Copenhagen
Denmark

<Variant Name>

Title		
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A4	954015200	1
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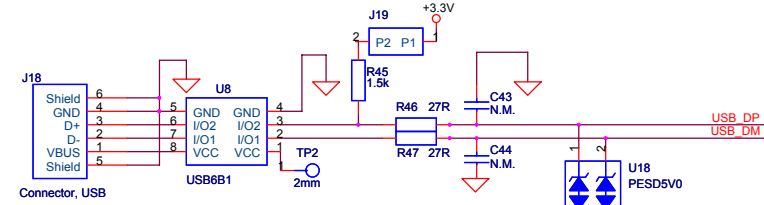
Application connectors



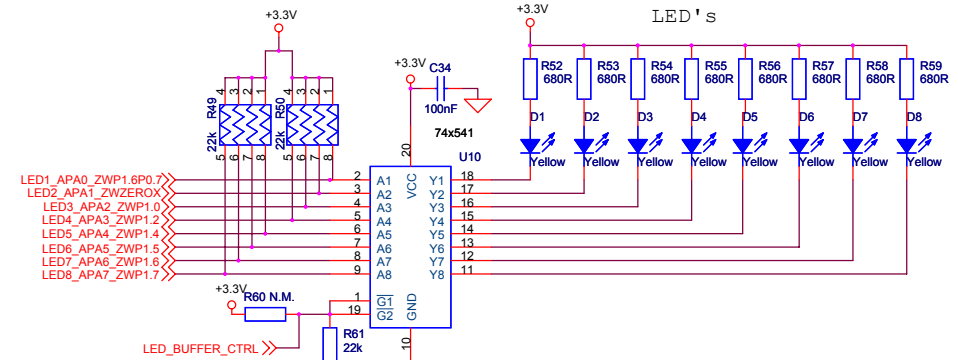
H1
Hole 4.00mm

H2
Hole 4.00mm

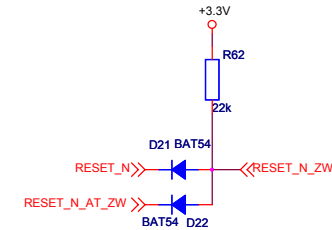
USB interface



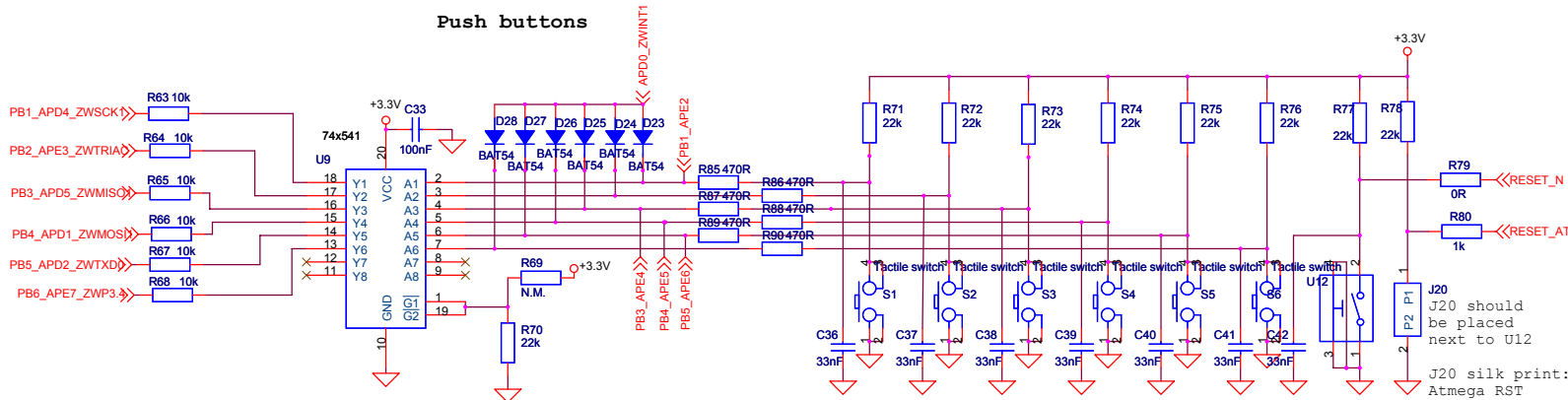
LEDs



Reset circuitry to ZW module



Push buttons



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