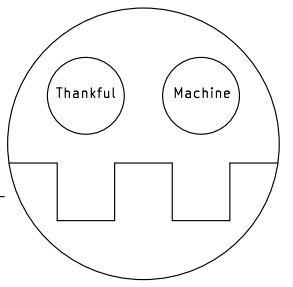


This might be incomplete or incorrect. You might blow up your precious 68K. Be careful.

Have fun!



GAL22V10
ADDRDECO

A23 A22 A21 A5 FC0 FC1 FC2 XRODTACK XR1DTACK NC NC GND
NC NC NC NC NC DTACK IACK XR1CE XROCE RAMCE ROMCE VCC

/IACK = FC0 * FC1 * FC2
/RAMCE = A23 * /A5 * IACK
/ROMCE = /A23 * /A22 * /A5 * IACK
/XROCE = /A23 * A22 * /A21 * /A5 * IACK
/XR1CE = /A23 * A22 * A21 * /A5 * IACK
DTACK = XRODTACK * XRODTACK * /ROMCE * /RAMCE

DESCRIPTION

Address decoder for M68000-based computer.
Incomplete decoding, many areas mirrored.

Programming:
% galsim --v addrdeco.pld
% minipro --device "ATF22V10C(UES)" --write addrdeco.jed

Address Space:
00000000 ROM
00400000 XRO
00600000 XR1
00800000 RAM

DTACK:
Memories are assumed to be fast enough to have "instant"
or "grounded" DTACK. The 68681 serial chips
respond accordingly.