

Mg. Thant Zin Win
 BCS - 1690
 Computer Architecture
 Assignment 1

1. let $X(x_1, x_0)$ be the first 2 bit binary number.
 let $Y(y_1, y_0)$ be the second 2 bit binary number.
 The summation of x_0 and y_0 can be done using half adder circuit because there is no carry input.

inputs		outputs	
x_0	y_0	s_0	c_0
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

$$s_0 = \bar{x}_0 y_0 + x_0 \bar{y}_0$$

$$c_0 = x_0 y_0$$

The summation of x_1 and y_1 must be the full adder circuit because there is carry input.

inputs			outputs	
x_1	y_1	c_0	s_1	c_1
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
1	0	0	1	0
0	1	1	0	1
1	1	0	0	1
1	0	1	0	1
1	1	1	1	1

$$\begin{aligned} s_1 &= \bar{x}_1 \bar{y}_1 c_0 + \bar{x}_1 y_1 \bar{c}_0 + x_1 \bar{y}_1 \bar{c}_0 + x_1 y_1 c_0 \\ &= \bar{x}_1 \bar{y}_1 x_0 y_0 + \bar{x}_1 y_1 \bar{x}_0 \bar{y}_0 + x_1 \bar{y}_1 \bar{x}_0 y_0 + x_1 y_1 x_0 \bar{y}_0 \\ &= \bar{x}_1 \bar{y}_1 x_0 y_0 + \bar{x}_1 y_1 (\bar{x}_0 + \bar{y}_0) + x_1 \bar{y}_1 (\bar{x}_0 + y_0) + x_1 y_1 x_0 y_0 \\ &= \bar{x}_1 \bar{y}_1 x_0 y_0 + \bar{x}_1 y_1 \bar{x}_0 + \bar{x}_1 y_1 \bar{y}_0 + x_1 \bar{y}_1 \bar{x}_0 + x_1 \bar{y}_1 y_0 + x_1 y_1 x_0 y_0 \end{aligned}$$

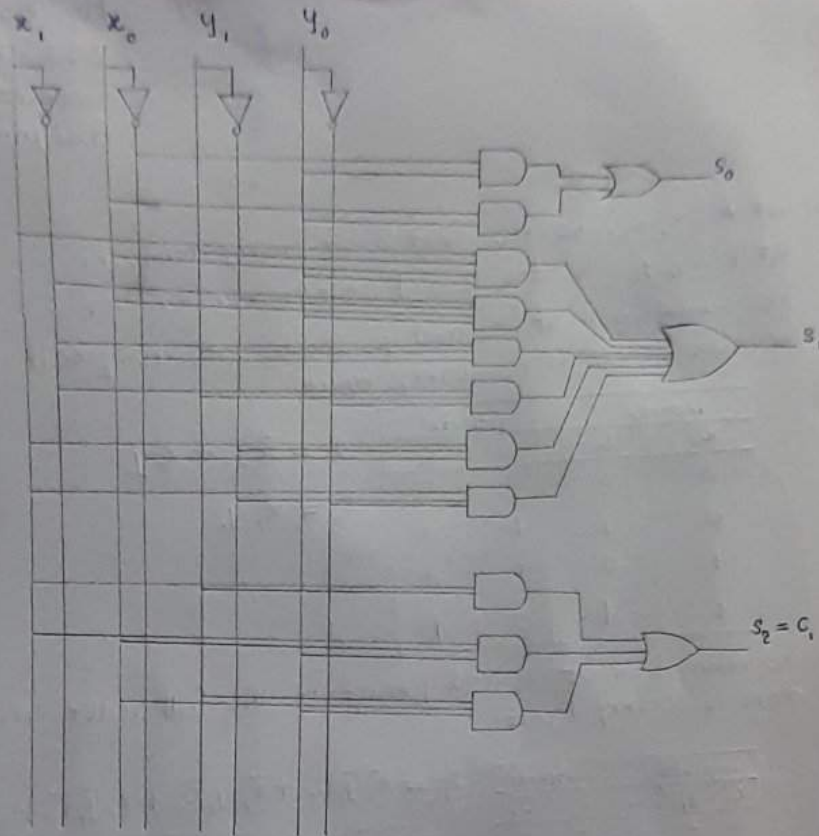
$$\begin{aligned} c_1 &= x_1 y_1 + y_1 c_0 + x_1 c_0 \\ &= x_1 y_1 + y_1 x_0 y_0 + x_1 x_0 y_0 \end{aligned}$$

for s_1

$x_1 y_1$	c_0	0	1
00			1
01		1	
11			1
10		1	

for c_1

$x_1 y_1$	c_0	0	1
00			
01			1
11		1	1
10			1



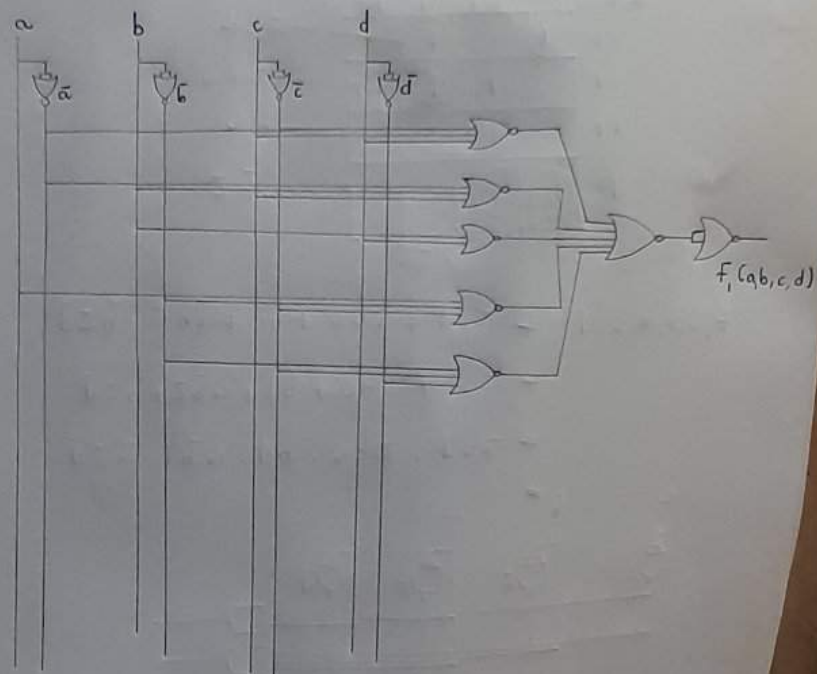
A two level combinational circuit design in SOP that computes the 3 bit sum of two 2 bit binary numbers using AND and OR gate

$$\begin{aligned} f_1(a, b, c, d) &= a(b+c)d + \bar{a}(b+d)(c+d) + \bar{b}\bar{c}\bar{d} \\ &= abd + a\bar{c}d + (\bar{a}b + \bar{a}d)(bc + bd + c + cd) + \bar{b}\bar{c}\bar{d} \\ &= abd + a\bar{c}d + \bar{a}bc + \bar{a}bd + \bar{a}bc + \bar{a}bd + \bar{a}bcd + \bar{a}bcd + \bar{a}bcd + \bar{a}bcd + \bar{b}\bar{c}\bar{d} \\ &= abd + a\bar{c}d + \bar{a}bc + \bar{a}bd + \bar{b}\bar{c}\bar{d} + \bar{a}bcd + \bar{a}bcd + \bar{a}bcd \end{aligned}$$

$$\begin{aligned} F_1(a, b, c, d) &= \overline{a}cd + \overline{a}bc + bd + a\overline{b}\overline{c} + \overline{b}\overline{c}\overline{d} \\ &= \overline{a}cd + \overline{a}bc + bd + a\overline{b}\overline{c} + \overline{b}\overline{c}\overline{d} \\ &= \overline{a}cd \cdot \overline{a}bc \cdot bd \cdot a\overline{b}\overline{c} \cdot \overline{b}\overline{c}\overline{d} \end{aligned}$$

All NAND realization for F_1

$$\begin{aligned}
 \text{c) } f_1(a, b, c, d) &= \overline{a}cd + \overline{a}bc + bd + a\overline{b}\overline{c} + \overline{b}\overline{c}\overline{d} \\
 &= \overline{a}cd + \overline{a}bc + bd + a\overline{b}\overline{c} + \overline{b}\overline{c}\overline{d} \\
 &= (\overline{a}+c+d)(\overline{a}+b+c)(b+d)(a+\overline{b}+\overline{c})(\overline{b}+\overline{c}+\overline{d}) \\
 &= \overline{(\overline{a}+c+d) + (\overline{a}+b+c) + (b+d) + (a+\overline{b}+\overline{c}) + (\overline{b}+\overline{c}+\overline{d})}
 \end{aligned}$$



All NOR Design for f_1