DISSERTATION TITLE, THAT CAN SPAN OVER MULTIPLE LINES IF NEEDED

First Last

Submitted to the Faculty of the Graduate School
of Augusta University in partial fulfillment
of the Requirements of the Degree of
Master of Science

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Acknowledgements

This part serves two purposes.

To write the acknowledgments (as a "*Thank you note*"). You can look for inspiration Chisholm, n.d. if you need some.

To include a detailed summary of the work performed by other authors on published or accepted manuscripts used in the thesis / dissertation, if applicable.

Abstract

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The abstract must not exceed 350 words. It must consist of the briefest possible summary of

the thesis / dissertation and the conclusions reached. Explanatory matter and opinion must

be omitted.

Keywords: Key1· Key2· A longer keyword

Table of Contents

1.	Intro	oduction	1
	1.1.	Title Levels	2
		1.1.1. Subsection	2
	1.2.	Debugging	3
2.	Refe	erences and Bibliography	4
3.	Writ	ting Mathematics	5
	3.1.	Theorem, Proof, and Others Environments	5
	3.2.	Formal Proofs	6
4.	Inse	rting PDFs, Figures, Tables and (Code) Listings	7
	4.1.	Inserting PDFs	7
		4.1.1. A paper proving concisely a result in automata theory that helped	
		solve a real programming problem Glück, 2013	8
	4.2.	Inserting Figures	17
	4.3.	Inserting Tables	17
	4.4.	Inserting Code Listings	18
5.	Land	dscape Pages	21
6.	Mar	gins and Fonts	23
	6.1.	Margins	23
	6.2.	Fonts	24
		6.2.1 Rody	24

6.2.2.	Symbols	 	 	25
References				26
A. Appendix	A (Optional)			27

List of Tables

1.	The price of categories	 	1
2.	Illustrating how to align entries in a table	 	18
3.	The price and advantages of fruits	 	1

List of Figures

1.	D'un autre âge	•			 	 ŀ	1′
2.	Difference between programming languages (s	sim	plifi	ed)			22

List of Listings

1.	An inductive definition in Coq			٠	•	•					•		•	19
2.	How to use braces ({and }) in bash									•	•			19
3.	"Hello World" in C													19

1. Introduction

This document is a guide on how to use it ("how meta!"), and its structure does not reflect the structure of a Thesis: you will need to erase (almost) all of its body and fill it with your own, organized in a coherent manner respectful of your reader's expectations, of your fields guidelines, and in agreement with your advisor. Note that The Graduate School recommends using APA (American Psychological Association, 2023) and that using another style requires approval of its Dean prior to writing the thesis or dissertation.

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1.1. Title Levels

As indicated in the koma-script manual, the class scrbook that is used for this document has access to 6 levels of titles:

- 1 \chapter{Test}
- 2 \section{Test}
- 3 \subsection{Test}
- 4 \subsubsection{Test}
- 5 \paragraph{Test}
- 6 \subparagraph{Test}

Only Chapters, Sections and Subsections will appear in the table of contents, by design.

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1.1.1. Subsection

This is a subsection.

1.1.1.1. Subsubsection

This is a subsubsection.

1.1.1.1. Paragraph This is a paragraph.

1.1.1.1.1. Sub-Paragraph This is a sub-paragraph.

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You can insert hyperlinks in different ways, including hyperlinks to this document¹ using e.g. the link automatically added to all chapters, following the convention described in pandoc's manual.

¹You may note that the footnote number is itself a link.

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You can add additional unicode symbols that may not be supported by this template or its font using the model

1 \newunicodechar{<unicode symbol>}{\ensuremath{<latex command>}}

(in head_c.tex in the markdown version), in this case additionally forcing the symbol <unicode symbol> to be rendered in math mode using \ensuremath.

3.1. Theorem, Proof, and Others Environments

Markdown only You can state e.g. theorems and proofs using pandoc's built-in "Definition list", that are rendered as description environments in LATEX.

Theorem Every $n \in \mathbb{N}$, n > 1 has a unique prime factorization.

Proof Carl Friedrich Gauss told me so.

To insert numbered theorems, definitions, and the like, and be able to reference them or add automatically the "qed" (\square) symbol, you need to use LATEX's theorem environment, label

commands, etc. Note that, by default, proofs are unnumbered environments, but that there are ways to reference them if you want to.

Theorem 1 (Pythagoras theorem). $\forall a,b,c,a^2+b^2=c^2$.

Proof. Proving Theorem 1 is not that easy.

Markdown only If you would rather keep the "pure" markdown syntax but improve pandoc using a filter, you can look at the pandoc filter "statement" and its discussion on related filters, but it may be more difficult to install and use properly.

3.2. Formal Proofs

You can easily represent formal proofs using LATEX's ebproof or bussproof packages:

$$\begin{array}{ccc} & [A] & [B] \\ \vdots & \vdots \\ A \vee B & C & C \\ \hline C & & \\ \end{array} \vee \mathbf{E}$$

4. Inserting PDFs, Figures, Tables and (Code) Listings

4.1. Inserting PDFs

PDF documents can be inserted using pdfpages's \includepdf command. For commodity, a \modifiedincludepdf is provided:

- 1 \modifiedincludepdf{options for includepdf}%
- 2 {label}%
- 3 {full path to the document}%
- 4 {title of the document}%
- 5 {"level" (e.g., section, subsection, etc.)}

Note that using label.x will refer to the page x of the inserted document (starting with 1): refer to the source code of this current document for an example usage. We insert in the following pages (pp. 8-16) an article as an example of PDF insertion.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 1/9)

Simulation of Two-Way Pushdown Automata Revisited

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Dedicated to David A. Schmidt on the Occasion of his 60th Birthday

The linear-time simulation of 2-way deterministic pushdown automata (2DPDA) by the Cook and Jones constructions is revisited. Following the semantics-based approach by Jones, an interpreter is given which, when extended with random-access memory, performs a linear-time simulation of 2DPDA. The recursive interpreter works without the dump list of the original constructions, which makes Cook's insight into linear-time simulation of exponential-time automata more intuitive and the complexity argument clearer. The simulation is then extended to 2-way nondeterministic pushdown automata (2NPDA) to provide for a cubic-time recognition of context-free languages. The time required to run the final construction depends on the degree of nondeterminism. The key mechanism that enables the polynomial-time simulations is the sharing of computations by memoization.

1 Introduction

We revisit a result from theoretical computer science from a programming perspective. Cook's surprising theorem [4] showed that *two-way deterministic pushdown automata* (2DPDA) can be simulated faster on a random-access machine (in linear time) than they may run natively (in exponential time). This insight was utilized by Knuth [8] to find a linear-time solution for the left-to-right pattern-matching problem, which can easily be expressed as a 2DPDA:

"This was the first time in Knuth's experience that automata theory had taught him how to solve a real programming problem better than he could solve it before." [8, p. 339]

Cook's original construction in 1971 is obscured by the fact that it does not follow the control flow of a pushdown automaton running on some input. It traces all possible flows backward thereby examining many unreachable computation paths, which makes the construction hard to follow. Jones clarified the essence of the construction using a semantics-based simulator that interprets the automaton in linear time while following the control flow forward thereby avoiding unreachable branches [6]. The simulator models the *symbol stack* of the automaton on its *call stack* using recursion in the meta-language and maintains a local list of surface configurations (dump list) to record their common terminator in a table when a pop-operation is simulated.

We follow Jones' semantics-based approach and give a simplified recursive simulator that does not require a local dump list and captures the essence of Cook's speedup theorem in a (hopefully) intuitive and easy to follow form. Furthermore, we then extend the construction from a simulation of deterministic automata to a simulation of two-way nondeterministic pushdown automata (2NPDA). The simulations are all realized by deterministic computation on a random-access machine.

Even though some time has passed since the theorem was originally stated, it continues to inspire studies in complexity theory and on the computational power of more practical programming paradigms,

A. Banerjee, O. Danvy, K.-G. Doh, J. Hatcliff (Eds): David A. Schmidt's 60th Birthday Festschrift EPTCS 129, 2013, pp. 250–258, doi:10.4204/EPTCS.129.15

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R. Glück 251

such as subclasses of imperative and functional languages (*e.g.* [2, 3, 7, 10]). It therefore appears worthwhile to capture the computational meaning of this classic result in clear and simple terms from a programming perspective. It is hoped that the progression from simple interpreters to simulators with memoization and termination detection makes these fundamental theoretical results more accessible.

We begin with a simple interpreter for two-way deterministic pushdown automata (Sect. 2) that we extend to simulate deterministic PDA in linear time (Sect. 3). We then introduce a nondeterministic choice operator (Sect. 4) and show the simulation of nondeterministic PDA (Sect. 5).

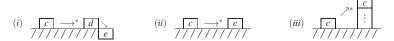
2 Deterministic PDA Interpreter

A two-way deterministic pushdown automaton (2DPDA) consists of a finite-state control attached to a stack and an input tape with one two-way read-only head [4]. The state p, the symbol read at head position i, and the symbol A on top of the stack determine the next action for a given tape, which is the automaton's input. Only when the stack top is popped does the symbol below the top become relevant for the following computation. The set of states, the set of input symbols and the set of stack symbols are fixed for an automaton. A transition function chooses the next action depending on the current surface configuration c = (p, i, A), shortly referred to as configuration. The instantaneous description (c, stack-rest) of an automaton includes the current configuration c and the stack below the top symbol A.

The automaton can **push** and **pop** stack symbols, and perform an operation **op** that modifies the current configuration without pushing or popping (e.g., move to a new tape position). The stack bottom and the left and right tape ends are marked by distinguished symbols. The head position i in a configuration (p,i,A) is always kept within the tape bounds and one can determine an empty stack. The automaton answers decision problems. It is said to accept an input if, when started in initial state p_0 with an empty stack and the head at the left endmarker, it terminates with **accept**, an empty stack and the head at the right endmarker. It can just **halt** with an empty stack without accepting an input. In the exposition below we tacitly assume some fixed input tape.

Termination. A configuration in which a pop-operation occurs is a *terminator* [4]. Every configuration c in a terminating computation has a unique terminator, that is the earliest terminator reached from c that returns the stack *below the height* at c. This case is illustrated below (i): d is the terminator of c. Terminator d can be viewed as the result of configuration c. Configuration c will always lead to d regardless of what is on the stack below. A configuration that accepts or halts the automaton is also a terminator.

If a configuration c is met again before the terminator is reached, which means that the stack never returned below the level at which c occurred for the first time, then the automaton is in an $infinite\ loop$. The second occurrence of c will eventually lead to a third occurrence of c, ad infinitum. The only two possible situations are illustrated below: either c repeats at the same level of the stack (ii) or at a higher level after some stack-operations have been performed (iii). In both cases, the contents of the stack below c (shaded) is untouched and irrelevant to the computation: c will always lead to an infinite loop.



Running Time. The *number of configurations* that an automaton can enter during a computation depends on the input tape. The states and symbols are fixed for an automaton. The number of head positions

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 3 / 9)

Simulation of Two-Way Pushdown Automata Revisited

on the input tape is bounded by the *length of the input tape*. The number of configurations is therefore linear in the length of the input tape, n = O(|tape|). We remark that the number of configurations of an automaton with k independent heads on the input tape is $n = O(|tape|^k)$. The k head positions are easily accommodated by configurations of the form $c = (p, i_1, \dots, i_k, A)$. An automaton can carry out an *exponential number of steps* before it terminates. For example, an automaton that during its computation

forms all stacks consisting of n zeros and ones takes $O(2^n)$ steps.

252

Interpreter. Figure 1 shows the interpreter for 2DPDA written in the style of an imperative language with recursion and call-by-value semantics. The interpreter Int can be run on a random-access machine (RAM). A call Int(c) = d computes the terminator d of a configuration c, where pop(d). There is no symbol stack and no loop in the interpreter. All operations are modeled on the call stack of the implementation language by recursive calls to the interpreter. A recursive call takes constant time, thus a call stack just adds a constant-time overhead compared to a data stack. Statements accept and halt stop the interpreter and report whether the input was accepted or not. The automaton is assumed to be correct and no special checks are performed by the interpreter. We will now discuss the interpreter in more detail. It is the basis for the three interpreters and simulators in the following sections.

In the interpreter we abstract from the concrete push-, op- and pop-operations. We define predicates push(c), op(c), pop(c), accept(c), halt(c) to be true if a configuration c causes the corresponding operation in the automaton. Their actual effect on a configuration is not relevant as long as the next configuration can be determined by the built-in operations next and follow. We let next(c) be the operation that yields in one step the next configuration, if op(c) or push(c), and follow(c,d) be the operation that yields in one step the next configuration given c and d, if pop(d).\text{} Each of these operations takes constant time, including next and follow that calculate the next configuration.

In case a configuration c causes a pop-operation, that is pop(c) is true in the cond-statement (Fig. 1), c is a terminator and the interpreter returns it as result. If a configuration c causes a push-operation, that is push(c) is true, first the terminator of the next configuration is calculated by lnt(next(c)) = d. The terminator always causes a pop-operation and interpretation continues at configuration follow(c,d) which follows from c and terminator d. In case op(c) is true, that is the operation neither pushes nor pops, the terminator of c is equal to the terminator lnt(next(c)) of the next configuration.

The effect of the operations on the configurations and the call stack can be summarized as follows.

$$c = \underbrace{ \begin{array}{c} (p,i,A) \\ \cdots \end{array} } \quad \begin{array}{c} push(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline (p,i,A) \\ \cdots \end{array} \\ = next(c)$$

$$c = \underbrace{ \begin{array}{c} (p,i,A) \\ \cdots \end{array} } \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} = next(c)$$

$$d = \underbrace{ \begin{array}{c} (q,j,B) \\ \cdots \end{array} } \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{c} (q,j,B) \\ \hline \end{array} \quad \begin{array}{c} op(c) \\ \hline \end{array} \quad \begin{array}{$$

 $^{^1}$ The conventional 'pop' just removes the top symbol from the stack. Our generalization that defines the next configuration by follow(c,d) does not affect the complexity arguments later and is convenient from a programming language perspective.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 4/9)

R. Glück 253

Figure 1: A recursive interpreter for deterministic PDA.

A push-operation may, for example, push a constant symbol B onto the stack or duplicate the current top A. Likewise, an op-operation may replace the current top A by a new top B, but without pushing or popping the stack, and move the tape head by changing position i into j. A pop-operation may just remove the stack top A to uncover B below or replace the uncovered symbol by a symbol C depending on A and B. The abstract pop-operation covers many common binary stack-operations familiar from stack programming languages (e.g., it may choose from symbols A and B the one that is smaller according to some order). Depending on the concrete set of binary operators and stack symbols this allows to express a number of interesting functions as pushdown automata.

Properties. The body of the interpreter contains no loop, only sequential statements. The time it takes to execute each of the statements is bounded by a constant (ignoring the time to evaluate a recursive call to a result). No side-effects are performed and no additional storage is used except for the local variable d. Even though written in an imperative style, the interpreter is purely functional. It terminates if and only if the pushdown automaton terminates on the same input. The correctness of the interpreter should be evident as it merely interprets the automaton one step at a time. Note the simplicity of the construction by recursively calling the interpreter for each action of the automaton. Also an op-operation that does not change the height of the symbol stack converts into a (tail-recursive) call on the call stack.

In a terminating computation, no call Int(c) can lead to a second call Int(c) as long as the first call has not returned a result, which means that it is still on the call stack. If a second call Int(c) occurs while the first one is still on the call stack, the interpreter is in an infinite recursion.

As a consequence, in a terminating computation the height of the call stack is bounded by n, the number of configurations, and the same call stack cannot repeat during the interpretation. After exhausting all possible call stacks of height up to n, that is all permutations of up to n configurations, the interpreter must terminate, that is within $O(n^n)$ steps. The interpreter can have a running time exponential in the number of configurations.

3 Linear-Time Simulation of Deterministic PDA

The 2DPDA-interpreter in Fig. 1 is purely functional and has no persistent storage. Each time the terminator d of a configuration c is computed and the same configuration is reached again, the terminator has to be recomputed by a call lnt(c), which means the entire subcomputation is repeated. To store known terminators and to share them across different procedure invocations, we extend the interpreter with *memoization* [9]. This straightforward extension gives linear-time simulation of 2DPDA. The sharing of terminators is the reason why Cook's speedup theorem works.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 5/9)

254

Simulation of Two-Way Pushdown Automata Revisited

```
procedure Sim(c: conf): conf;
  if defined(T[c]) then return T[c]; /* find shortcut */
  cond
                d := Sim(follow(c,Sim(next(c))));
     push(c):
                d := Sim(next(c));
     op(c):
     pop(c):
                d := c;
     halt(c):
                halt:
     accept(c): accept;
  end:
  T[c] := d;
                                      /* memoize result */
  return d
```

Figure 2: A linear-time simulator for deterministic PDA.

RAM extension. Figure 2 shows the interpreter with memoization, called *simulator*. It works in the same way as the interpreter except that each time before a call Sim(c) returns the terminator d of c, the terminator is stored in a table T by assignment T[c] := d. Next time the terminator is needed, it can be retrieved from T, avoiding its recomputation. Terminators are now shared dynamically at run time and over the entire simulation. Table T can be implemented as a one-dimensional array indexed by configurations and can hold one terminator for each of the n configurations that can occur during a computation. All table entries are initially undefined. It is easy to see that the shortcut (if-statement) and the memoization (table assignment) do not change the result of the automaton. Storing and retrieving a terminator takes constant time on a RAM (see Cook for a charged RAM model instead of a unit-cost model [4]). An "automatic storage management" also means that many terminators are recorded during a computation that are not needed later, but we shall see that this does not affect the linearity argument. A more thorough analysis would surely reveal that memoization points are only required at a few places in an automaton (cf [3, 10]).

Linear-time simulation. In a terminating computation, before a second call Sim(c) is made, the first call must have returned and stored the terminator d of c at T[c]. Once the terminator is known, it need not be recomputed and can be fetched from the table. Hence, the cond-statement, which is guarded by a lookup in T, is executed *at most once* for any c. Recursive calls to the simulator occur only from within the cond-statement, namely one call if op(c) and two calls if push(c). Consequently, Sim can be called at most 2n times, where n is the number of possible configurations. This also limits how often the if-statement guarding the cond-statement is executed. Hence, the total number of execution steps during a terminating simulation is bounded linearly by n. Recall that n is linear in the length of the input tape, n = O(|tape|). This concludes the argument for the linear-time simulation of a 2DPDA on a RAM.

Discussion. Deterministic pushdown automata are the accepting device for *deterministic context-free languages*. More precisely, they are exactly recognized by *I-way* deterministic pushdown automata (IDPDA), that is, deterministic pushdown automata that never move their head to the left on the input. The LR grammar of a deterministic context-free language is easy to convert into a IDPDA (*e.g.* [5]). Thus, recognition of this subclass of context-free languages using the memoizing simulator Sim (Fig. 2) takes at most linear time (as does the classic LR-parsing algorithm by Knuth). In the following we extend the simulator to recognize all context-free languages in cubic time.

The method by Aho et al. [1] requires $O(n^2)$ for simulating 2DPDA, a result which was then strength-

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R. Glück 255

```
procedure Int(c: conf): confset;
   if visited(T[c]) then return { };
                                                    /* detect infinite branch */
   T[c] := Visited;
                                                    /* mark configuration */
   cond
                      d := \bigcup Int(follow(c,e)) where e \in Int(next(c));
      push(c):
      op(c):
                      d := Int(next(c));
      \mathsf{choose}(\mathsf{c}) \colon \mathsf{d} := \mathsf{Int}(\mathsf{nextleft}(\mathsf{c})) \cup \mathsf{Int}(\mathsf{nextright}(\mathsf{c}));
                      d := \{c\};
      pop(c):
      halt(c):
                      d := \{\};
      accept(c): accept;
   end;
   \mathsf{T}[\mathsf{c}] := \mathsf{Undef};
                                                    /* unmark configuration */
   return d
```

Figure 3: A recursive interpreter for nondeterministic PDA.

ened to O(n) by Cook [4]. Both methods work bottom-up. In contrast, the simulator Sim works top-down following the forward control flow as does the one by Jones [6]. It clearly shows that the key mechanism that turns a recursive pushdown interpreter into a linear-time simulator is memoization.

4 Interpretation of Nondeterministic PDA

In a two-way *nondeterministic* pushdown automaton (2NPDA) the computation path is not uniquely determined. A deterministic automaton can be made *nondeterministic* by introducing an operation **choose** that allows the automaton to select any of two computation paths in a configuration c(cf. [7]). This means that a configuration no longer has a unique terminator, but a set of possible terminators. We let nextleft(c) and nextright(c) be the abstract operations that yield in one step the two next configurations that are possible if choose(c). For simplicity, the new operation can neither push nor pop stack symbols. With a choose-operation two transitions are possible:

$$c = \underbrace{ \begin{pmatrix} (p,i,A) \\ \dots \end{pmatrix}}_{c} = \underbrace{ \begin{pmatrix} (p,i,A)$$

A nondeterministic automaton is said to *accept* an input if it has at least one accepting computation when started in the initial state p_0 with an empty stack and the head at the left tape end. It has the ability to guess the right choice that leads to the shortest accepting computation. In an interpreter this "angelic nondeterminism" can be thought of as searching through a tree of all possible computation sequences, some of which may be infinite or non-accepting, to find at least one accepting sequence. Branching in the computation tree is due to nondeterministic choose-operations in the automaton.

Interpreter. The interpreter for *nondeterministic* PDA that can be run on a RAM is shown in Fig. 3. Two main changes to the original interpreter in Fig. 1 are necessary to accommodate the "guessing": (1) a *set of terminators* instead of a single terminator is returned, and (2) a *termination check* ("seen before") that stops interpretation along an infinite computation sequence. We detail the two modifications below.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 7/9)

Simulation of Two-Wav Pushdown Automata Revisited

- 256
 - 1. Terminator sets: A choose-operation requires the union of the terminator sets obtained from the two next configurations, nextleft(c) and nextright(c). In case of a push-operation, and this is the most involved modification, each configuration e in the terminator set obtained by the inner call Int(next(c)) must be followed by an outer call. The big set union used for this purpose is a shorthand for a while-loop over the inner terminator set. A pop-operation now returns a singleton set $\{c\}$ instead of c. Finally, instead of making a full stop at a halt-operation, an empty set is returned in order not to miss an accepting computation along another possible branch.
 - 2. Termination check: As discussed before, non-termination occurs when the interpreter is called a second time with the same configuration c as argument while the first call has not yet returned. This situation can be detected by marking c in a table when a call Int(c) is made and unmarking c when the call returns. If a call with a marked c as argument is made, an infinite computation is detected and the interpreter returns an empty terminator set. The same table T as before can be used, but can now hold the additional value Visited. Initially all table entries are set to Undef.

The cardinality of a terminator set is bounded by n, the number of configurations that can occur in a computation. The most costly set operation in the interpreter is the union of terminator sets. Assuming a suitable choice of data structures, a union operation takes time linear in the total cardinality of the sets, that is the union of two sets with cardinalities u and v takes time O(u+v). All remaining set-operations needed in the interpreter are straightforward and take constant time: creating a set (empty, singleton), and picking and removing an arbitrary element from a set (in the set comprehension). In the discussion below we assume such an implementation of the set operations.²

A choose-operation, which unites two terminator sets each of cardinality up to n, takes linear time O(n). A push-operation, where the inner call $\mathsf{Int}(\mathsf{next}(\mathsf{c}))$ returns a set of at most n terminators, each of which, when followed by the outer call $\mathsf{Int}(\mathsf{follow}(\mathsf{c},\mathsf{e}))$, can again return a set of up to n terminators, requires the union of n sets each of cardinality up to n, which then takes quadratic time $O(n^2)$. This is the most expensive set-operation in the cond-statement.

In the case of a deterministic automaton, that is, an automaton *without* choose-operation, the new interpreter in Fig. 3 operates with singleton sets only, and the set-operations introduce at most a constant-time overhead compared to the original interpreter in Fig. 1. This is useful because the new interpreter "falls back" to its original behavior and, except for a constant time overhead in the new interpreter, there is no penalty in using it to run deterministic PDA and, as an extra benefit, it always terminates.

There is a major pitfall. If a nondeterministic automaton is left-recursive, then the termination check may stop left-recursion too early and miss useful branches contributing to a terminator set. In the case of 1NPDA there always exists a non-left-recursive version (presumably the same for 2NPDA). Alternatively, one might bound the unfolding of a left-recursion in terms of the input assuming some normal-form automaton (the termination check in Fig. 3 limits left-recursion unfolding to one).

5 Cubic-Time Simulation of Nondeterministic PDA

To turn the new interpreter (Fig. 3) into a fast simulator (Fig. 4) we use the same memoization method as in Sect. 3. The use of table T parallels its use in the deterministic case except that for each of the n possible configurations the table can now hold a set of up to n terminators and the value Visited. The body of the simulator is again guarded by an if-statement (first line) that returns the terminator set of a

²A straightforward implementation of such a set data structure might be a Boolean array of length *n* to indicate membership of a configuration c in a set together with an unsorted list of all configurations contained in that set.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 8 / 9)

R. Glück 257

```
procedure Sim(c: conf): confset;
   if defined(T[c]) then return T[c]; /* find shortcut */
  if visited(T[c]) then return \{\};
                                          /* detect infinite branch */
                                          /* mark configuration */
   T[c] := Visited;
   cond
     push(c):
                  d := \bigcup Sim(follow(c,e)) where e \in Sim(next(c));
     op(c):
                  d := Sim(next(c));
     choose(c): d := Sim(nextleft(c)) \cup Sim(nextright(c));
     pop(c):
                  d := \{c\};
     halt(c):
                  d := \{\};
     \mathsf{accept}(\mathsf{c}) \colon \  \, \mathsf{accept};
   end:
   T[c] := d;
                                          /* memoize result */
  return d
```

Figure 4: A cubic-time simulator for nondeterministic PDA.

configuration c, if it is available in table T. Otherwise, and if no infinite computation path is detected, c is marked as Visited in T and its terminator set is computed.

Before returning, terminator set d of c is stored in T, which overwrites the mark Visited. The condstatement is executed at most once for each configuration. The mark Visited is only needed the first time the procedure is called, when the table does not yet contain a terminator set for c. Thus, the same table can be used for marking configurations and for storing their terminator sets. A terminator set may be empty if none of the branches rooted in c is accepting. Otherwise, the interpreter is unchanged.

Cubic-time simulation. As before, the cond-statement is executed at most once for each of the n configuration due to the guards at the beginning of Sim. Up to n+1 calls to Sim may occur in the case of a push-operation, namely one inner call and at most n outer calls, one for each n0 n1. Hence, Sim can be called at most n2 n3 simulation. This also limits how often the if-statements guarding the cond-statement are executed.

In the cond-statement, as before, the simulation of the op-, pop-, halt-, accept-operations takes constant time, O(1). The union of two sets of at most n terminators in case of a choose-operation may take linear time, O(n). The union of the terminator sets in a push-operation is the most costly operation and may take quadratic time, $O(n^2)$. A push is simulated at most once per execution of a cond-statement, which is at most n times. Hence, the total number of execution steps during a simulation is cubic in the number of configurations, $O(n^3)$. Recall that n is linear in the length of the input tape, n = O(|tape|). This ends the argument for the cubic-time simulation of (non-left-recursive) 2NPDA on a RAM.

Discussion. We observe that the "complexity generator" in the cond-statement is *not* the choose-operation, even though it introduces two computation branches, rather the handling of up to n continuations and the union of their terminator sets in case of a push-operation. If the cardinality of each terminator set that can occur during a simulation is bounded by a *constant*, that is, not dependent on the input, the simulation time is *linear* in the input as before. Deterministic automata, where the cardinality of each terminator set is at most one, and a class of nondeterministic automata, where the cardinality is bounded by some k, are all simulated in linear time by Sim. The top-down method is useful because the same simulator runs them in the time corresponding to their degree of nondeterminism.

A paper proving concisely a result in automata theory that helped solve a real programming problem Glück, 2013 (p. 9 / 9)

Simulation of Two-Way Pushdown Automata Revisited

One-way nondeterministic pushdown automata (1NPDA) are the accepting device for context-free languages. Every context-free language has a grammar without left recursion and it is straightforward to convert the grammar into a 1NPDA. This means that recognition of context-free languages using the simulator (Fig. 4) has the same worst-case time complexity as the classic parsing algorithms that can handle the full class of context-free languages (Earley, Cocke-Younger-Kasami), that is $O(|string|^3)$. As discussed before, the performance of the simulator is determined by the degree of nondeterminism in the automaton. Recognition of deterministic context-free languages using the simulator takes, again, at most linear time. In practice, of course, specialized parsing algorithms will have better run times (due to

the constant term hidden in the *O*-notation) and use less space than the recursive simulator. Again, the mechanism that enables polynomial-time simulation is the sharing of computations by memoization.

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258

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4.2. Inserting Figures

Markdown only You can easily insert images and figures using Pandoc, as in Figure 1, a painting by Jérôme Minard under copyleft.



Figure 1.: D'un autre âge

4.3. Inserting Tables

Markdown only You can write tables using pandoc's syntax*es*, as in Tables 1, 2 and 3 (all borrowed from https://www.flutterbys.com.au/stats/tut/tut17.3.html).

Table 1.: The price of categories

Column A	Column B	Column C
Category 1	High 95.00	100.00
High		
Category 2	High 82.50	80.50
High		

Table 2.: Illustrating how to align entries in a table

Default	Left	Center	Right
High	Cat 1	A	100.00
High	Cat 2	В	85.50
Low	Cat 3	C	80.00

Table 3.: The price and advantages of fruits

Fruit	Price	Advantages
Bananas	\$1.34	• built-in wrapper
0	¢2.10	• bright color
Oranges	\$2.10	cures scurvytasty

4.4. Inserting Code Listings

Code is displayed using the listings package. Check the "Table 1: Predefined languages" of the listings package documentation to see the list of supported languages by default. Markdown only Your can display code using various possible syntaxes.

As a fenced block:

```
public class HelloWorld {
   public static void main(String[] args) {
       System.out.println("Hello, World");
}
```

In a figure, as in Listings 1, 2 or 3 (that uses respectively the backtick, the tildes, and listingutlisting to display the code – this latter option allows to load a file directly).

```
1 (** Courtesy of https://coq.inria.fr/a-short-introduction-to-coq. **)
2 Inductive even: N \to Prop :=
3 | even_0 : even 0
4 | even_S n : odd n \to even (n + 1)
5 with odd: N \to Prop :=
6 | odd_S n : even n \to odd (n + 1).
```

Listing 1: An inductive definition in Coq

```
1 # Courtesy of https://stackoverflow.com/a/2188369
2 for num in {000..2}; do echo "$num"; done
```

Listing 2: How to use braces ({and }) in bash

```
1 /* Courtesy of Brian Kernighan and https://en.wikipedia.org/wiki/%22Hello, 
_World!%22_program#C */
2 #include <stdio.h>
3 int main(void)
4 {
5 printf("Hello, world\n");
6 return 0;
7 }
```

Listing 3: "Hello World" in C

5. Landscape Pages

You can obtain landscape pages using the landscape package in LATEX.

Markdown only This feature is not accessible in pure markdown: if you want to have landscape pages, you need to use LaTeX commands in your document.

Note that the drawing presented in Figure 2 was obtained using LATEX's package tiKz, and that the source code is shared in the pictures folder.

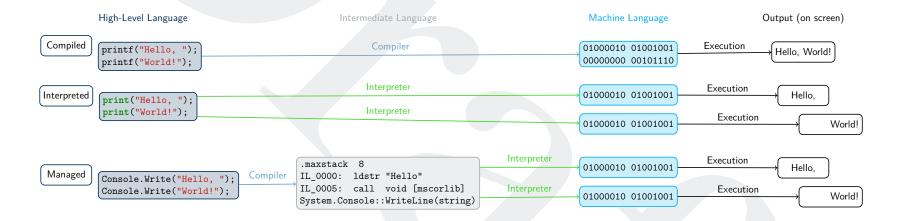


Figure 2.: Difference between programming languages (simplified)

6. Margins and Fonts

6.1. Margins

The margin have been set to fit the graduate school's requirements to:

Actual page layout values.

\paperheight = 11.00215in	\paperwidth = 8.50166in
\hoffset = 0in	\voffset = 0in
$\ensuremath{\verb{vensidemargin}} = 0.50009 in$	\oddsidemargin = 0.50009in
\topmargin = 0in	\headheight = 0in
\headsep = 0in	textheight = 9.00177 in
textwidth = 6.00117 in	footskip = 0.70236in
\marginparsep = 0in	$\mbox{\em margin} \mbox{\em parpush} = 0 \mbox{\em in}$
\columnsep = 0in	\columnseprule = 0in
1em = 0.16608in	1ex = 0.07472in

Please, do not change those values.

6.2. Fonts

6.2.1. Body

The font used in the body of the document is "TeX Gyre Termes Font Family", which is an extension of the standard Times New Roman that is free for commercial use, and can be freely distributed. It is set to 12pt in all of the document, and adjusted when needed to the appropriate size (particularly in the cover page, where most attributes need to be set at 16pts).

The "usual" correspondence between points and LATEX commands is as follows:

tiny is equivalent to 6pt

scriptsize is equivalent to 8pt

footnotesize is equivalent to 10pt

small is equivalent to 10.95pt

normalsize is equivalent to 12pt

large is equivalent to 14.4pt

Large is equivalent to 17.28pt

LARGE is equivalent to 20.74pt

huge is equivalent to 24.88pt

Huge is equivalent to 24.88pt

6.2.2. Symbols

For better unicode support, the Symbola font is also used. Starting with version 11, the licence of this font is too restrictive for non-personal use. As a consequence, users are asked to make sure they do not use a version greater than v.10.24, which is "free for any use" and archived on-line.

By default, the following symbols, not available in the TeX Gyre Termes Font Family, are displayed using Symbola: $^{\bigcirc}$, \times , $^{\wedge}$, $^{\bigcirc}$, $^{\bigcirc}$, $^{\bigcirc}$, $^{\vee}$

1 \newunicodechar{<unicode symbol>}{\symb <unicode symbol>}

(in head_c.tex in the markdown version), so that <unicode symbol> will be rendered using the Symbola font.

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A. Appendix A (Optional)

Insert here protocols, figures not included, larger listings, etc.