## **Shared Memory with More than 1 Block**

Imagine we launch four blocks in a kernel, each block with 8 threads, as shown below.

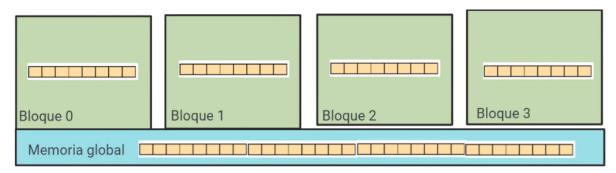


Figure 1: img

Thus, we will manipulate a vector of size 32 (4x8), the same size as the total threads launched.

In Global Memory, we have the vector thanks to cudaMalloc(). Global Memory is a memory that all threads from all blocks can see.

Last time, in the kernel we typed \_\_shared\_\_ int vector[32], so that in a block there was a 32 vector in shared memory. But now, when launch many blocks the shared memory vector \_\_shared\_\_ int vector[32] will not be reserved 4 times, but the vector size will be split among all the blocks launched: each block has now a vector of size 8.

But now we need to copy from the Global Memory vector of size 32 into the shared vector size 8 in each block: the first block copies the first 8 values, and so on. The gId is computed:

```
int gId = threadIdx.x + blockDim.x * blockIdx.x;
```

So that it looks as follows:

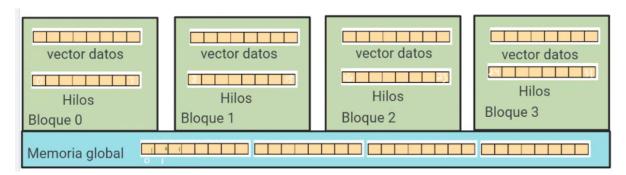


Figure 2: img

The block 1 cannot write anything on the shared vector of another block. Then, how to manage the shared vector indices in order to copy the global vector to the shared one?

The shared vector can be accessed with the **same** indices as if it was on global memory, because the reservation is global from the kernel, and thus the indices have continuity among blocks.

```
1 __global__ void kernel(int *v){
2    int gId = threadIdx.x + blockDim.x * blockIdx.x;
3    __shared__ int vector[32];
4    vector[gId] = v[gId];
5 }
```

Now, instead of partitioning the full 32 size vector, now we split into half the shared vector of size 8 that is in each block, and do the same thing over and over to perform a parallel reduction.

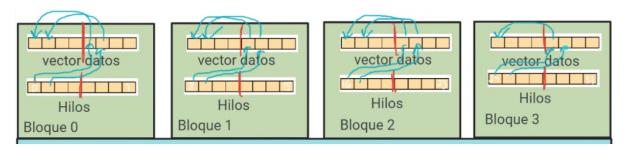


Figure 3: img

But what will be our step variable? Instead of 32/2, we need 8/2 initially. Consider that we have the following defined macros (that are known to the kernel):

```
1 #define numThreadsPerBlock 8
2 #define numBlocks 4
3 #define vecSize numThreadsPerBlock*numBlocks
```

Thus,

```
1 __global__ void kernel(int *v){
2    int gId = threadIdx.x + blockDim.x * blockIdx.x;
3    __shared__ int vector[32];
4    vector[gId] = v[gId];
5    int step = numThreadsPerBlock / 2;
6 }
```

and now, how to choose the first 4 threads of each block, in order to begin the parallel reduction step?

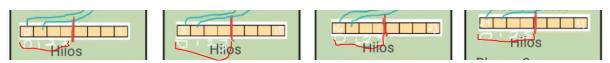


Figure 4: img

```
__global__ void kernel(int *v){
2
      int gId = threadIdx.x + blockDim.x * blockIdx.x;
       __shared__ int vector[32];
4
      vector[gId] = v[gId];
      int step = numThreadsPerBlock / 2;
6
      if (threadIdx.x < step){ // the first 4 threads per block:</pre>
           // use gId to move from block to block spaces in the
              shared vector (always the first 4)
8
           // then add step to take the next half value and sum
              it up
9
           vector[gId] += vector[gId + step];
      }
  }
```

But where is our final result? consider that the parallel reduction now is done per block:

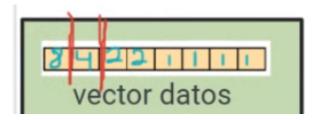


Figure 5: img

In the end, because the block is accessing only to its shared vector piece, thus the partial sum is in the first element of each block's vector of size 8, but we cannot access them accross blocks. This is where the **global memory comes back**: we copy the number 8 in each block shared vector's first cell to the global memory vector, using the global id of such thread so that they write it in the global memory vector as:

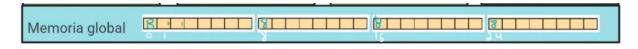


Figure 6: img

Now back in the GM vector, we need to apply again a parallel reduction again, but to do that we need to copy all the 8's to the first 4 cells of the GM vector:



Figure 7: img

We will choose these 0 1 2 3 threads by choosing the threads with the gIds that are smaller than the number of blocks:

```
__global__ void kernel(int *v){
2
       int gId = threadIdx.x + blockDim.x * blockIdx.x;
       __shared__ int vector[32];
       vector[gId] = v[gId];
4
       int step = numThreadsPerBlock / 2;
5
6
       if (threadIdx.x < step){ // the first 4 threads per block:
           0 1 2 3
           vector[gId] += vector[gId + step];
8
       }
9
       if (threadIdx.x == 0){ // copy the partial results to the
          global mem vector
           v[gId] = vector[gId];
       if (gId < numBlocks) { // choose the first 4 threads to
12
          copy in the first 4 cells the partial sums
           v[gId] = v[gId*numThreadsPerBlock]; // 0 <- 0*8, 1 <-
              1*8 ...
14
       }
15 }
```

Now that we have the partial sums in the beginning cells of the GM vector, we need to add all these using parallel reduction. The new step now is:

```
int step = numBlocks / 2;
```

```
8
           vector[gId] += vector[gId + step];
9
           // synch
       }
11
       if (threadIdx.x == 0){ // copy the partial results to the}
          global mem vector
12
           v[gId] = vector[gId];
           // synch
14
       }
       if (gId < numBlocks) { // choose the first 4 threads to
          copy in the first 4 cells the partial sums
           v[gId] = v[gId*numThreadsPerBlock]; // 0 <- 0*8, 1 <-
               1*8 ...
           // apply parallel reduction over v
           step = numBlocks / 2;
18
19
           if (gId < step){
               v[gId] += v[gId + step];
21
           }
       }
23
       if (gId == 0){
24
           *sum = v[0];
       }
26 }
```

In the end now we have the total result of the sum in the first cell of the GM vector. Remember to synchronize threads after each shared memory vector usage.

Note: requisite for parallel reduction is that the vector size is an even number (divisible by 2)

## **Implementation**

```
#include "cuda_runtime.h"
2
  #include "device_launch_parameters.h"
3
4 #include <stdio.h>
5 #include <stdlib.h>
6 #include <iostream>
7
8 #define numBlocks 8
9 #define threadsPerBlock 1024
11 using namespace std;
12
  __host__ void checkCUDAError(const char* msg) {
14
       cudaError_t error;
       cudaDeviceSynchronize();
       error = cudaGetLastError();
17
       if (error != cudaSuccess) {
18
           printf("ERROR %d: %s (%s)\n", error,
              cudaGetErrorString(error), msg);
```

```
19 }
20 }
21
22 __host__ void validate(int* result_CPU, int* result_GPU) {
23
       if (*result_CPU != *result_GPU) {
24
           printf("[FAILED] Kernel validation.\n");
25
            return;
26
       }
       printf("[SUCCESS] Kernel validation.\n");
28
       return;
29 }
31 __host__ void CPU_reduction(int* v, int* sum) {
32
       for (int i = 0; i < numBlocks * threadsPerBlock; i++) {</pre>
            *sum += v[i];
34
       }
35 }
36
37
   __global__ void GPU_reduction(int* v, int* sum) {
38
        __shared__ int vector[numBlocks * threadsPerBlock];
        int gId = threadIdx.x + blockDim.x * blockIdx.x;
41
       vector[gId] = v[gId];
42
        __syncthreads();
43
       int step = threadsPerBlock / 2;
44
        while (step) {
45
            if (threadIdx.x < step) {</pre>
46
                vector[gId] = vector[gId] + vector[gId + step];
                __syncthreads();
47
48
            }
49
            step = step / 2;
       }
        __syncthreads();
52
       if (threadIdx.x == 0) { // copy the partial results to the
            global mem vector
            //printf("SM->vector[%d]: %d\n", gId, vector[gId]);
54
            v[gId] = vector[gId];
            __syncthreads();
56
            //printf("GM->v[%d]: %d\n", gId, v[gId]);
        if (gId < numBlocks) { // choose the first 4 threads to
58
           copy in the first 4 cells the partial sums
59
            v[gId] = v[gId * threadsPerBlock]; // 0 <- 0*8, 1 <-
               1*8 ...
            __syncthreads();
            //printf("%d<-%d\n", v[gId], v[gId * threadsPerBlock])</pre>
61
       }
62
63
       int new_step = numBlocks / 2;
       while (new_step) {
64
65
           if (gId < new_step) {</pre>
```

```
66
                 v[gId] += v[gId + new_step];
67
            }
68
            new_step = new_step / 2;
69
        }
        __syncthreads();
        if (gId == 0) {
72
            *sum = v[gId];
        }
    }
74
76 int main() {
78
        int* dev_a, * dev_sum;
79
        int host_sum = 0, CPU_sum = 0;
80
        int * host_a = (int *) malloc(size of (int) * numBlocks *
           threadsPerBlock);
        cudaMalloc((void**)&dev_a, sizeof(int) * numBlocks *
           threadsPerBlock);
82
        cudaMalloc((void**)&dev_sum, sizeof(int));
83
84
        for (int i = 0; i < numBlocks * threadsPerBlock; i++) {</pre>
85
            host_a[i] = 1;
86
        }
87
88
        cudaMemcpy(dev_a, host_a, sizeof(int) * numBlocks *
           threadsPerBlock, cudaMemcpyHostToDevice);
89
90
        dim3 grid(numBlocks, 1, 1);
        dim3 block(threadsPerBlock, 1, 1);
92
        GPU_reduction << < grid, block >> > (dev_a, dev_sum);
        cudaDeviceSynchronize();
94
        checkCUDAError("Error at kernel");
96
        printf("N: %d\n", numBlocks * threadsPerBlock);
        cudaMemcpy(&host_sum, dev_sum, sizeof(int),
           cudaMemcpyDeviceToHost);
98
        printf("GPU result: %d\n", host_sum);
99
100
        CPU_reduction(host_a, &CPU_sum);
        printf("CPU result: %d\n", CPU_sum);
102
        validate(&CPU_sum, &host_sum);
104
        return 0;
106 }
```

## **Output**

```
N: 8192
GPU result: 8192
(FUP result: 8192
[SUCCESS] Kernel validation.

C:\Users\Administrator\Desktop\lab01\x64\Debug\lab01.exe (process 13516) exited with code 0.
To automatically close the console when debugging stops, enable Tools->Options->Debugging->Automatically close the console when debugging stops.

Press any key to close this window . . .
```

Figure 8: img