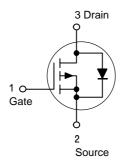


Power MOSFET 130 mAmps, 50 Volts

P-Channel SOT-23

These miniature surface mount MOSFETs reduce power loss conserve energy, making this device ideal for use in small power management circuitry. Typical applications are dc-dc converters, load switching, power management in portable and battery-powered products such as computers, printers, cellular and cordless telephones.

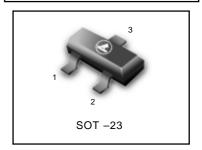
- Energy Efficient
- Miniature SOT-23 Surface Mount Package Saves Board Space
- Pb-Free Package is available.
- We declare that the material of product are Halogen Free and compliance with RoHS requirements.
- S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.



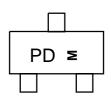
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	50	Vdc
Gate-to-Source Voltage - Continuous	V_{GS}	± 20	Vdc
Drain Current - Continuous @ $T_A = 25^{\circ}C$ - Pulsed Drain Current ($t_p \le 10 \mu s$)	I _D I _{DM}	130 520	mA
Total Power Dissipation @ T _A = 25°C	P_{D}	225	mW
Operating and Storage Temperature Range	T _J , T _{stg}	– 55 to 150	°C
Thermal Resistance – Junction–to–Ambient	$R_{\theta JA}$	556	°C/W
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	T _L	260	°C

LBSS84LT1G S-LBSS84LT1G



Marking Diagram



PD = Device Code M = Month Code

ORDERING INFORMATION

Device	Package	Shipping
LBSS84LT1G S-LBSS84LT1G	SOT-23	3000/Tape&Reel
LBSS84LT3G S-LBSS84LT3G	SOT-23	10000/Tape&Reel



LBSS84LT1G,S-LBSS84LT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Char	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		1		•	•	•
Drain–to–Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 250 μAdc)		V _{(BR)DSS}	50	_	-	Vdc
Zero Gate Voltage Drain Current (V _{DS} = 25 Vdc, V _{GS} = 0 Vdc) (V _{DS} = 50 Vdc, V _{GS} = 0 Vdc) (V _{DS} = 50 Vdc, V _{GS} = 0 Vdc, T _J :	IDSS		- - -	0.1 15 60	μAdc	
Gate-Body Leakage Current (VGS =	IGSS	-	_	±10	nAdc	
ON CHARACTERISTICS (Note 1.)						
Gate–Source Threaded Voltage ($V_{DS} = V_{GS}$, $I_D = 250 \mu Adc$)		VGS(th)	0.8	_	2.0	Vdc
Static Drain-to-Source On-Resistar (V _{GS} = 5.0 Vdc, I _D = 100 mAdc)	rDS(on)	-	5.0	10	Ohms	
Transfer Admittance (V _{DS} = 25 Vdc, I _D = 100 mAdc, f	lyfsl	50	_	-	mS	
DYNAMIC CHARACTERISTICS						
Input Capacitance	(V _{DS} = 5.0 Vdc)	C _{iss}	-	30	-	pF
Output Capacitance	(V _{DS} = 5.0 Vdc)	C _{oss}	_	10	-	
Transfer Capacitance	(V _{DG} = 5.0 Vdc)	C _{rss}	1	5.0	-	1
SWITCHING CHARACTERISTICS (N	ote 2.)					
Turn-On Delay Time		^t d(on)	-	2.5	-	ns
Rise Time	$(V_{DD} = -15 \text{ Vdc}, I_{D} = -2.5 \text{ Adc},$	t _r	-	1.0	-	
Turn-Off Delay Time	$R_L = 50 \Omega$)	t _d (off)	ı	16	-	1
Fall Time		tf	1	8.0	_	
Gate Charge		Q _T	-	6000	_	pC
SOURCE-DRAIN DIODE CHARACTI	RISTICS	·				•
Continuous Current		IS	-	_	0.130	Α
Pulsed Current		ISM	-	-	0.520	
Forward Voltage (Note 2.)		V _{SD}	-	2.5	-	V

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

TYPICAL ELECTRICAL CHARACTERISTICS

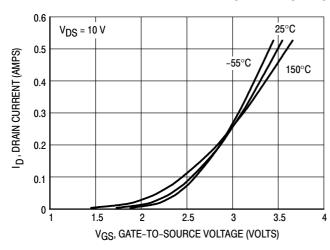


Figure 1. Transfer Characteristics

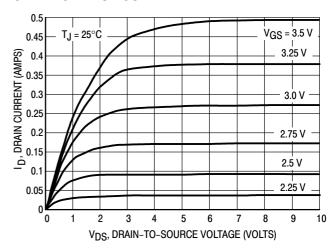


Figure 2. On-Region Characteristics

^{2.} Switching characteristics are independent of operating junction temperature.



LBSS84LT1G,S-LBSS84LT1G

TYPICAL ELECTRICAL CHARACTERISTICS

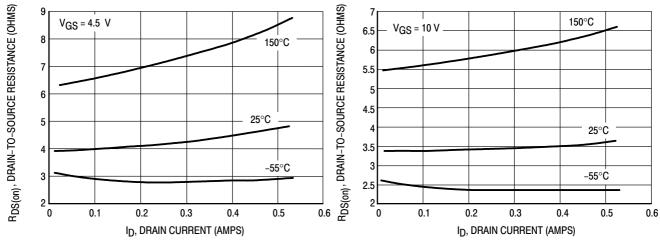


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current

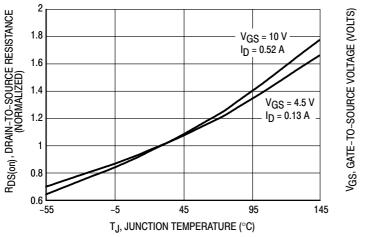


Figure 5. On-Resistance Variation with Temperature

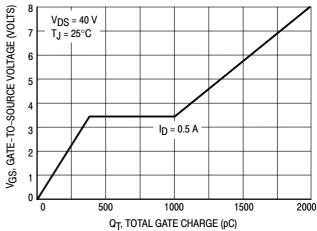


Figure 6. Gate Charge

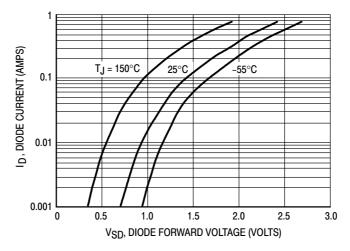
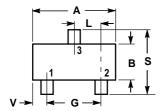


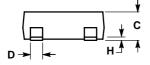
Figure 7. Body Diode Forward Voltage



LBSS84LT1G,S-LBSS84LT1G

SOT-23







NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,1982
- 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
Α	0.1102	0.1197	2.80	3.04
В	0.0472	0.0551	1.20	1.40
С	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
Н	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

PIN 1. BASE 2. EMITTER 3. COLLECTOR

