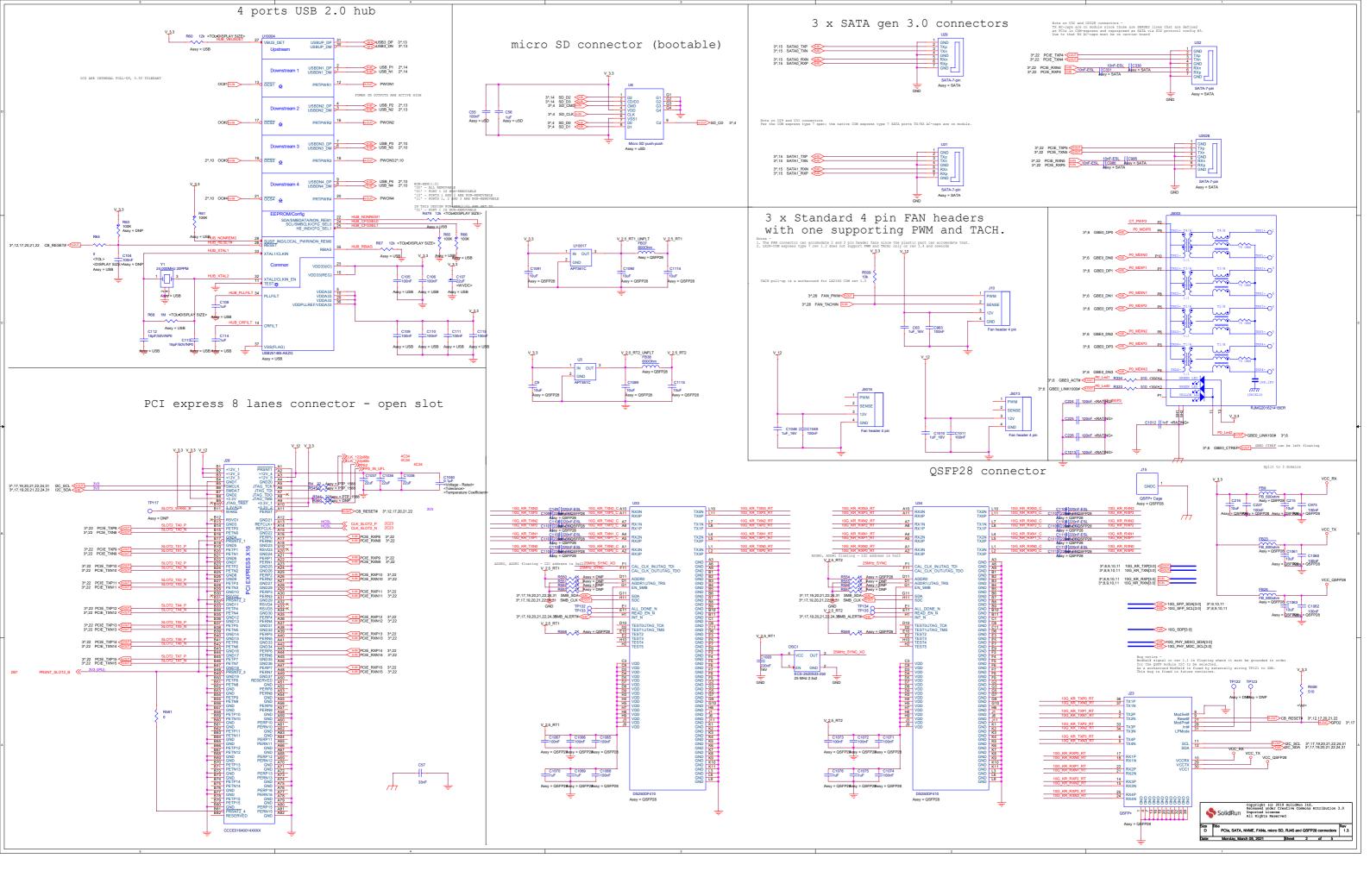
SMB 12C devices -0x22 - COM 4x25dbps TX retimer 0x23 - COM 4x25dbps RX retime STM32 MCU (multi master) 0x30 - IDT CLK EEFROM (not assembled) 0x4C - OCMO DAC (not assembled) 0x4A - LX2-COM Temperature sensor 0x4C - LX2-COM Unassembled temp sensor 0x7F - LX2-COM I2C switch R541 Assy = PTP 1588 PPS COM express type 7 AB connector I1588\_PULSE\_OUT1\_COM COM express type 7 AB connector 11588 PULSE OUT2 COM 10G\_SDP1 USB\_SSTX\_N0 3^,13 USB\_SSTX\_P0 3^,13 GND (FIXED)
GND (FIXED)
GND (FIXED)
GND (FIXED)
TO USB\_SSTX\_N1 3^,13 USB\_SSTX\_P1 3^,13 The below RC circuit is single ended filter for the MDC clock coming from the COM module. Since COM express FCB rev 1.4 this is not required anymous USB\_SSTX\_N2 3^,13 USB\_SSTX\_P2 3^,13 mi3/\48cnUSB\_SSRX\_N2M and N3/13 USB SSRX P2 I1588\_TRIG\_IN2\_COM 10G\_SDP3 HB USB\_SSTX\_N3 3^,13 USB\_SSTX\_P3 3^,13 3^,13 USB\_SSRX\_N3 HBI 3^,13 USB\_SSRX\_P3 HBI SMB CLK 9:17,19.2021 22.24.31

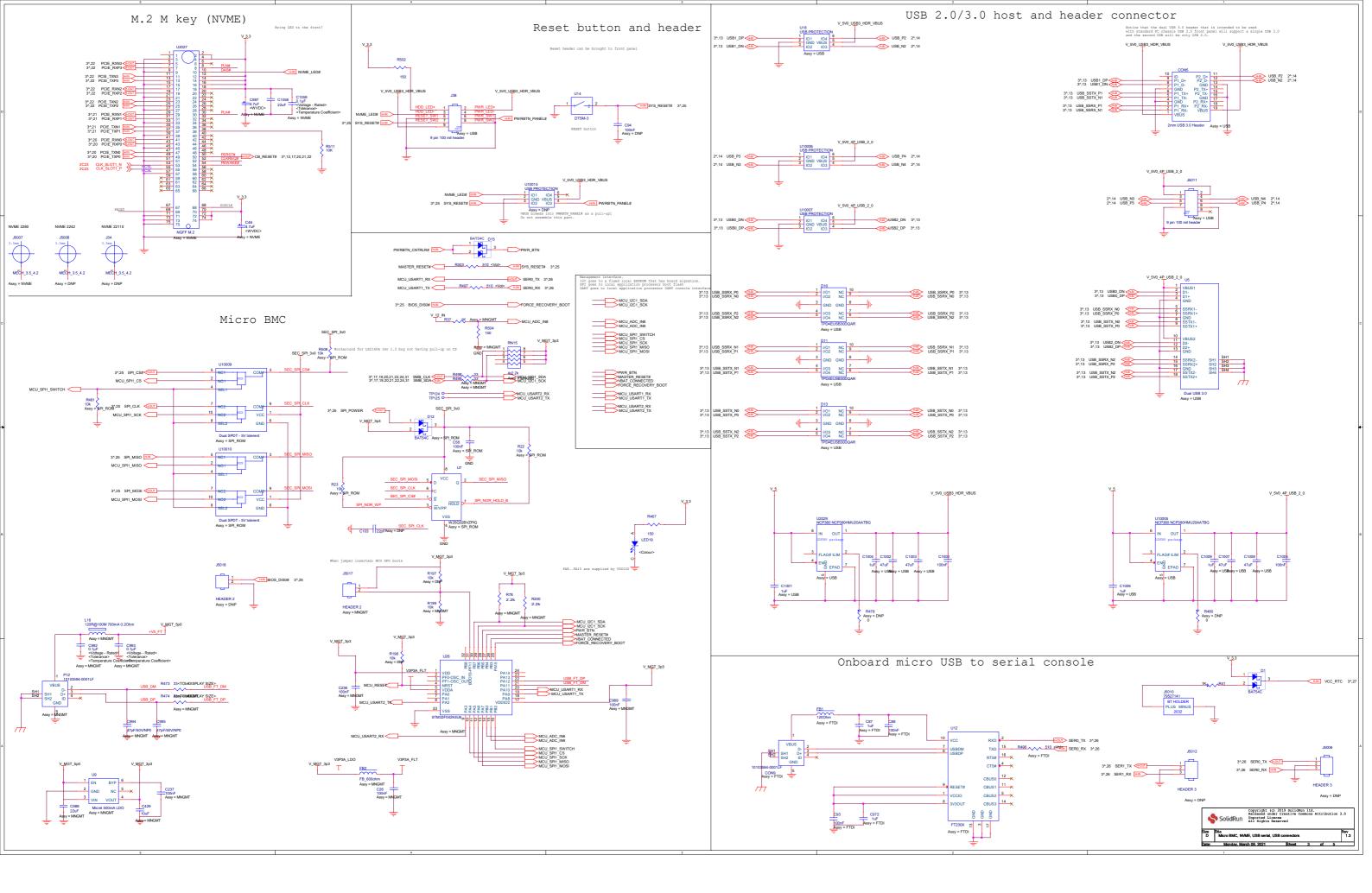
SMB SDA 9:17,19.2021 22.24.31

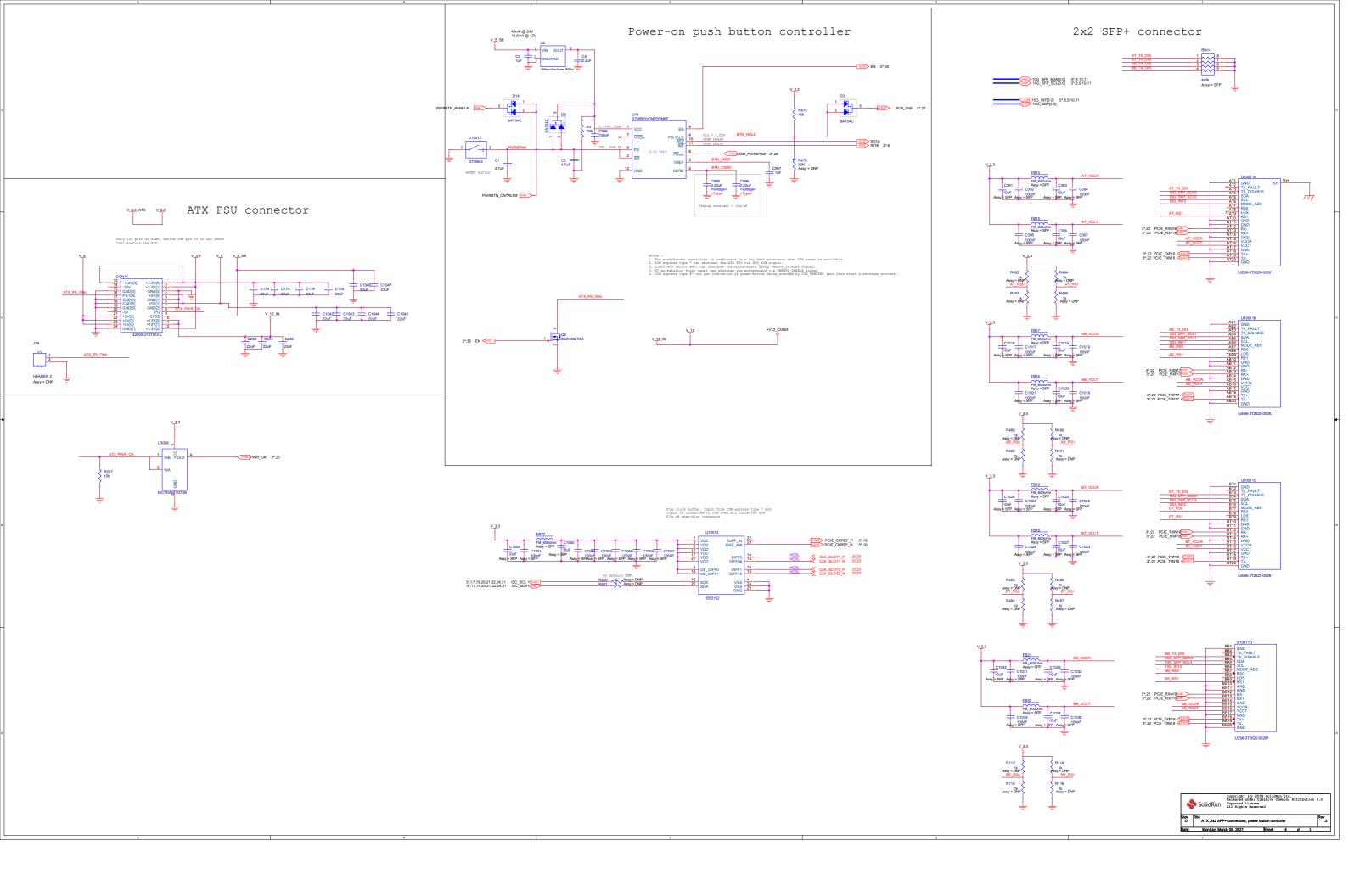
SMB LAERBY17,19.2021 22.24.31 HOUT PCIE\_TXP6 3^,22 HOUT PCIE\_TXN6 3^,22 3^,22 PCIE\_RXP6 H-N 3^,22 PCIE\_RXN6 H-N 3^,15 SATA0\_TXP 3^,15 SATA0\_TXN PCIE\_TXP7 3^,22 10,0407 PCIE\_TXN7 3^,22 3^,22 PCIE\_RXP7 H-N 3^,22 PCIEGRANT2 H-IN 3^,15 SATA0\_RXP H3 3^,15 SATA0\_RXN HBI SATA1\_RXP 3^,16 HBI SATA1\_RXN 3^,16 10G\_KR\_RXP3 10G\_KR\_RXN3 10G\_KR\_TXP3 10G\_KR\_TXN3 3^,22 PCIE\_TXP15 40 3^,22 PCIE\_TXN15 40 H-IN 10G\_PHY\_CAP\_23 H-IN 10G\_PHY\_CAP\_01 10G, KR, TX3-GND
10G, RR, TX3-10G, KR, TX2-10G, KR, TX2-10G, KR, TX2-10G, SPP, SCI, 2 10G, 10G\_KR\_TXP2 10G\_KR\_TXN2 GND 10G KR RX2+ 10G\_KR\_RX2-GND (FIXED) 10G\_SFP\_SDA3 10G\_SFP\_SDA2 10G\_PHY\_RST\_01 10G\_PHY\_RST\_01 10G\_LED\_SDA 3^,22 SUS\_S5# HOUT 3^,22 PCIE\_TXP14 3^,22 PCIE\_TXN14 TP97O 3^,15 SATA\_ACT# 10G\_SFP\_SDA3 10G\_SFP\_SDA2 10G\_SFP\_SCL3 10G\_SFP\_SCL2 SD1 clock bypass for 1588 sync R534 Assy = PTP 1588 CLK\_161\_BYP\_P R535 Assy = PTP 1588 CLK\_161\_BYP\_N HOUT 12C\_SCL 3^,17,19,20,21,22,24,31 D TP103

H-IN PCIE\_RXP13 3^,22

H-IN PCIE\_RXN13 3^,22 3^,22 PCIE\_TXP13 HOUT 3^,22 PCIE TXN13 HOUT 10G\_KR\_TXP1 10G\_KR\_TXN1 CALON PINCE DISTANCE DE L'ALLES D 3^,22 PCIE\_TXP12 HOUT 3^,22 PCIE TXN12 HOUT H-IN PCIE\_RXP12 3^,22 H-IN PCIE\_RXN12 3^,22 3^,21 USB2\_DN H8I 3^,21 USB2\_DP H8I HB USB3\_DN 3^,21 HB USB3\_DP 3^,21 3^,13 USB0\_DN HBI 3^,13 USB0\_DP HBI 3^,27 VCC\_RTC H-IN 88\_TRIG\_IN1\_COM HBI USB1\_DN 3^,20 HBI USB1\_DP 3^,20 3^,22 PCIE\_RXP16H-N 3^,22 PCIE\_RXN16H-N H-OUT PCIE\_TXP16 3^,22 H-IN SYS RESET# 3^,25 PCIE\_TXP17 3^,22
TYPESU PCIE\_TXN17 3^,22
TYPESU PCIE\_TXN17 3^,22
HOU PCIE\_TXN18 3^,22 GB RESET# 3^,12,17,20,21,22 3^,22 PCIE\_RXP17H-N 3^,22 PCIE\_RXN17H-N 4 PF1# N PCIE RXP5 3^22 N PCIE RXN5 3^22 SD CMD 3^14 N PCIE RXN4 3^22 N PCIE RXN4 3^22 SP02 3^17 N PCIE RXN3 3^22 N PCIE RXN3 3^22 N PCIE RXN3 3^22 3^,22 PCIE\_RXP18H-N 3^,22 PCIE\_RXN18H-N H-OUT 10G\_KR\_TXP[3:0] 3^,8,9,10,11 H-OUT 10G\_KR\_TXN[3:0] 3^,8,9,10,11 H-OUT PCIE\_TXP19 3^,22 H-OUT PCIE\_TXN19 3^,22 3^,22 PCIE\_RXP19H-IN 3^,22 PCIE\_RXN19H-IN HIN 10G\_KR\_RXP[3:0] 3^,8,9,10,11 HIN 10G\_KR\_RXN[3:0] 3^,8,9,10,11 3^,22 PCIE\_TXP3 HOUT 3^,22 PCIE\_TXN3 HOUT H8 10G\_SFP\_SDA[3:0] 3^,9,10,11 10G\_SFP\_SCL[3:0] 3^,8,9,10,11 H-OUT PCIE\_TXP20 3^,22 H-OUT PCIE\_TXN20 3^,22 3^,22 PCIE\_RXP20H-IN 3^,22 PCIE\_RXN20H-IN H-N PCIE RXP2 3^22 H-N PCIE RXN2 3^22 H-N SD\_CD 3^14 H-N PCIE RXN1 3^21 H-N PCIE RXN1 3^21 H-N PCIE RXN1 3^21 3^,22 PCIE\_TXP2 3^,22 PCIE\_TXN2 PCIE\_TXP21 3^,22 3^,22 PCIE\_RXP21H-N 3^,22 PCIE\_RXN21H-N HIN 10G\_INT[3:0] 3^,8,9,10,11 10G\_SDP[3:0] HOUT PCIE\_TXP22 3^,22 3^,22 PCIE\_RXP22H-N 3^,22 PCIE\_RXN22H-N 3^,14 SD\_D2 3^,20 PCIE\_TXP0 3^,20 PCIE\_TXN0 HBI 10G\_PHY\_MDIO\_SDA[3:0] HBI 10G\_PHY\_MDC\_SCL[3:0] H-IN PCIE\_RXP0 3^,20 H-IN PCIE\_RXN0 3^,20 H-007 PCIE\_TXP23 3^,22 H-007 PCIE\_TXN23 3^,22 3^,22 PCIE\_RXP23H-IN 3^,22 PCIE\_RXN23H-IN PCIE\_RX23+ PCIE\_RX23-3^,22 PCIE\_TXP8 HOUT 3^,22 PCIE\_TXN8 HOUT H-IN PCIE\_RXP8 3^,22 H-IN PCIE\_RXN8 3^,22 HOUT PCIE\_TXP24 3^,22 3^,22 PCIE\_RXP24H-N 3^,22 PCIE\_RXN24H-N CND
PCIE RX9+
PCIE RX9PCIE RX10PCIE RX10PCIE RX10PCIE RX10PCIE RX10PCIE RX11PCIE RX11PCIE RX11VCC 5V SBY
VCC 5V SBY 3^,22 PCIE\_TXP9 HOUT 3^,22 PCIE\_TXN9 HOUT H-IN PCIE\_RXP9 3^,22 H-IN PCIE\_RXN9 3^,22 PCIE\_TXP25 3^.22 HOUT PCIE\_TXN25 3^.2211588\_ALARM\_OUT1\_COM 3^,22 PCIE\_RXP25H-N 3^,22 PCIE\_RXN25H-N 3^,22 PCIE\_TXP10 FOUT 3^,22 PCIE\_TXN10 FOUT H-IN PCIE\_RXP10 3^,22 H-IN PCIE\_RXN10 3^,22 HOUT PCIE\_TXP26 3^,22 HOUT PCIE\_TXN26 3^,22 3^22 PCIE\_RXP26H-N 3^22 PCIE\_RXN26H-N 3^,22 PCIE\_TXP11 FOUT 3^,22 PCIE\_TXN11 FOUT H-IN PCIE\_RXP11 3^,22 H-IN PCIE\_RXN11 3^,22 HOUT PCIE\_TXP27 3^,22 HOUT PCIE\_TXN27 3^,22 3^,22 PCIE\_RXP27H-IN 3^,22 PCIE\_RXN27H-IN HOUT PCIE TNP28 3^22
HOUT PCIE TNP28 3^22
HOUT PCIE TNP29 3^22
HOUT PCIE TNP29 3^22 3^,22 PCIE\_RXP28H-N 3^,22 PCIE\_RXN28H-N VCC 5V SBY DISTRIBED BBS MCSI TRI EN MCSI TRAD M 3^,19 PCIE\_CKREF\_P HOUT 3^,19 PCIE\_CKREF\_N HOUT GND PCIE\_RX29+ PCIE\_RX29-3^,22 PCIE\_RXP29H-N 3^,22 PCIE\_RXN29H-N 3^25 SPI POWER 3^25 SPI MISO H-N
3^25 SPI MISO H-N
3^14 SD CLK 4-0-01
3^25 SPI CLK 4-0-01
3^25 SPI MOSI 4-0-01 H-001 PCIE\_TXP30 3^,22 H-001 PCIE\_TXN30 3^,22 3^,22 PCIE\_RXP30H-N 3^,22 PCIE\_RXN30H-N 0 PCIE\_TXN30 3^.22
11 PCIE\_TXN31 3^.22
2 PCIE\_TXN31 3^.22
3 +V12\_CCM12CIE\_TXN31 3^.22 3^,22 PCIE\_RXP3 H-N 3^,22 PCIE\_RXN3 H-2 POM 3^,26 SER0\_TX HOUT 3^,26 SER0\_RX HIN C977 C980 0.1µF 0.1µF 3^,26 SER1\_TX HIN 3^,26 SER1\_RX HIN HOUT FAN\_PWM 3^,28 H-IN FAN\_TACHIN 3^,28 IEEE-1588 Access Header J42 M2.5 OVDD RST\_IEEESLT\_B 4C8 I1588\_CLK\_IN\_COM MECH\_2.7\_7 MECH\_2.7\_7 MECH\_2.7\_7 2D8 CFG\_IEEE\_SRC XX CLK 1588 CGEN XX Copyright (c) 2019 SolidRun ltd.
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## 1588v2 & SYNCE

