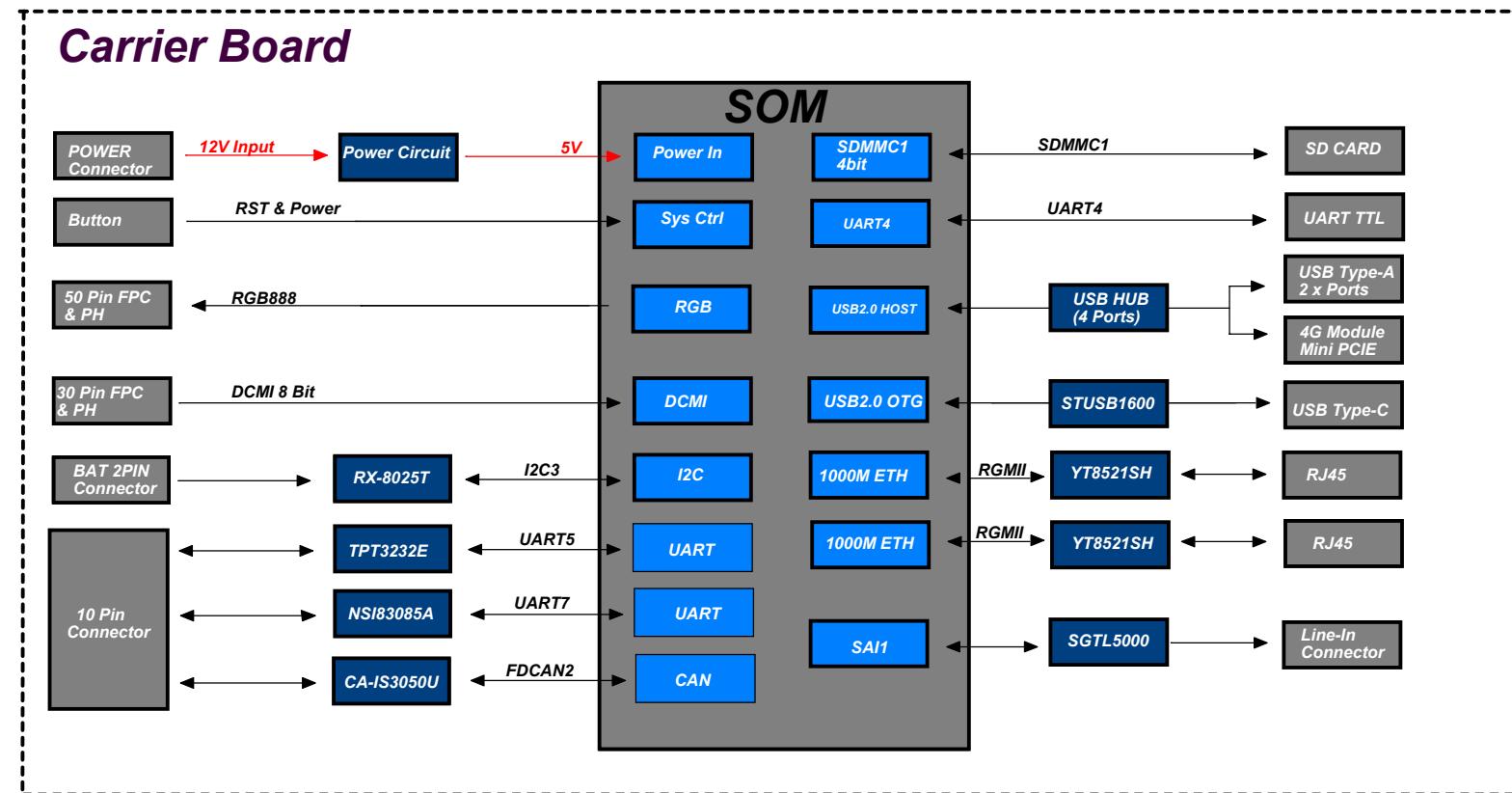


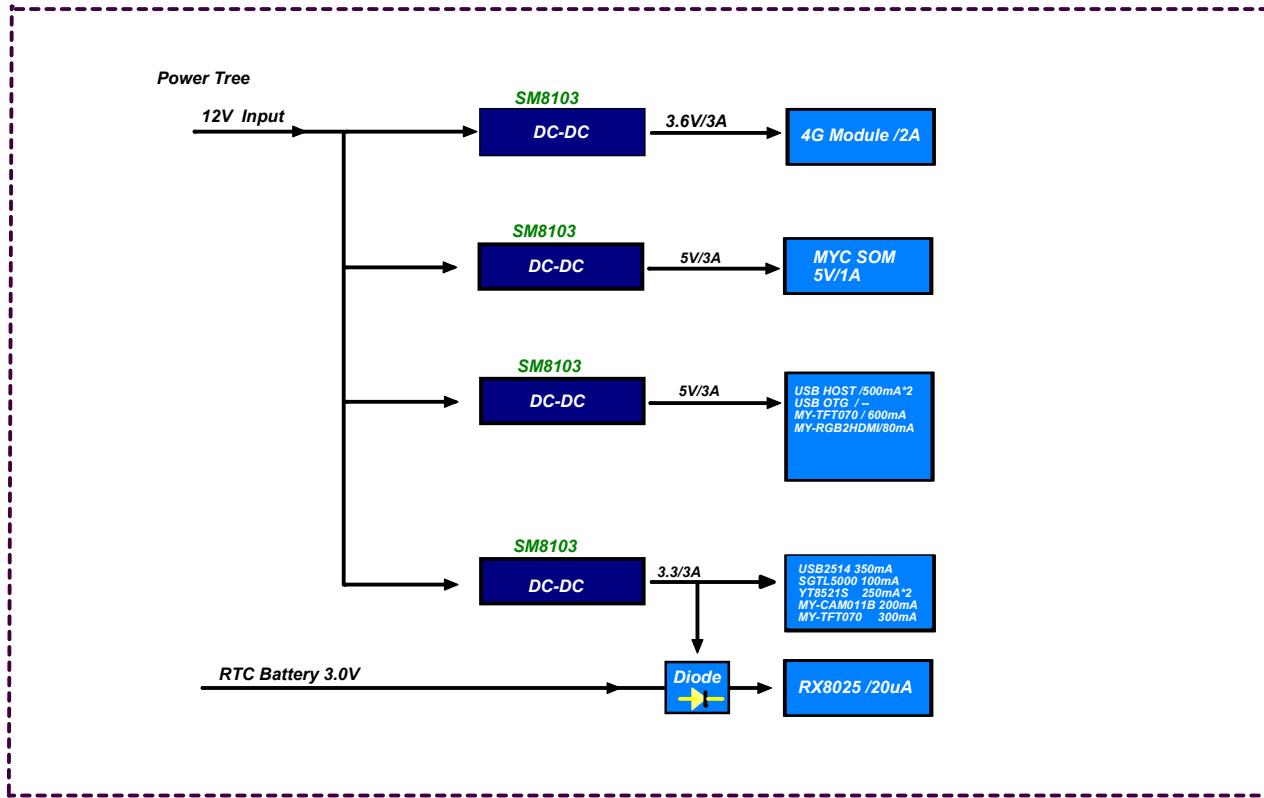
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7. SD & BOOT & LED & Debug
8. PH
9. Audio, RTC, IO
10. ENET x2
11. 4G MINI -PCIE
12. MYC Connect
13. Mechanical

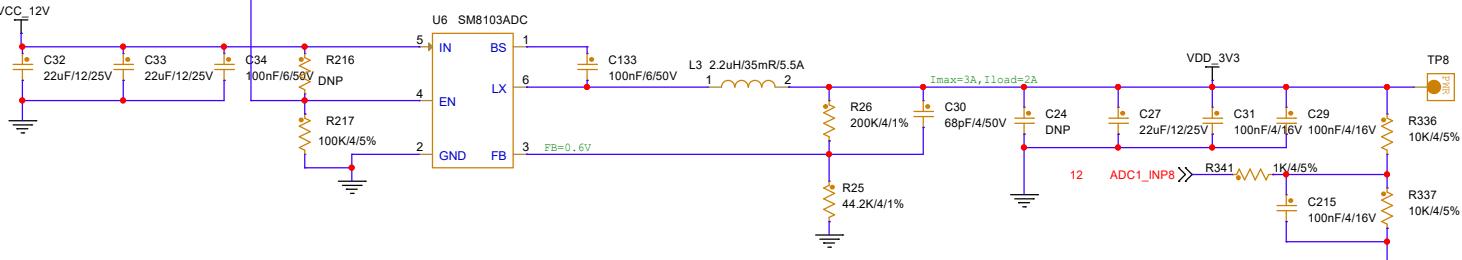
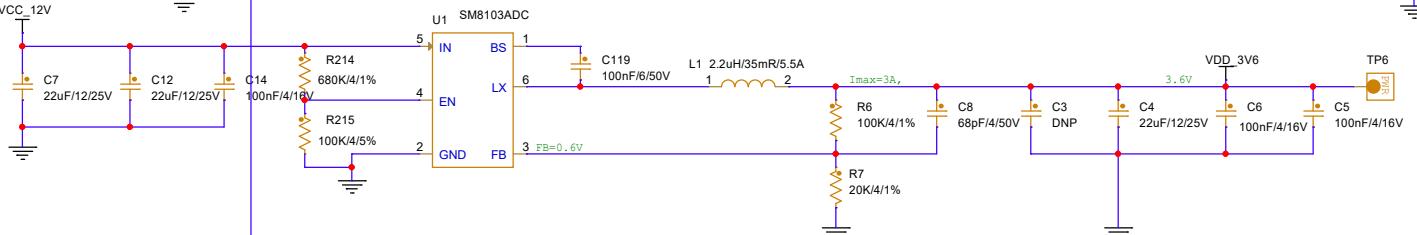
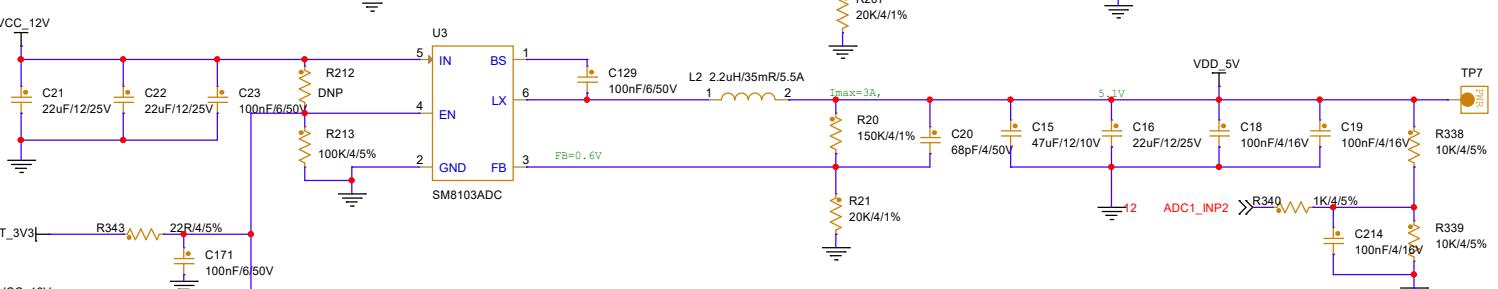
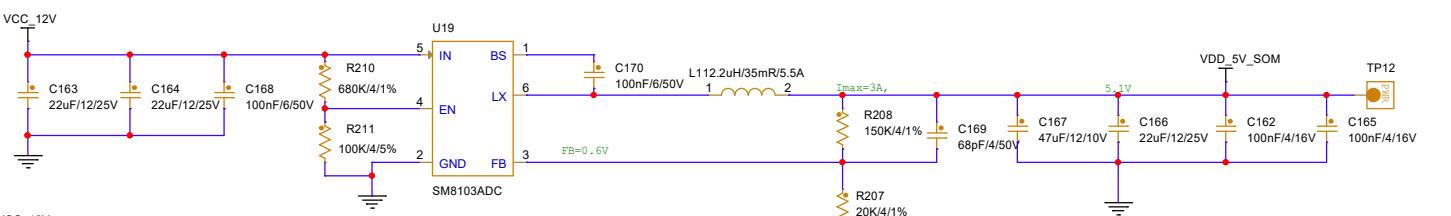
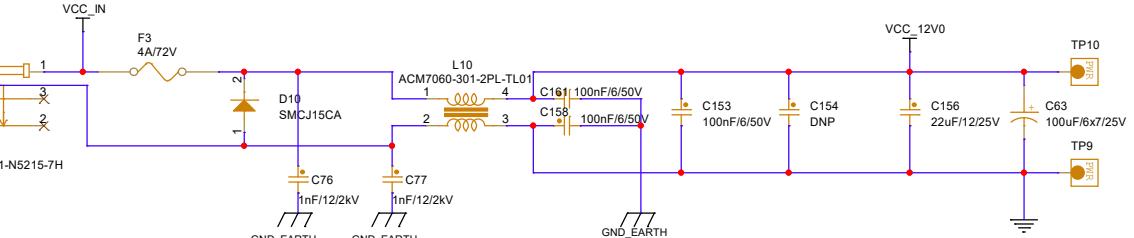
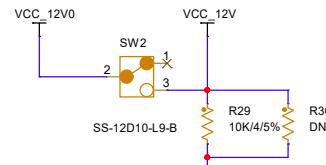
VERSION HISTORY

Revision	Description	Date	Drawn	Checked	Approved
V01	Initial Edition	2021-09-24			
V02		2023-02-28			
V10	Release version	2023-04-18			





Power Switch (On/Off)



MYIR

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Title: MYB-YF13X-SCH-V10

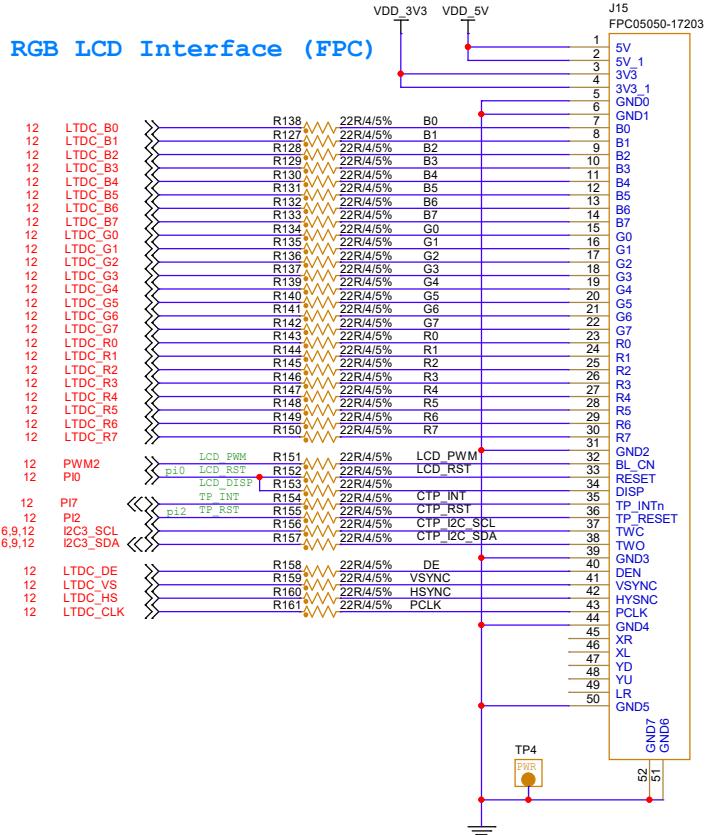
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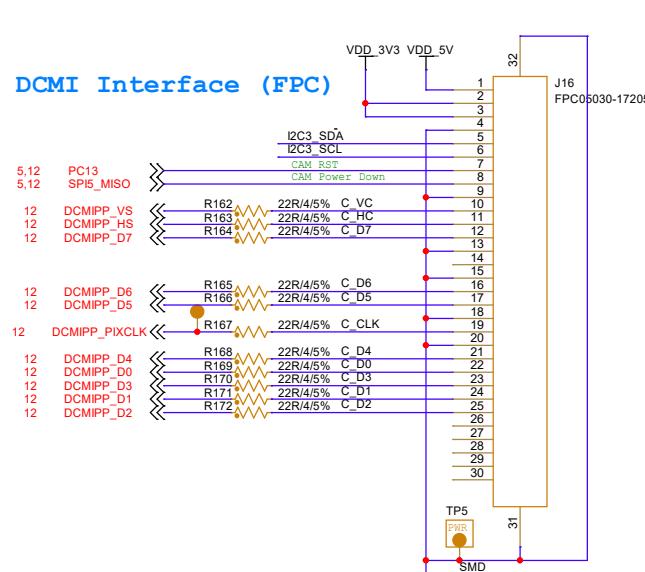
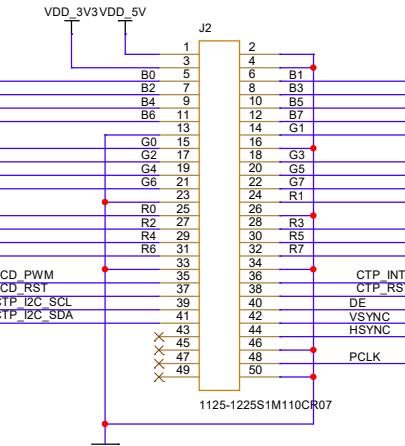
Rev:

V1.0

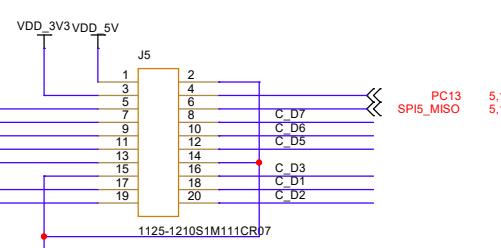
Draw By: MYIR Date: Tuesday, April 18, 2023 Sheet: 4 of 13



Pin Header 2x25



Pin Header 2x10



<http://www.myir-tech.com>

Title: MYB-YF13X-SCH-V10

Size: A3

Document Number: 05-RGB & DCMI

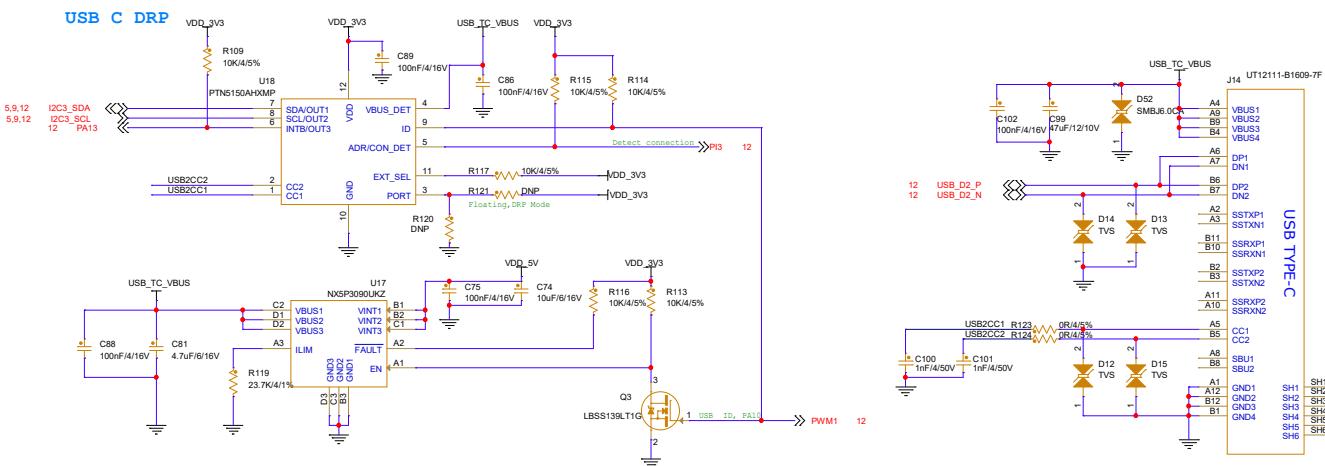
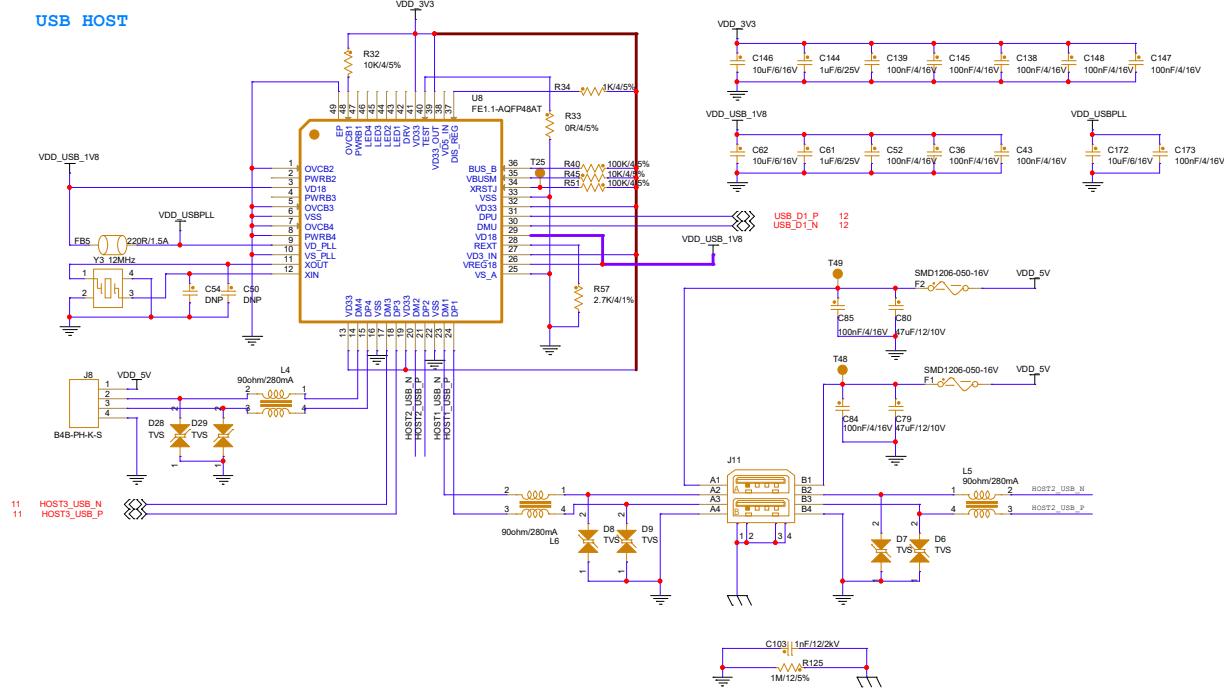
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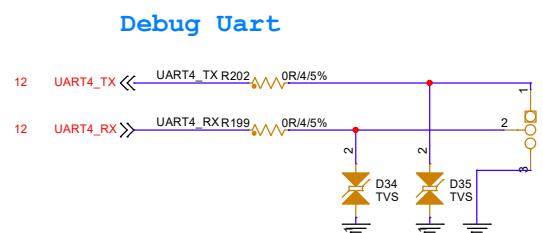
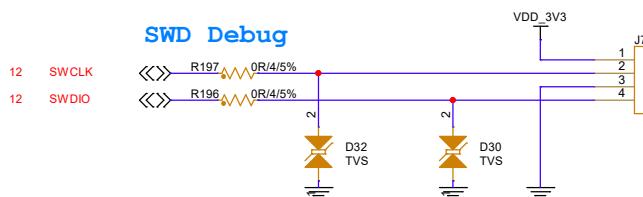
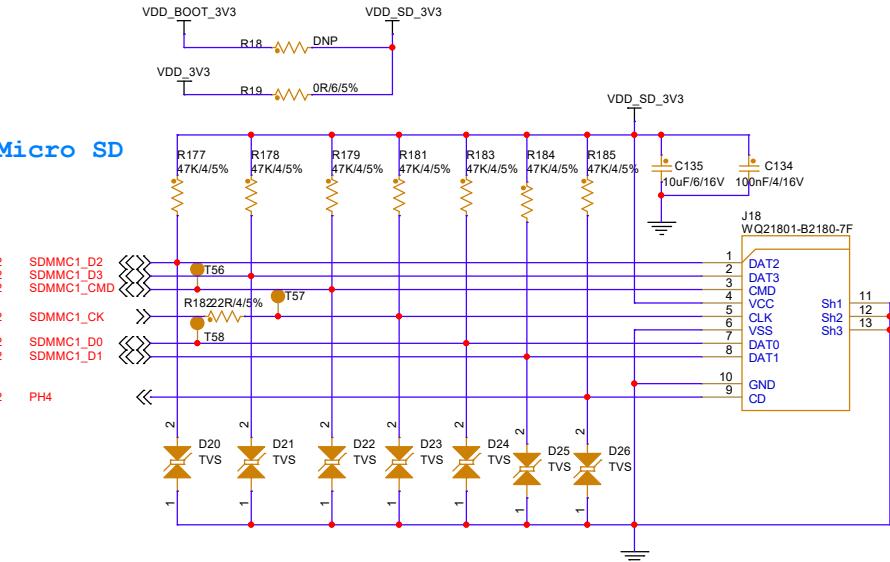
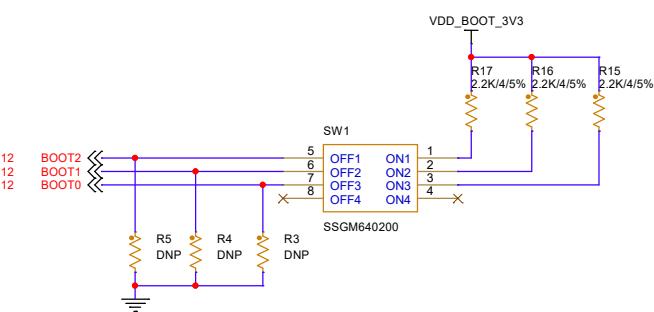
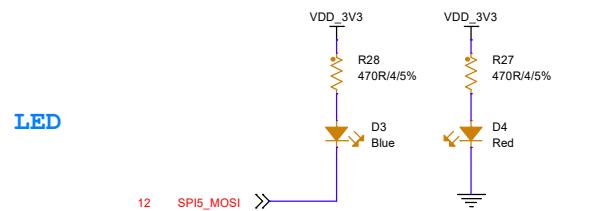
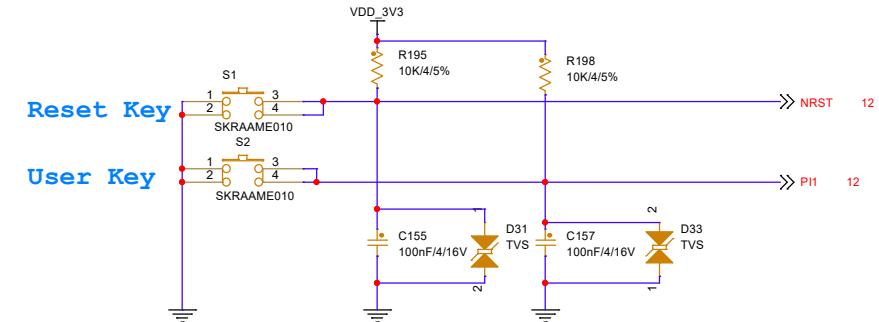
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Draw By: MYIR

Date: Tuesday, April 18, 2023

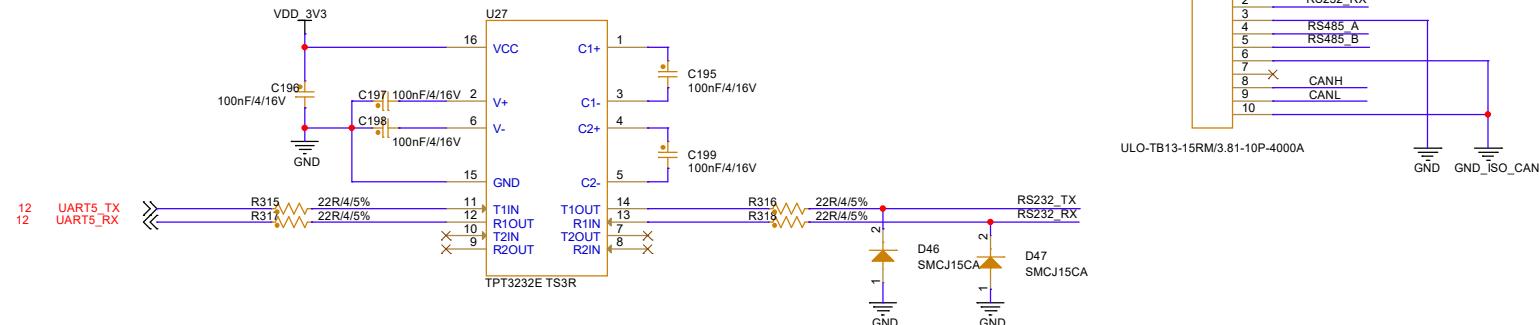
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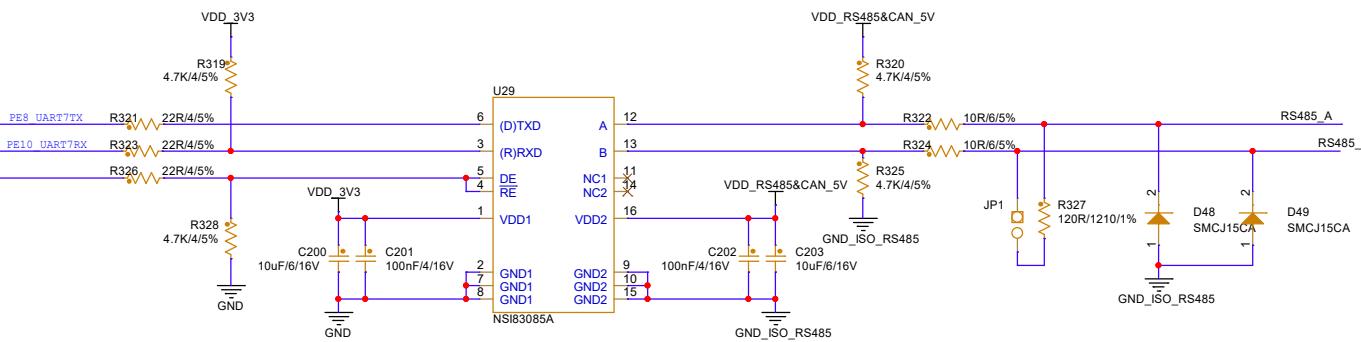


B2 B1 B0	Initial boot mode	Comments
0 0 0	UART and USB	Download image
0 1 0	eMMC	Boot from eMMC
0 1 1	Nand Flash	Boot from Nand Flash
1 0 1	SD Card	Boot from Micro SD
1 1 1	QSPI Nand Flash	BOOT from QSPI Nand Flash

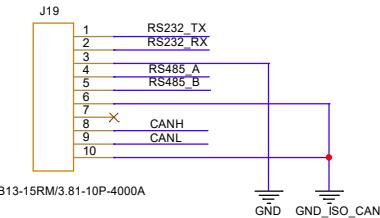
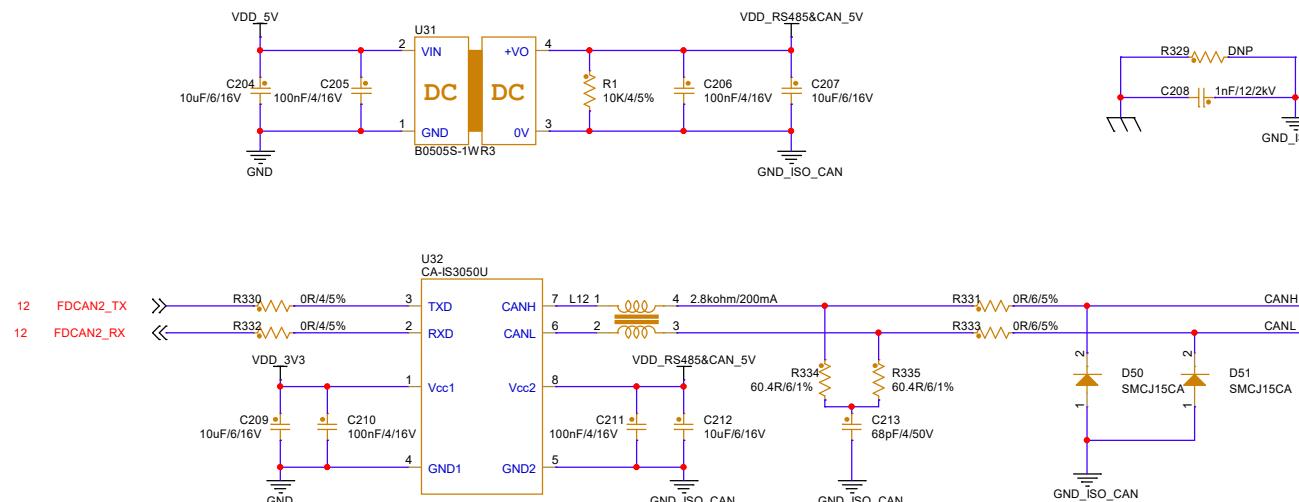
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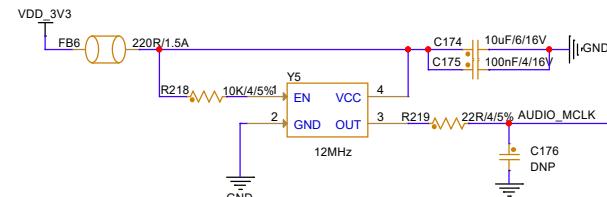
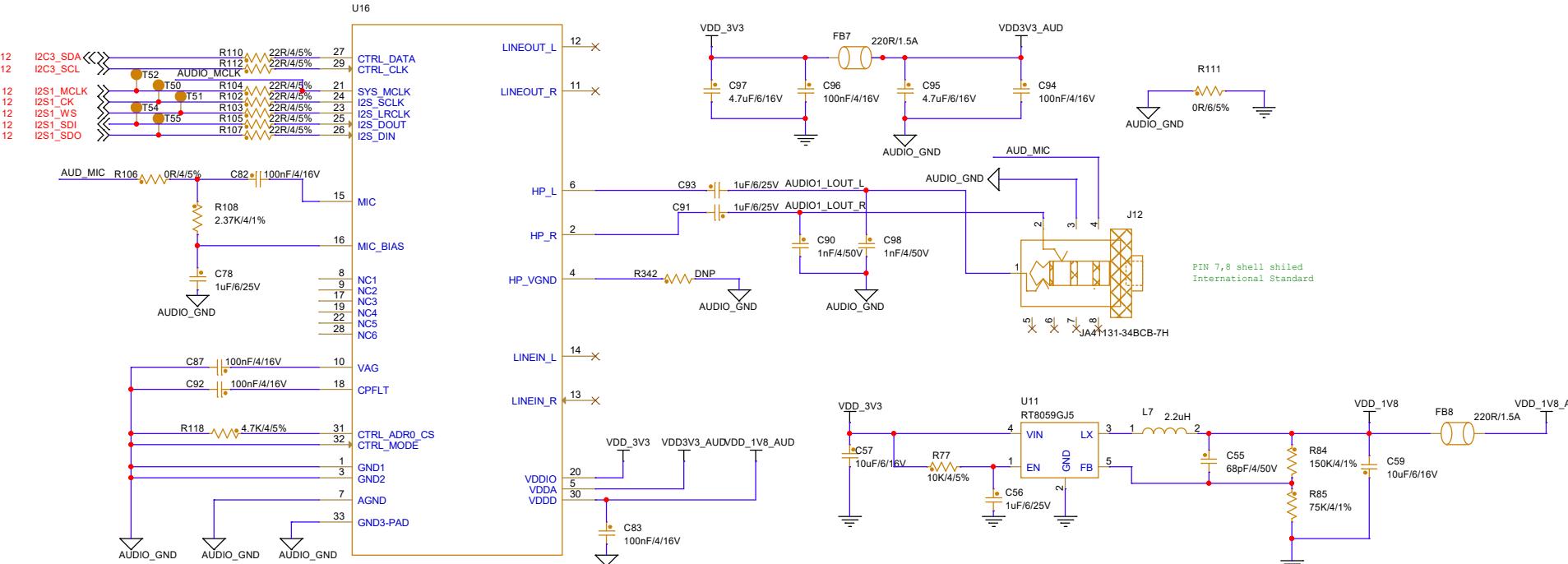


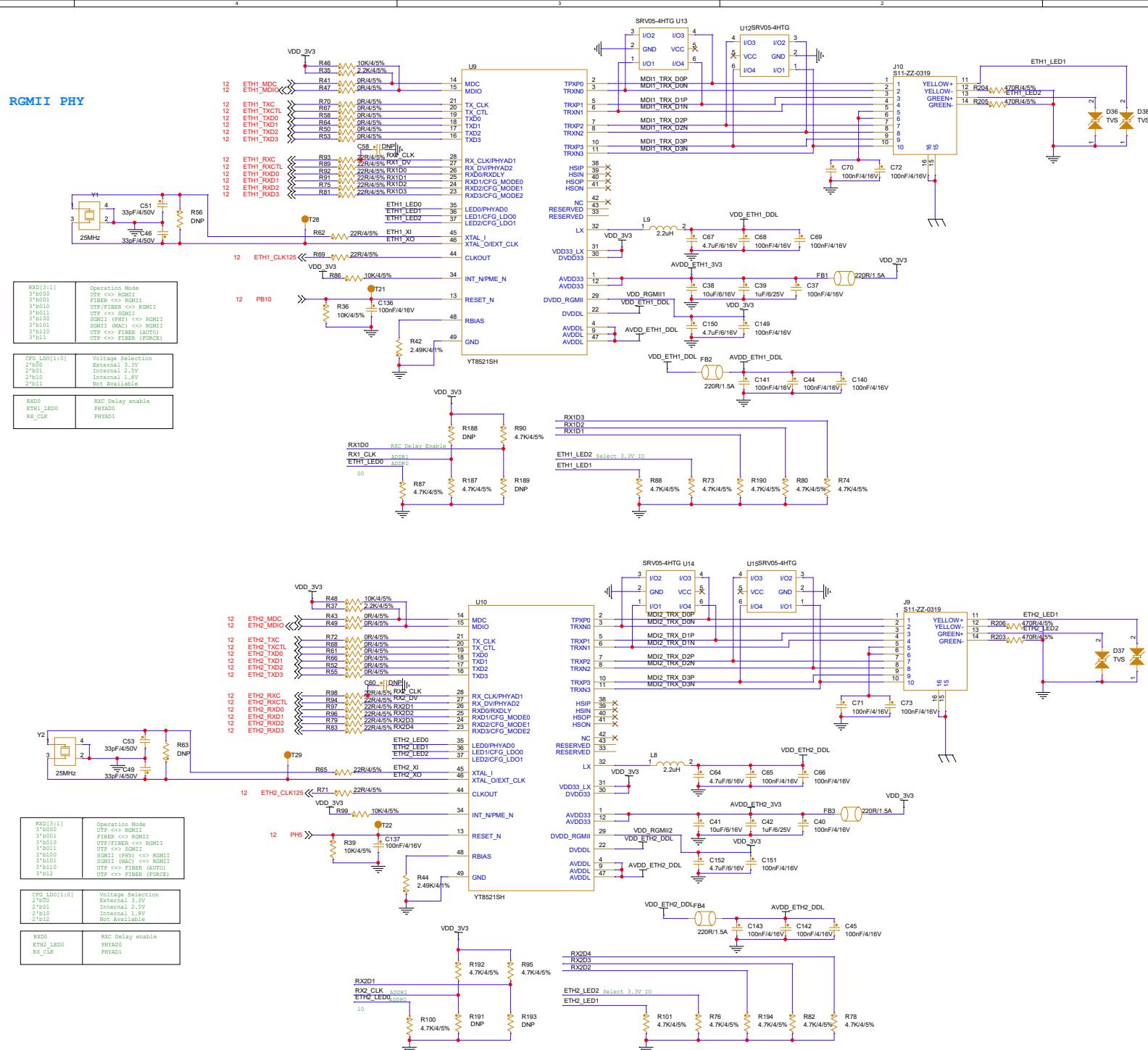
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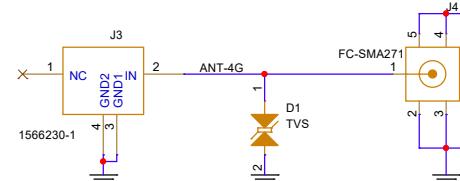
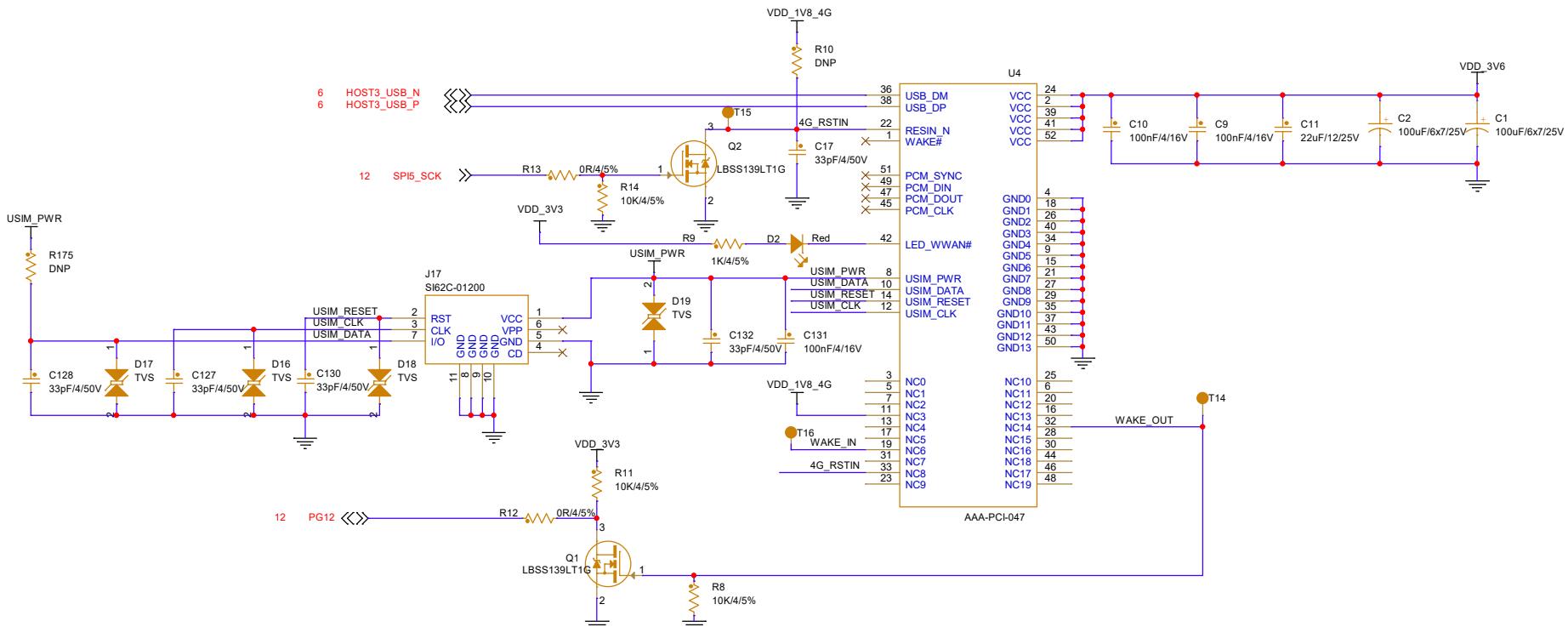


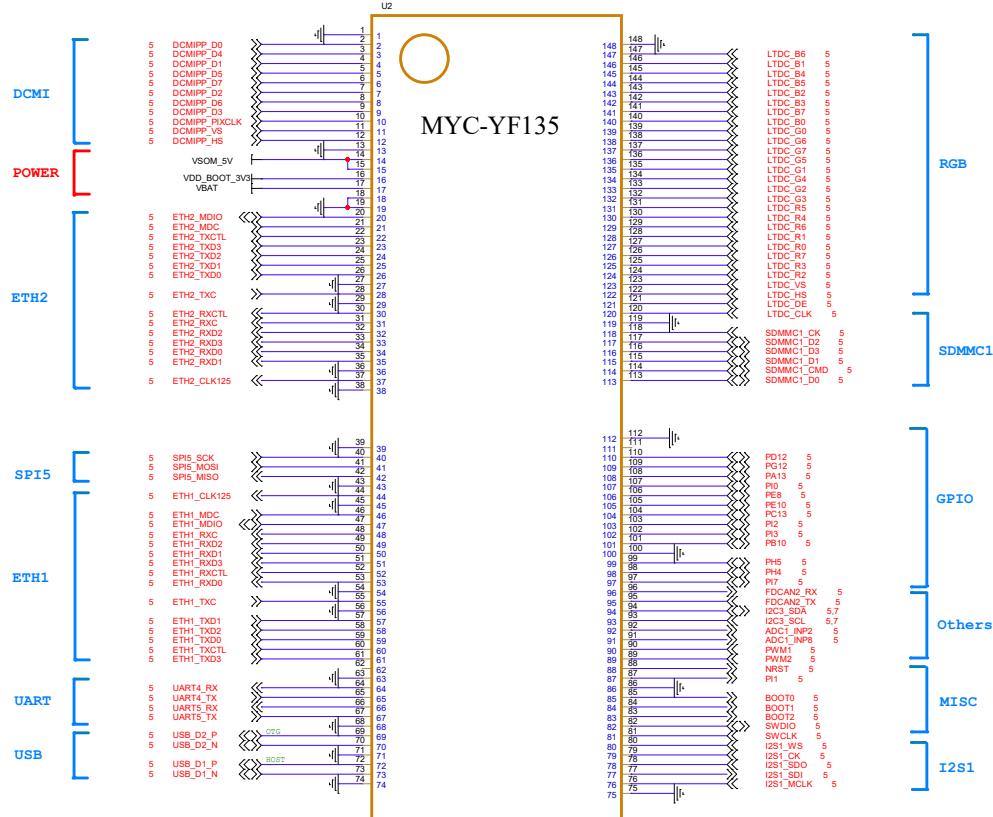
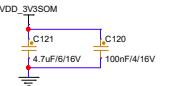
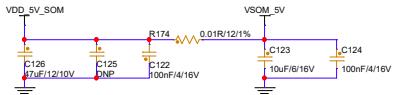
CAN

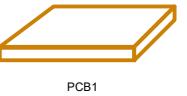




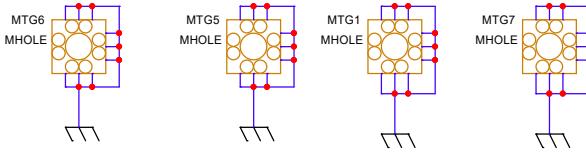








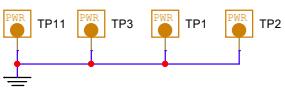
PCB1



Board Mechanical Hole



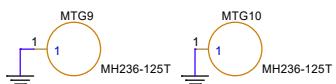
Mark Point



GND Test Point

TEMPLATE1
TEMPLATE_A2

MYIR PCB Template



Mechanical Hole for Mounting 4G Module