xbaudRateGenerator

baudRateGenerator

r_Rx_Counter0_i
RTL_ADD

r_Rx_Counter_i
S=3'b101
S=default
RTL_MUX

r_Rx_Counter_reg[2:0]
CLR
RTL_REG_ASYNC

o_Rx_ClkTick_i
A[2:0] O
RTL_ROM

o_Rx_ClkTick0_i
RTL_INV

o_Rx_ClkTick_reg
CLR
RTL_REG_ASYNC

o_Rx_ClkTick

r_Tx_Counter0_i
RTL_ADD

r_Tx_Counter_i
S=7'b1101011
S=default
RTL_MUX

r_Tx_Counter_reg[6:0]
CLR
RTL_REG_ASYNC

o_Tx_ClkTick_i
A[6:0] O
RTL_ROM

o_Tx_ClkTick0_i
RTL_INV

o_Tx_ClkTick_reg
CLR
RTL_REG_ASYNC

o_Tx_ClkTick

clk
reset_n