

xbaudRateGenerator o_Rx_ClkTick_i r_Rx_Counter_reg[2:0] A[2:0] O r_Rx_Counter_i CLR r_Rx_Counter0_i RTL_ROM o_Rx_ClkTick_reg O[2:0] Q O[2:0] S=default I1[2:0] CLR 10[2:0] RTL_MUX o_Rx_ClkTick S[2:0] o_Rx_ClkTick0_i RTL_ADD RTL_REG_ASYNC D r_Tx_Counter_i r_Tx_Counter_reg[6:0] RTL_INV RTL_REG_ASYNC r_Tx_Counter0_i CLR O[6:0] O[6:0] S=default I1[6:0] Q o_Tx_ClkTick_i 10[6:0] o_Tx_ClkTick_reg RTL_MUX S[6:0] RTL_ADD A[6:0] O CLR RTL_REG_ASYNC RTL_ROM o_Tx_ClkTick CE clk reset_n D o_Tx_ClkTick0_i RTL_REG_ASYNC RTL_INV baudRateGenerator





