TRANSFER REGISTER(TX)

This module allows the transfer of data between the 16bit and 8bit busses/registers

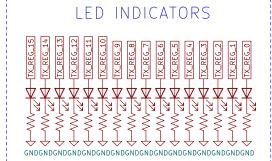
SEL LSB MSB

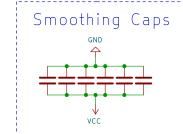
H - Latch Bus Data into TX_LSB
L - Latch Bus Data into TX_MSB
H - Latch Address LSB Data into TX_LSB
L - Latch Address MSB Data into TX_MSB
L - Latch Address into TX

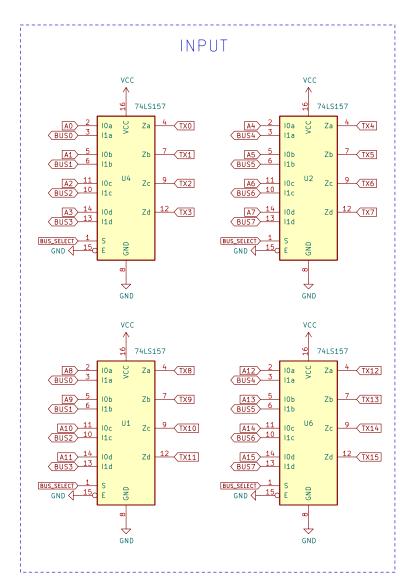
ASSERT_LSB_MAIN - TX_LSB => Data Bus ASSERT_MSB_MAIN - TX_MSB => Data Bus ASSERT_ADDRESS - TX => Address Bus

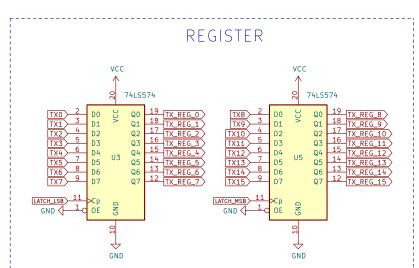
Pull U/D Transfer Bus Address Bus

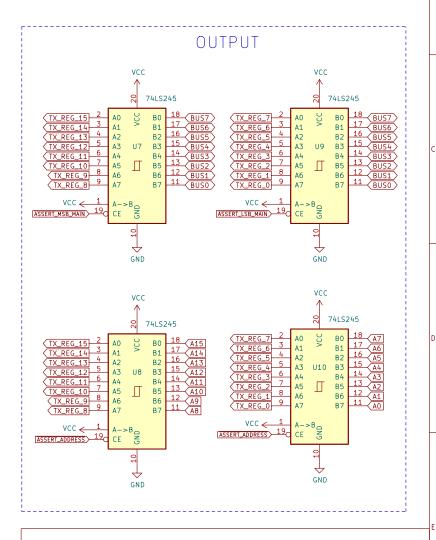












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