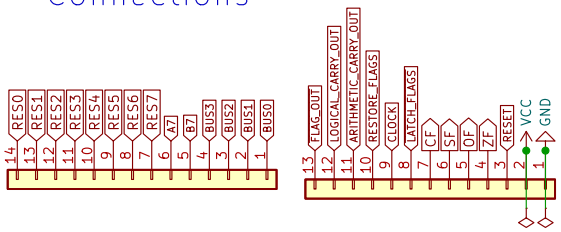
FLAGS REGISTER

$\overline{\text{LATCH\_FLAGS}}$ – A LOW signal will store the data asserted from the multiplexer into the Flags Register (FR)

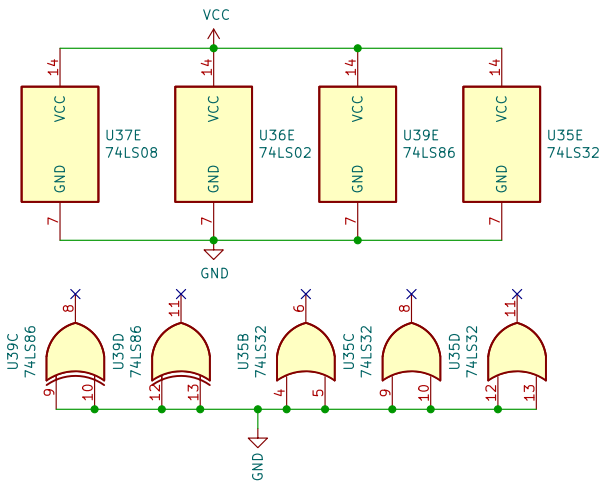
- $\overline{\text{RESTORE}}$: LOW, uses signals from ALU
- RESTORE: HIGH, uses signal asserted on data bus

$\overline{\text{FLAG\_OUT}}$ – Assers the current flags statuses onto the Data bus, typically used to push it onto the stack to handle an ISR

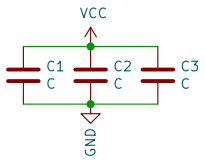
Connections



Logic Gate Power

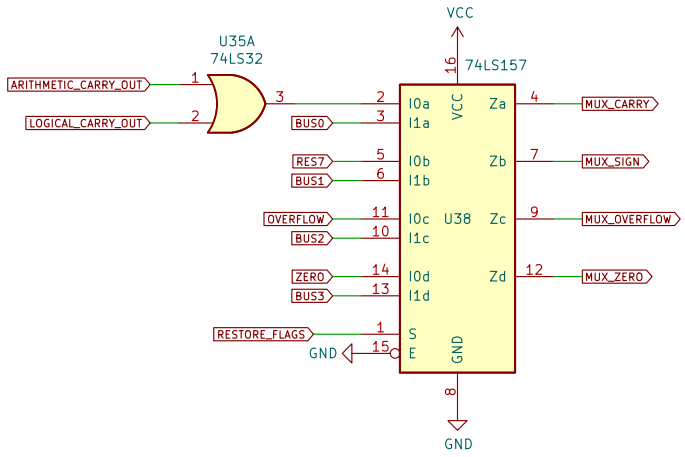


Smoothing Caps

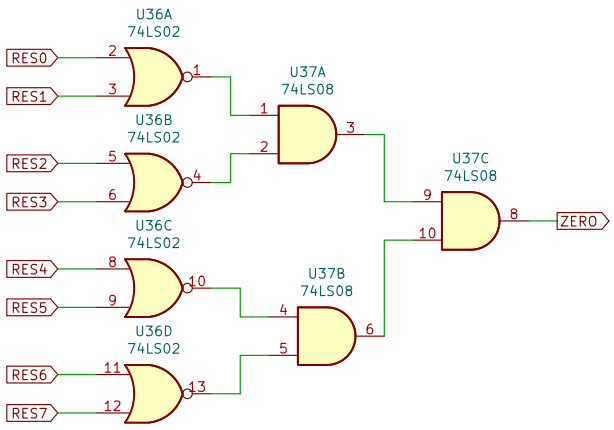


Source Multiplexer

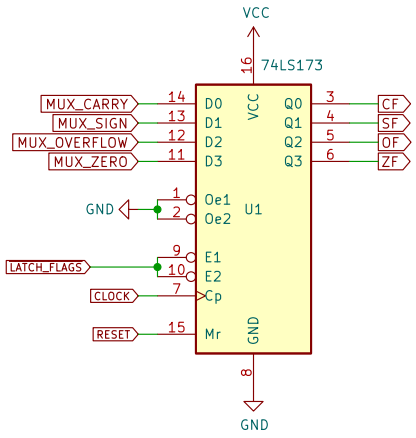
Flags come directly from ALU, or, from the flag/data bus to restore flags from the stack or another location



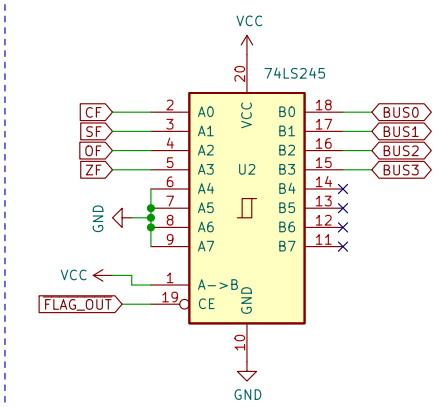
Zero



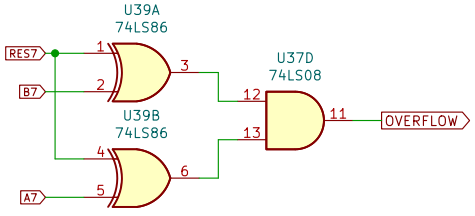
REGISTER



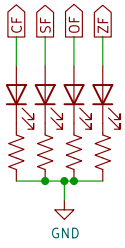
Bus Connection



OVERFLOW



Leds



For storing and asserting current flag statuses from ALU

theWickedWebDev/8-Bit-Computer

Sheet: /

File: Flags Register.kicad_sch

Title: Flags Register

Size: User	Date: 2022-01-03	**Rev: 3**
KiCad E.D.A. kicad (6.0.0-0)		Id: 1/1