

GPR Assertions Controller

CONTROL WORD HIGH BYTE

Assertions to 8bit Bus

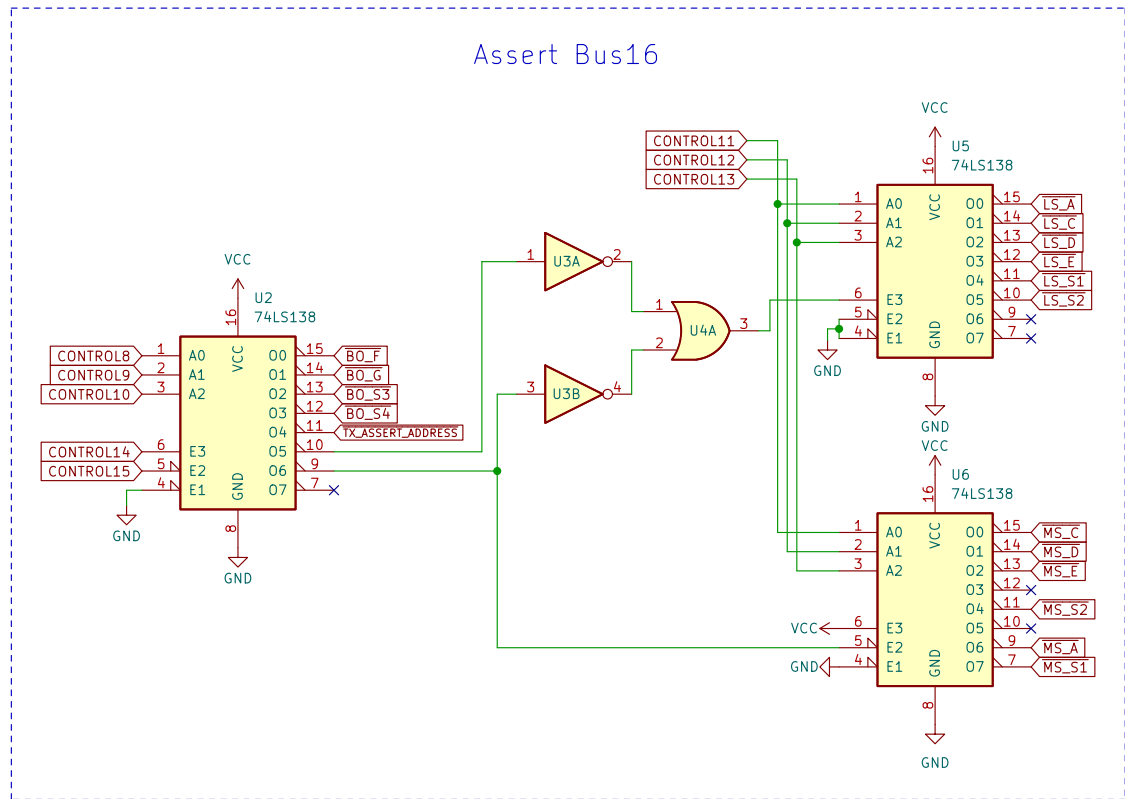
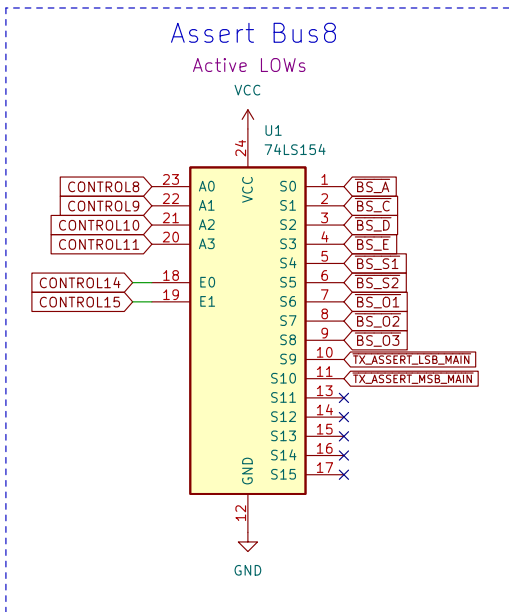
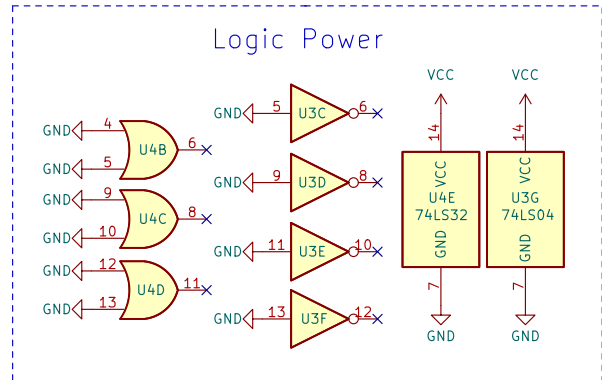
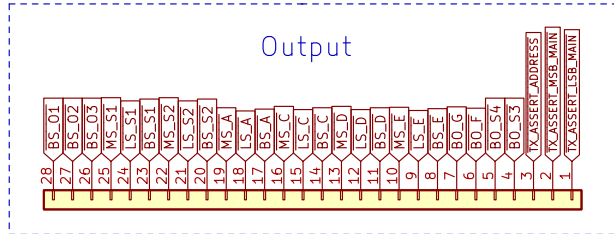
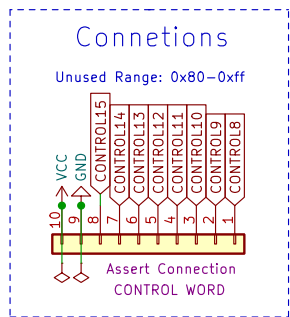
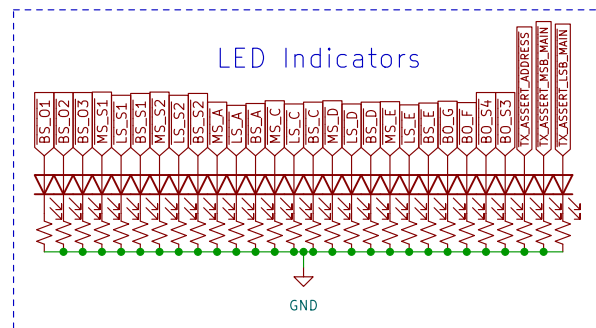
11xxxx - Assertions N00P
00xx0000 - Assert A
00xx0001 - Assert C
00xx0010 - Assert D
00xx0011 - Assert E
00xx0100 - Assert Scratch 1
00xx0101 - Assert Scratch 2
00xx0110 - Assert Output 1
00xx0111 - Assert Output 2
00xx1000 - Assert Output 3
00xx1001 - Assert TX LSB
00xx1010 - Assert TX MSB
00xx1011 - **** unused active low
00xx1100 - **** unused active low
00xx1101 - **** unused active low
00xx1110 - **** unused active low
00xx1111 - **** unused active low

Assertions to 16bit Address Bus

01xxx000 - Assert F
01xxx001 - Assert G
01xxx010 - Assert S3
01xxx011 - Assert S4
01xxx100 - Assert TX
01xxx101 - Assert r onto 16bit bus**
01xxx110 - Assert rr onto 16bit bus**
01xxx111 - **** unused active low

01000101 - Assert Aonto 16bit bus
01001101 - Assert C onto 16bit bus
01010101 - Assert D onto 16bit bus
01011101 - Assert E onto 16bit bus
01100101 - Assert S1 onto 16bit bus
01101101 - Assert S2 onto 16bit bus
01110101 - ****unused r => 16bit LSB
01111101 - ****unused r => 16bit LSB

01000110 - Assert AC onto 16bit bus
01001110 - Assert CD onto 16bit bus
01010110 - Assert DE onto 16bit bus
01011110 - ****unused rr => 16bit (Assert E onto 16bit bus)
01100110 - Assert SS onto 16bit bus
01101110 - ****unused rr => 16bit (Assert S2 onto 16bit bus)
01110110 - Assert A onto MSB of 16bit bus
01111110 - Assert S1 onto MSB of 16bit bus



Sheet: /
File: register-board-controller.kicad_sch

Title:

Size: User
KiCad E.D.A. kicad (6.0.0-0)

Date:

Rev:

Id: 1/1