

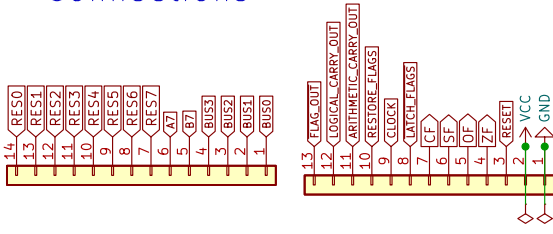
FLAGS REGISTER

LATCH_FLAGS – A LOW signal will store the data asserted from the multiplexer into the Flags Register (FR)

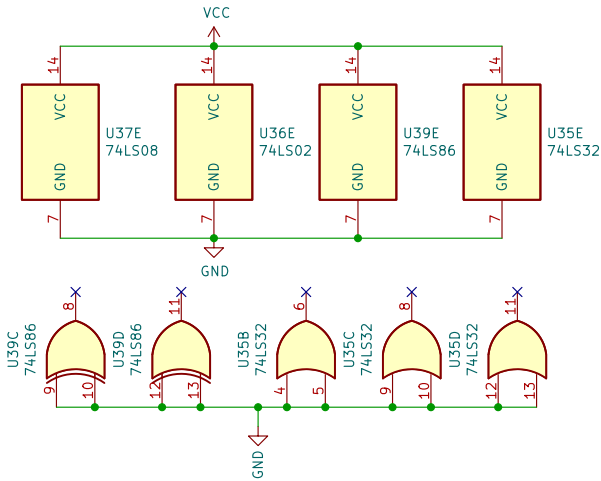
- RESTORE: LOW, uses signals from ALU
- RESTORE: HIGH, uses signal asserted on data bus

FLAG_OUT – Asserts the current flags statuses onto the Data bus, typically used to push it onto the stack to handle an ISR

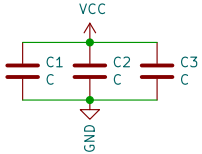
Connections



Logic Gate Power

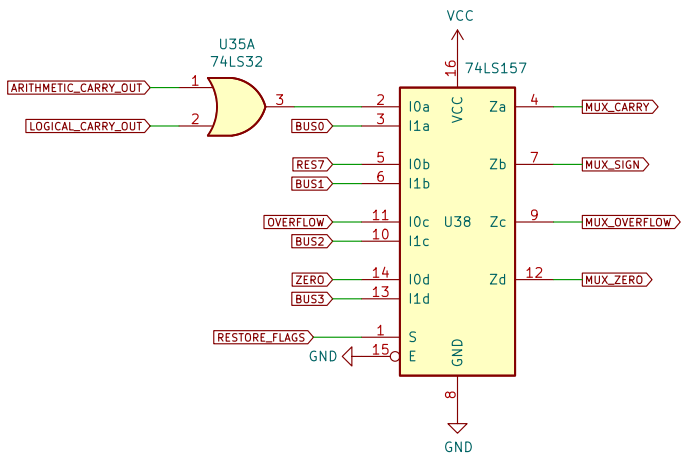


Smoothing Caps

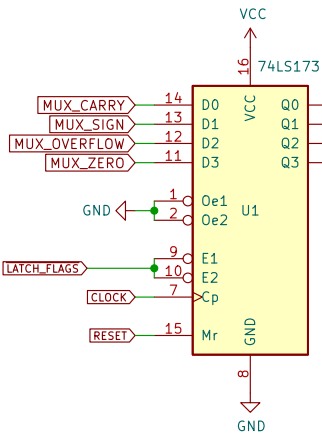


Source Multiplexer

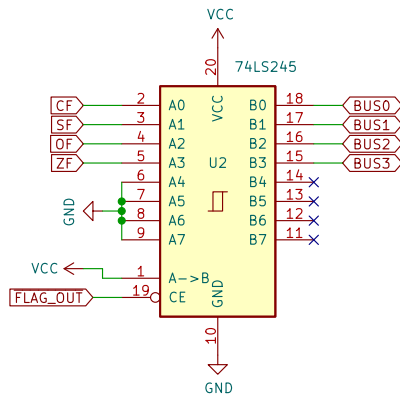
Flags come directly from ALU, or, from the flag/data bus to restore flags from the stack or another location



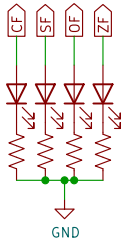
REGISTER



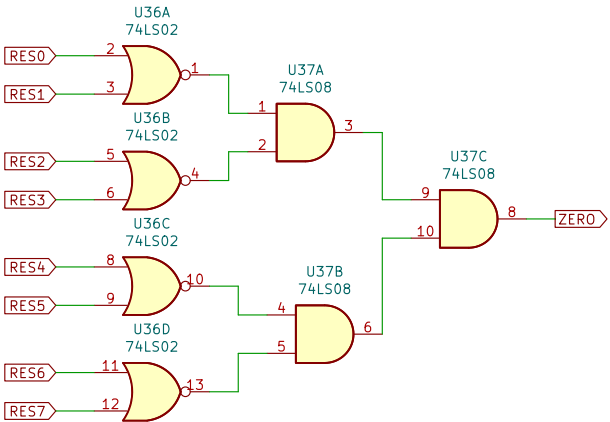
Bus Connection



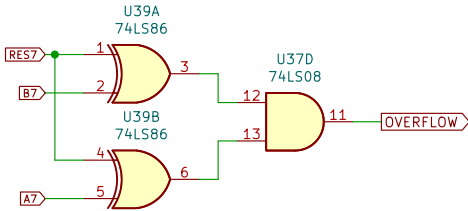
Leds



Zero



OVERFLOW



For storing and asserting current flag statuses from ALU
theWickedWebDev/8-Bit-Computer

Sheet: /
File: Flags Register.kicad_sch

Title: Flags Register

Size: User Date: 2022-01-03
KiCad E.D.A. eeschema (6.0.0-0)

Rev: 3
Id: 1/1