

TRANSFER REGISTER(TX)

This module allows the transfer of data between the 16bit and 8bit busses/registers

SEL	LSB	MSB	
H	L	H	- Latch Bus Data into TX_LSB
H	H	L	- Latch Bus Data into TX_MSB
L	L	H	- Latch Address LSB Data into TX_LSB
L	H	L	- Latch Address MSB Data into TX_MSB
L	L	L	- Latch Address into TX

ASSERT_LSB_MAIN - TX_LSB => Data Bus
ASSERT_MSB_MAIN - TX_MSB => Data Bus
ASSERT_ADDRESS - TX => Address Bus

