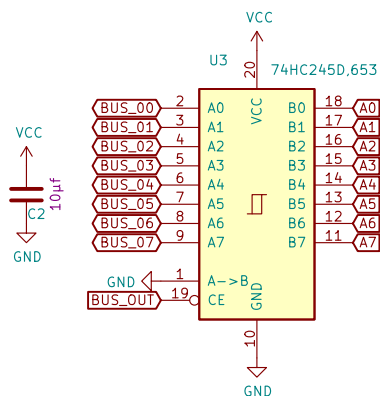


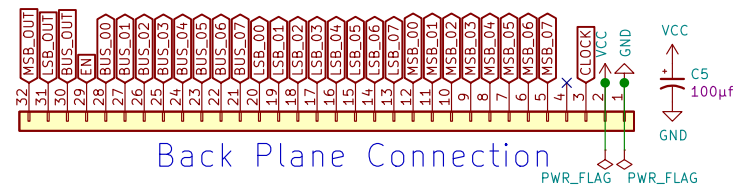
## 8bit Register

This will latch in data from the Data BUS with the rising edge of the clock and a HIGH signal on EN line

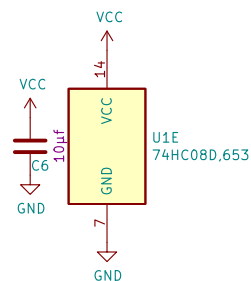


## Non Inverting Buffer

When BUS\_OUT is set LOW, this register will assert its value out onto the DATA BUS

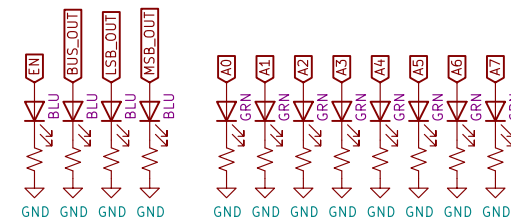


## Back Plane Connection

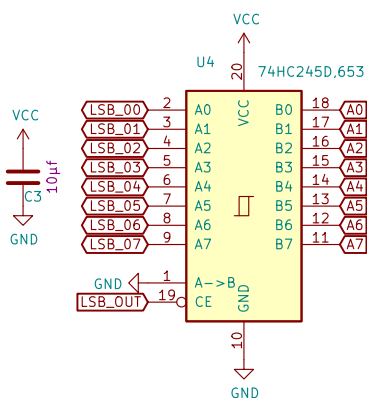


## AND Gate

Used to Enable the Register along with the Clock Pulse

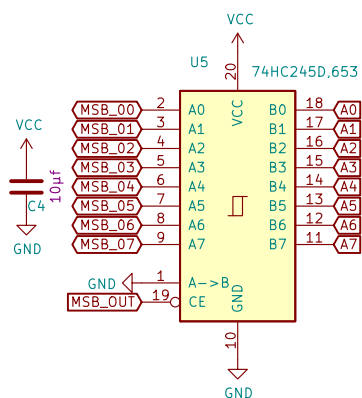


## LED Indicators



## Non Inverting Buffer

When LSB\_OUT is set LOW, this register will assert its value out onto the LSB BUS



## Non Inverting Buffer

When MSB\_OUT is set LOW, this register will assert its value out onto the MSB BUS

Sheet: /  
File: smd-register-array.sch

### Title:

Size: A4  
KiCad E.D.A. kicad (5.1.10-1-10\_14)

Date:

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