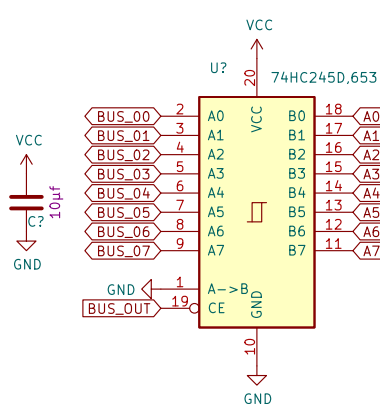


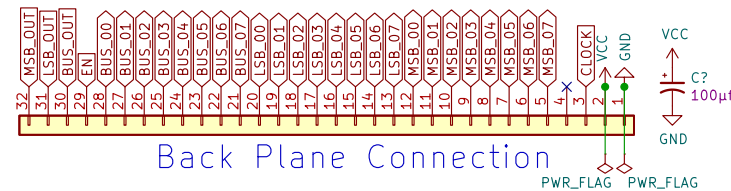
8bit Register

This will latch in data from the Data BUS with the rising edge of the clock and a HIGH signal on EN line

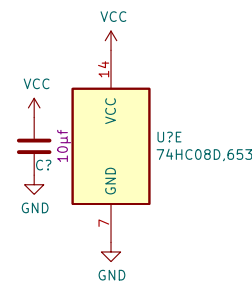


Non Inverting Buffer

When BUS_OUT is set LOW, this register will assert its value out onto the DATA BUS

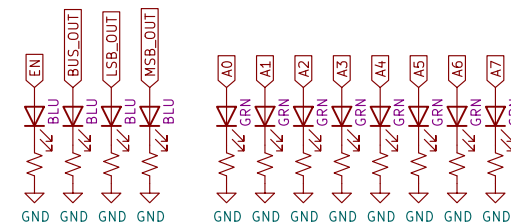


Back Plane Connection

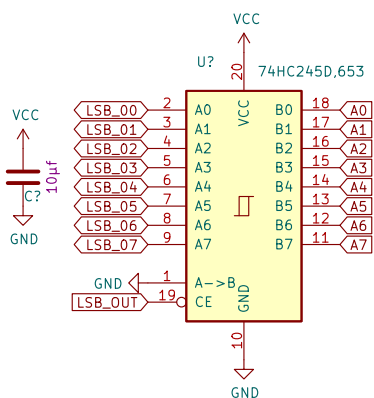


AND Gate

Used to Enable the Register along with the Clock Pulse

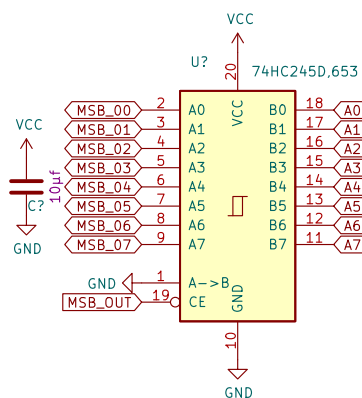


LED Indicators



Non Inverting Buffer

When LSB_OUT is set LOW, this register will assert its value out onto the LSB BUS



Non Inverting Buffer

When MSB_OUT is set LOW, this register will assert its value out onto the MSB BUS

theWickedWebDev/8-bit-computer

Sheet: /

File: smd-register-array.kicad_sch

Title: 8-bit General Purpose Register

Size: A4 Date: 2022-01-18

KiCad E.D.A. kicad (6.0.0-0)

Rev: 2

Id: 1/1