

Memory Module

Provides 992K Of RAM and 32K Of ROM. Address space from \$0:\$7fff is reserved from ROM and \$8000:\$ffff is reserved for RAM.

The code segment (CS) and the data segment(DS) registers should be initialized at the start of your program.

Setting $\overline{DS_IN}$ or $\overline{CS_IN}$ low will latch the value presented on the data bus into the corresponding segment register.

A HIGH signal on the WE line will write to RAM as long as the address provided falls into a valid RAM address.

A LOW signal on the \overline{OE} line will assert the contents at the specified address out onto the data bus.

MODE selects which segment to use, Code or Data. A HIGH signal will retrieve DATA back from memory by using the DS, whereas a LOW signal will return back CODE

