ALU Conditional Jump Logic CPU Connections Logic Gates Power LED Indicators Pull U/D Jumps MUX Unused Logic Gates FINAL OR GATE FOR PC_LOAD control line GND (19 6 JAJNBE 7 JBEJNA 8 JNBJAEJNO 9 JBJNAEJ 10 JNEJNZ 11 JEJZ 13 JNS OR_BE_NA 2 ORJAJNBE 3 OR_NO> ORJUNGE 4 ORJS) ORJGEJNL 5 D CD4072 OR_NS 5 D CD4072 OR G NLE 9 OR_LE_NG 10 OR_NE_NZ)10 OR_B_NAE_C 1 OR_JUMP)1 OR_NB_AE_NC 1 JL / JNGE 0100 0000 Jump if not greater or equal Unconditional Jump JG / JNLE JGE / JNL 0010 0011 Jump if greater or equal Jump if not less SF = OF Jump if greater SF=OF 6 Jump if not less or equal ZF = 0 and SF = OF Jump if less or equal Jump if not greater ZF = 1 or SF <> OF 0101 JA / JNBE Jump if above From Previous Solution (JGE / JNL) Jump if not below or equal CF = 0 and ZF = 0JNB / JAE / JNC Jump if not below 0111 U14A OR JLE JNG SF=OF Jump if above or equal Jump if not carry ZERO_FLAG JNE / JNZ Jump if not equal JE / JZ Jump if equal, 1011 Jump if not zero 0110 Jump if NOT SIGN Jump if below or equal Jump if zero Jump if not above CF = 1 or ZF = 1 JB / JNAE / JC Jump if below Jump if not above or equal jump if Carry CF = 1 1101 Jump if NOT OVERFLOW 1110 Jump if OVERFLOW 1100 Jump if SIGN Trilobyte CPU Sheet: / File: control-unit.kicad_sch Title: Conditional Jump Logic - ALU Size: User Date: 2022-07-19 KiCad E.D.A. kicad (6.0.0-0)