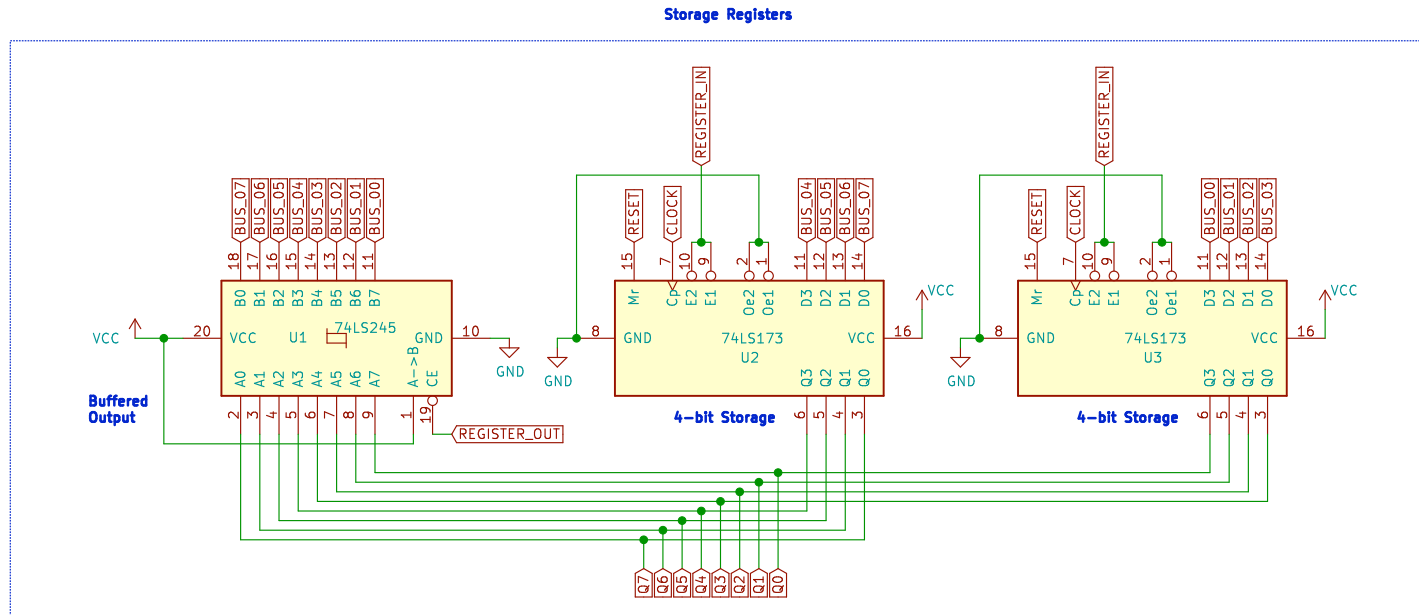
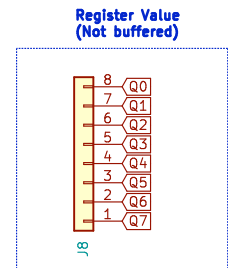
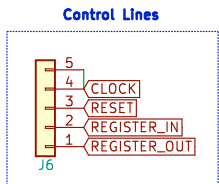
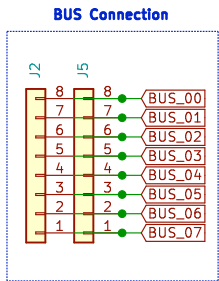


Powered with 5V DC
Either through:
– the screw terminal block, or
– the 2 header pins

–When REGISTER_IN is set LOW, the data loaded onto the BUS lines are latched into the two 74LS173 chips.

– When REGISTER_OUT is set LOW, the data that is currently latched in the registers will be output to the BUS lines.

– RESET, when set HIGH will clear all stored data



theWickedWebDev

Sheet: /
File: REGISTER.sch

Title: 8bit Storage Register Module

Size: A4
KiCad E.D.A. kicad (5.1.9-0-10_14)

Date:
Rev: 2
Id: 1/1